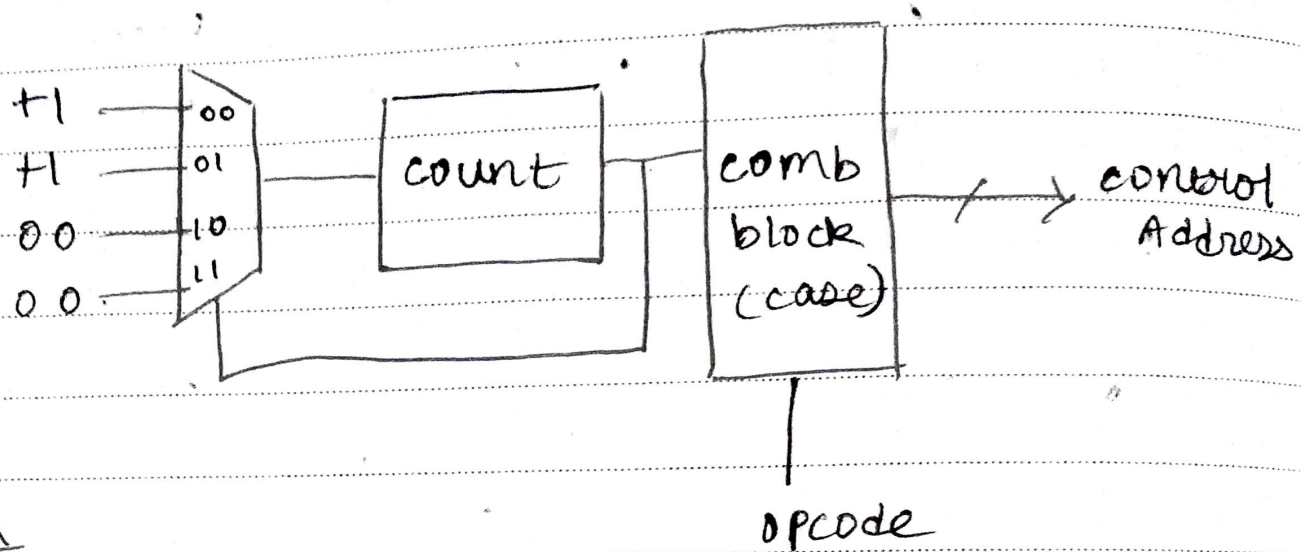
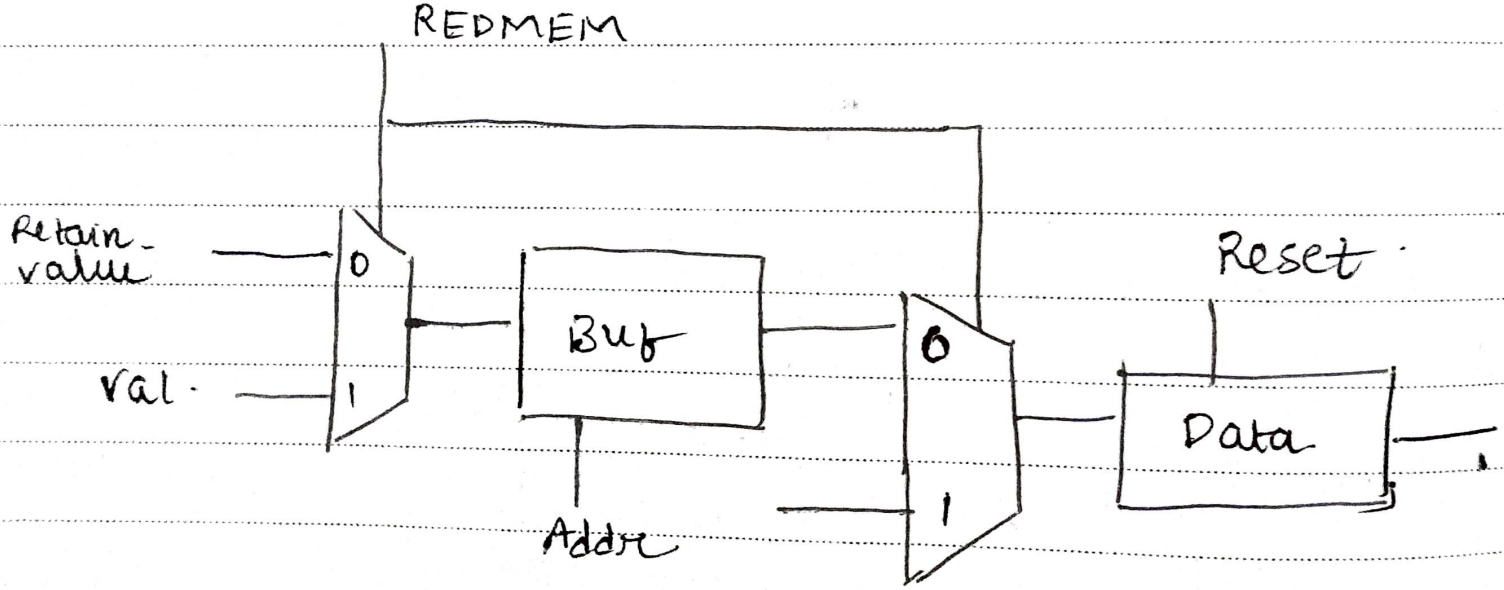
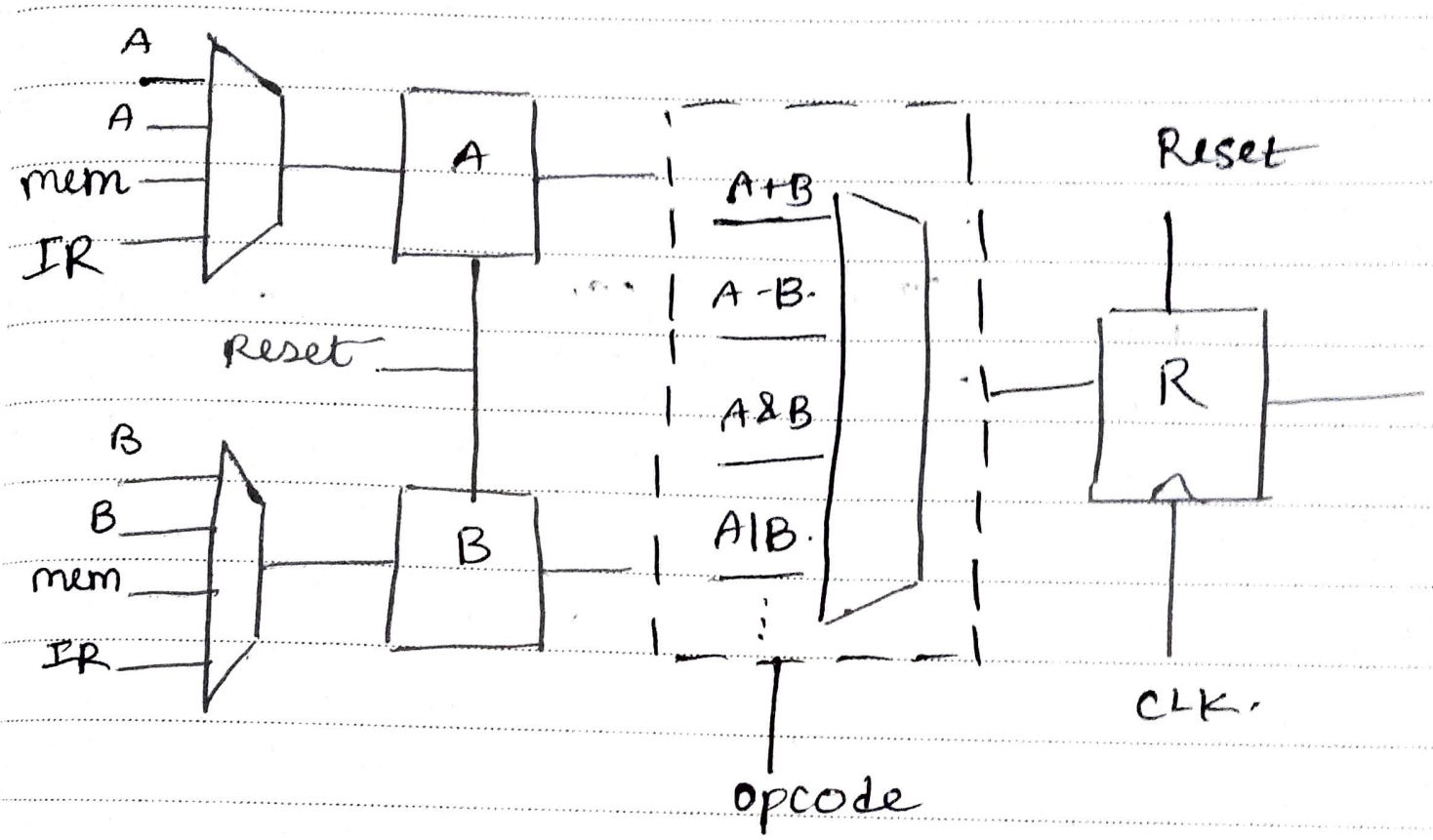
Sequencer



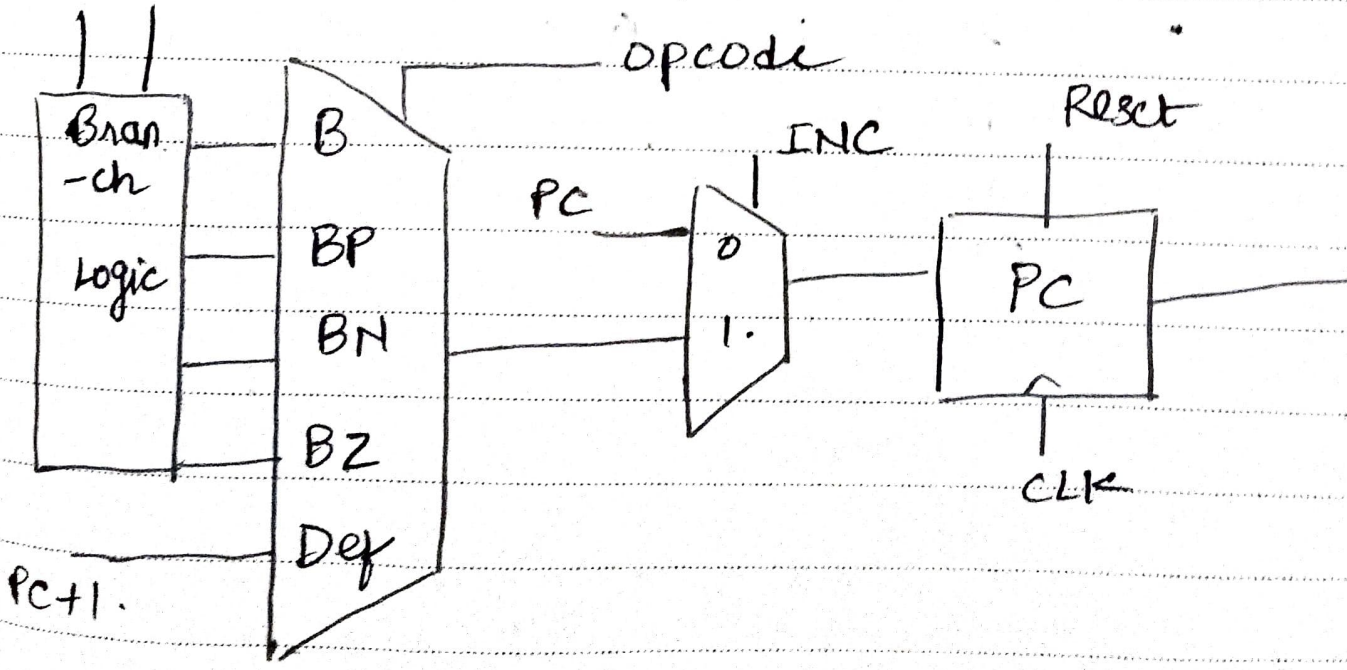
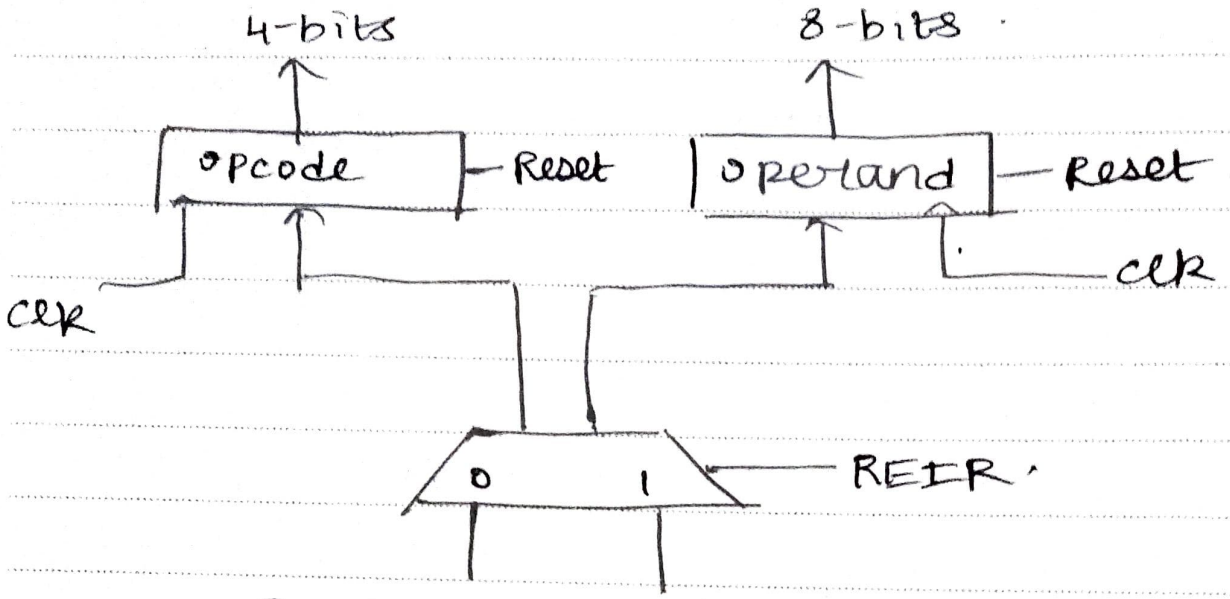
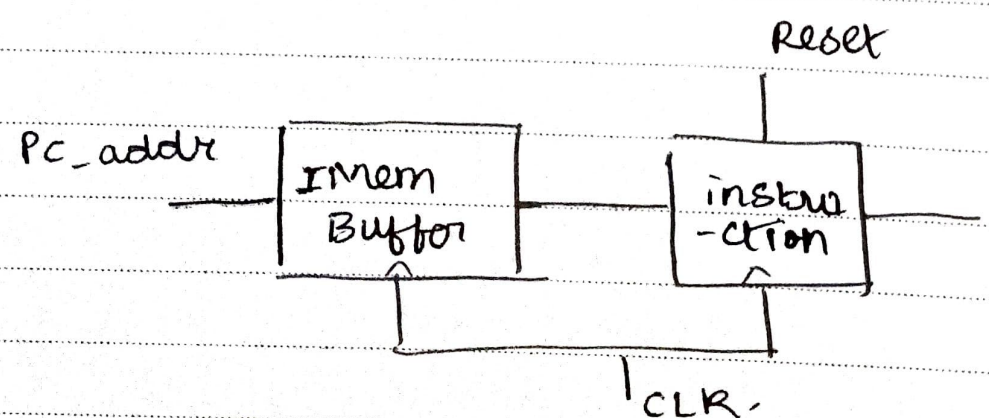
DMEM



ALU Datapath & PC - Branch logic



R ~~Operand for~~ offset (from IR).



Instruction Register & Instruction memory