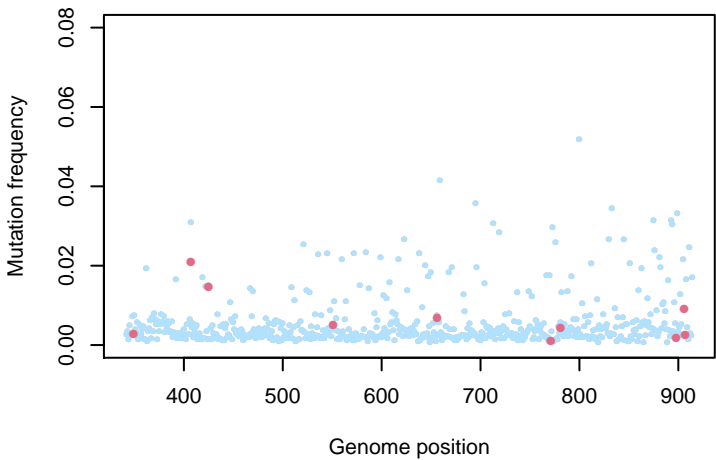
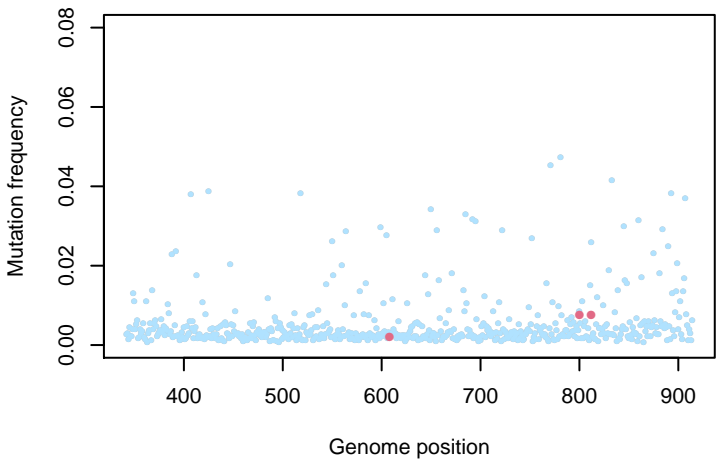


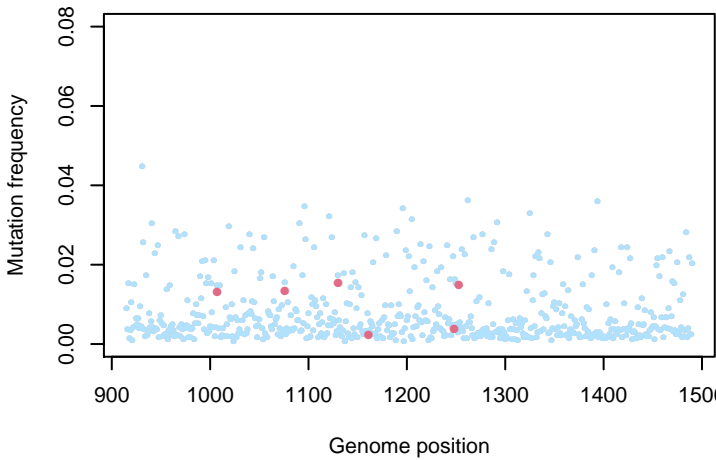
Core 1A



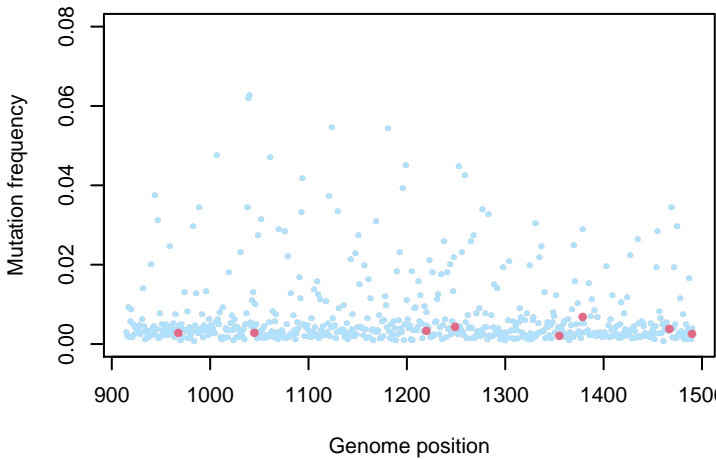
Core 1B



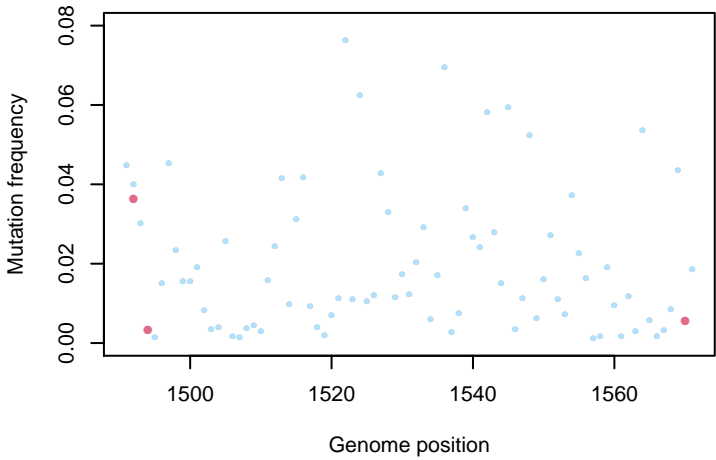
E1 1A



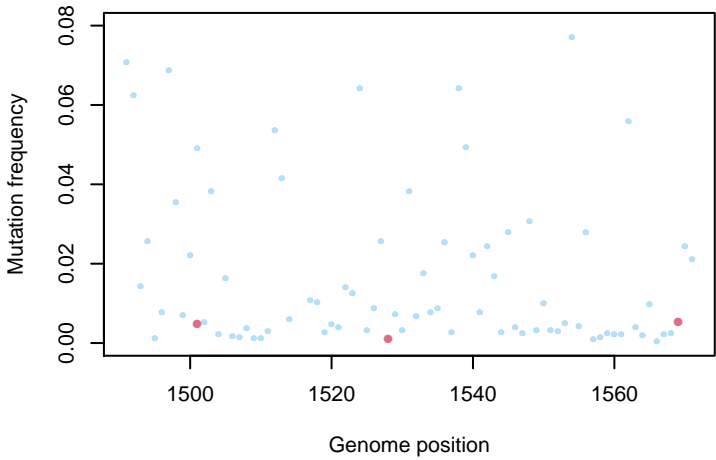
E1 1B



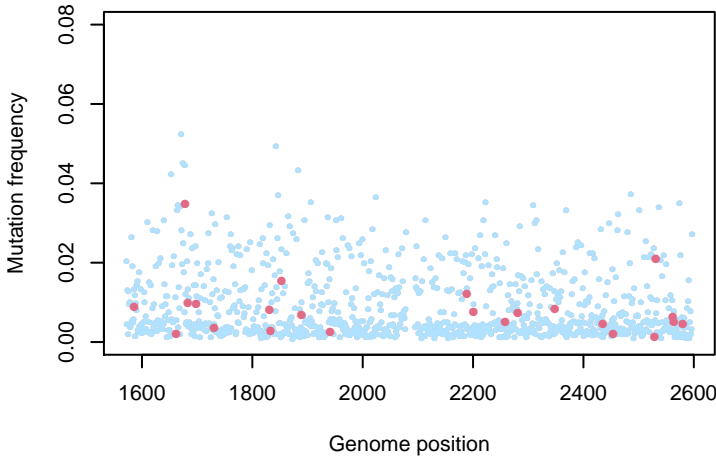
HVR1 1A



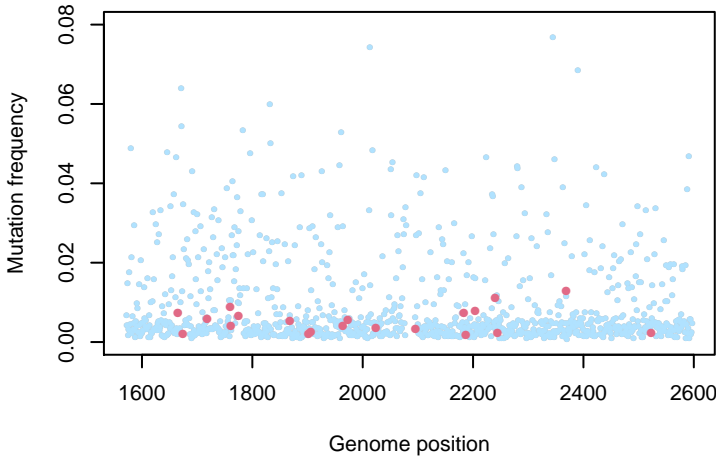
HVR1 1B



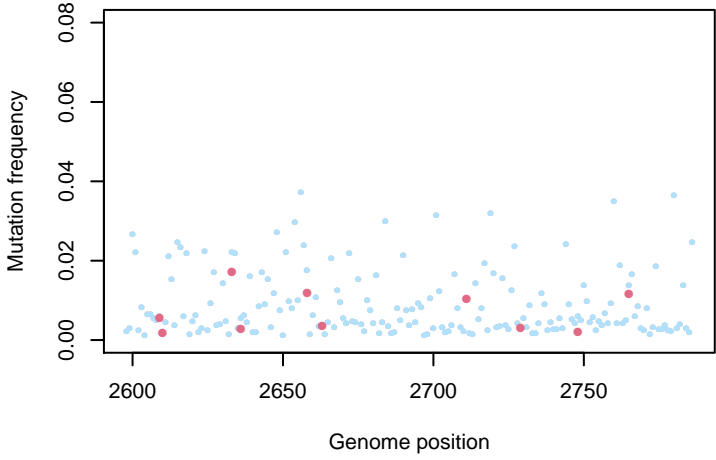
E2 1A



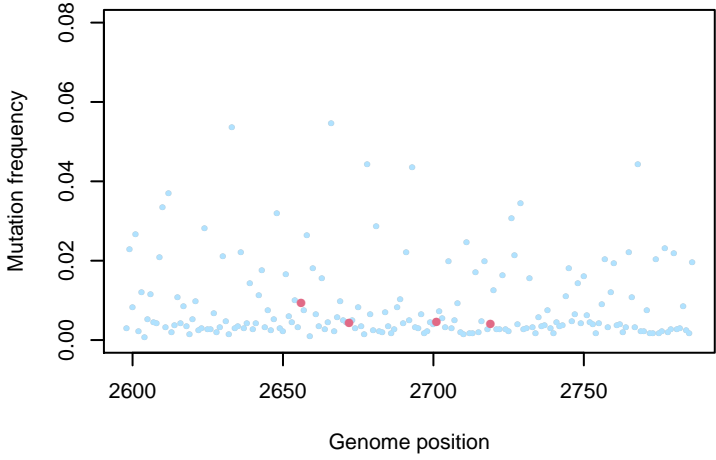
E2 1B



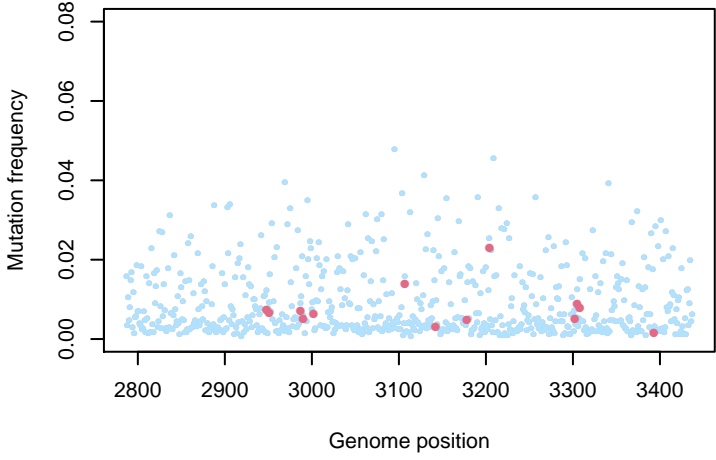
NS1 1A



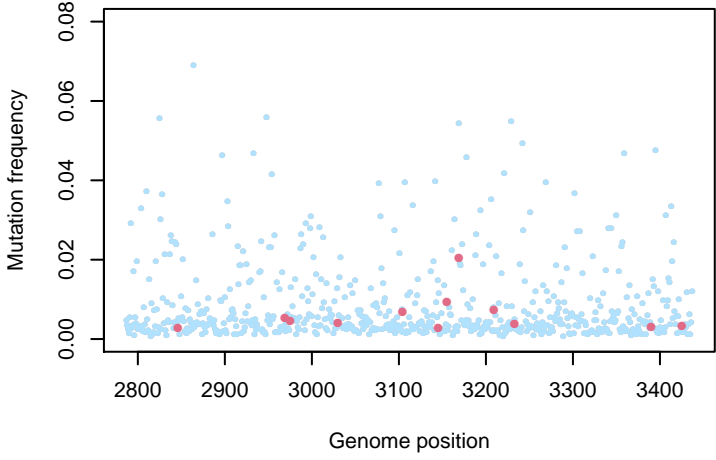
NS1 1B



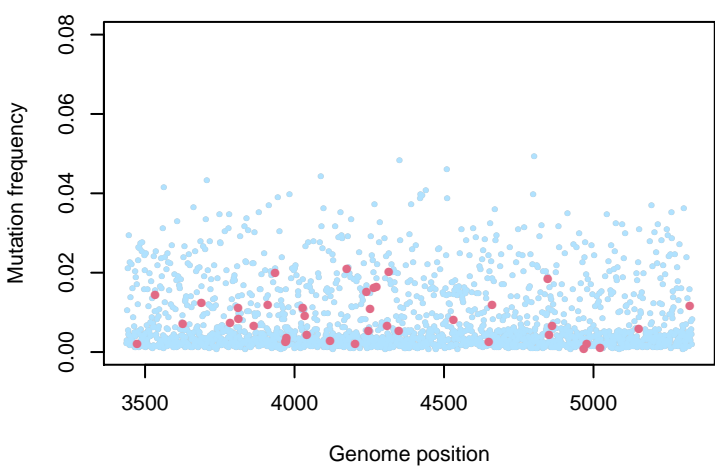
NS2 1A



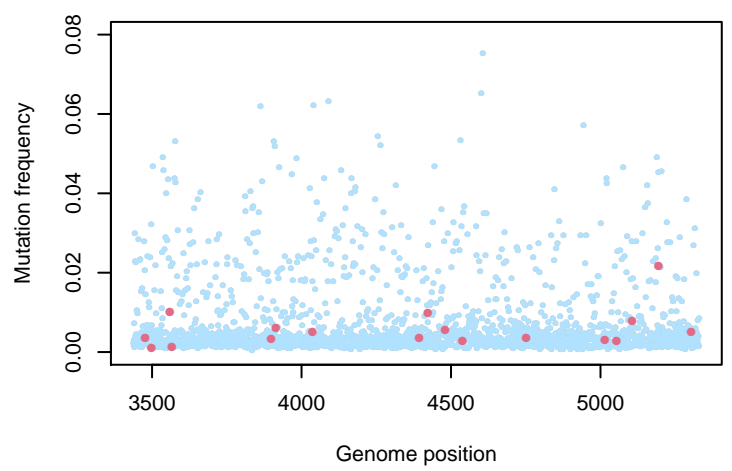
NS2 1B



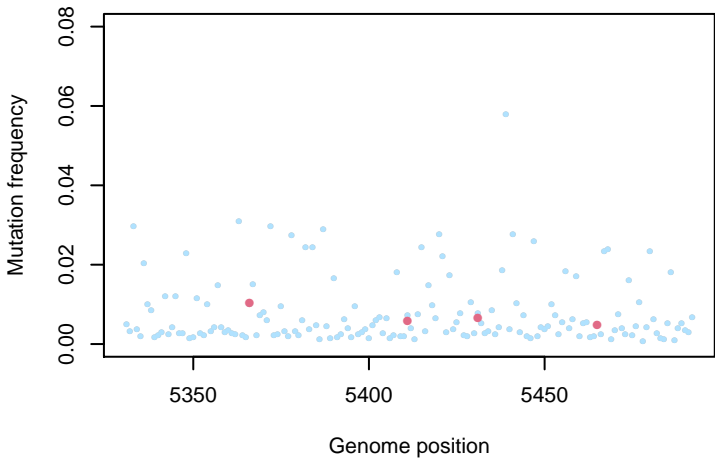
NS3 1A



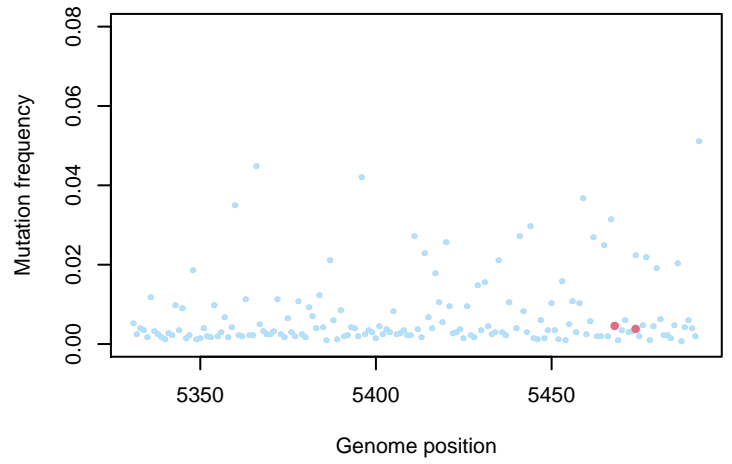
NS3 1B



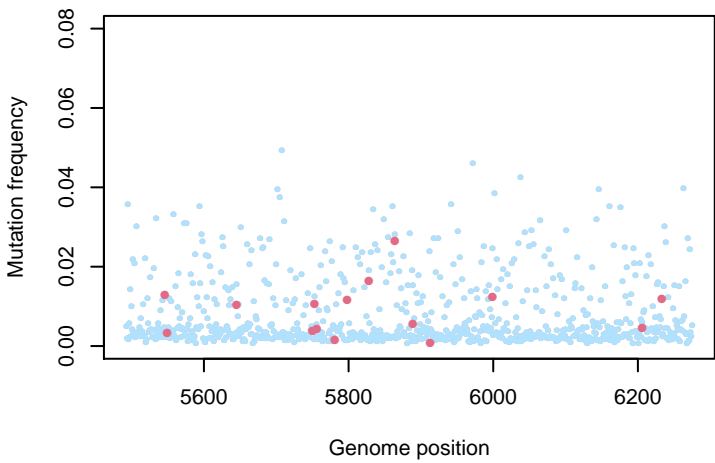
NS4A 1A



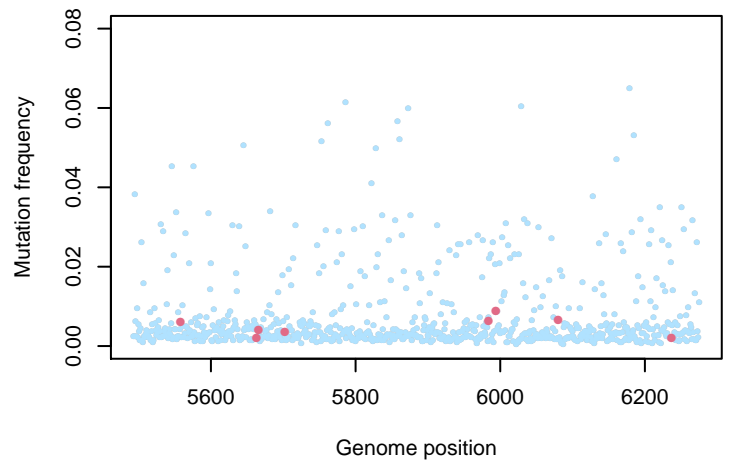
NS4A 1B



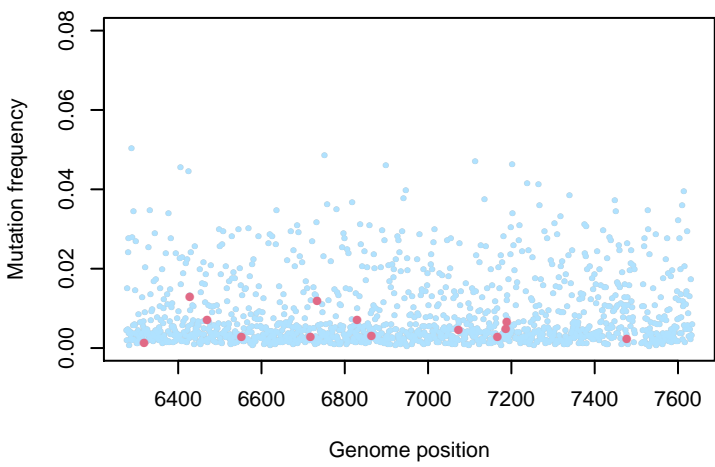
NS4B 1A



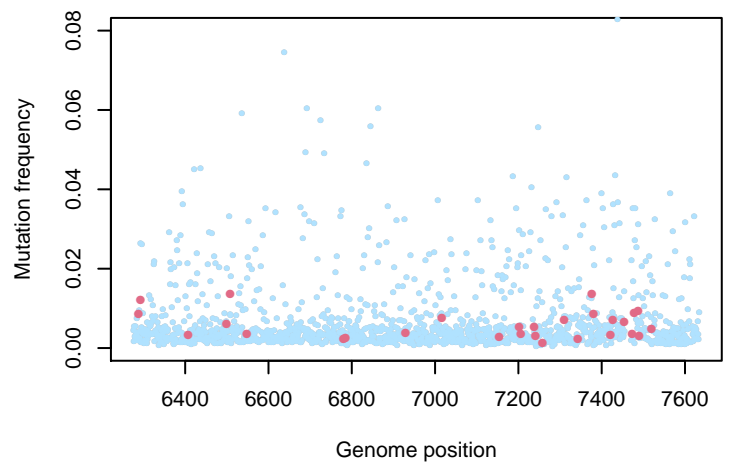
NS4B 1B



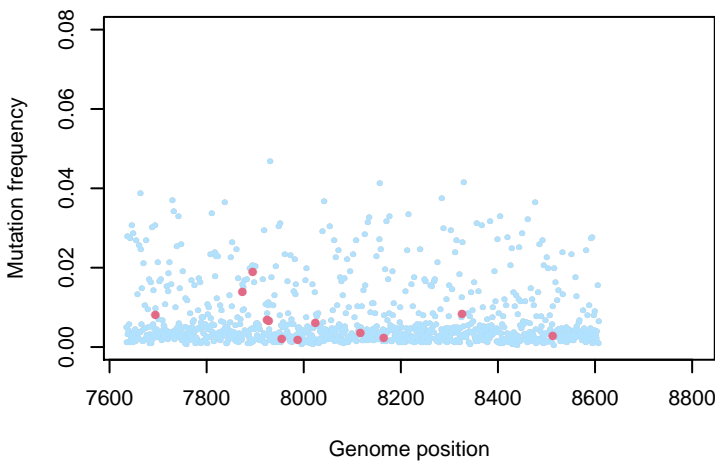
NS5A 1A



NS5A 1B



NS5B 1A



NS5B 1B

