Kai Arsenault

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Education

Wentworth Institute of Technology | Boston, MA — Expected April 2021
Bachelors of Science, Computer Engineering — GPA 3.78
Minor, Computer Science — Dean's List
Member of IEEE-Eta Kappa Nu (IEEE-HKN), the honor society of IEEE

Relevant Coursework
Data structures, Network programming, Database management systems, Analog circuit design
Object oriented programming, Hardware security, Microcontrollers using C, Digital Logic

Related Experience

Defense and Aerospace Software Intern, Teradyne | Boston, MA — January - May 2019
Implemented a C++ loopback test for a fiber-optic based uart protocol device.
Upadated front and back end of .NET applications (WinForms and WPF).
Wrote documentation using markdown with doxygen for .NET applications.
Worked in teams using Azure DevOps and Team Foundation Server (TFS).

Software Engineer Intern, Nasuni | Boston, MA — May - September 2019
Designed, implemented and tested a python tool suite that extracts and builds the lifecycle of filesystem objects on a single on-premise NAS appliance or multiple such geographically-distributed appliances.
Worked in teams using Agile project management through JIRA

Skills

Programming Languages:
Python, C++, Java, C# (WPF and WinForms), C, Bash, LaTeX—— Familiar with Verilog, VHDL
Technical Skills:
Linux (Debian, RedHat), Git and Azure DevOps, .NET Framework, Analog and digital circuit design
Agile project management (JIRA), NuGet package management, Arduino, VMWare, Vim,
Test Instruments:
Oscilloscope, wave function generator, digital multimeter, waveform generator, power supply

Academic Projects

Triple DES Encryptor/Decryptor | Hardware Security | Individual — October 2019
Wrote Python application that can encrypt and decrypt a message using a triple DES algorithm

Microcontroller Communicaiton | Microcontrollers in C | Team of 3 — April 2019
Used C to program a PIC16F87X microcontroller to read in a voltage and then transmit it to another PIC16 using i2c based protocol.
Reading and writing out to microcontroller's registers to enable functionalities such as the timer ISR and to manipulate data using shift registers