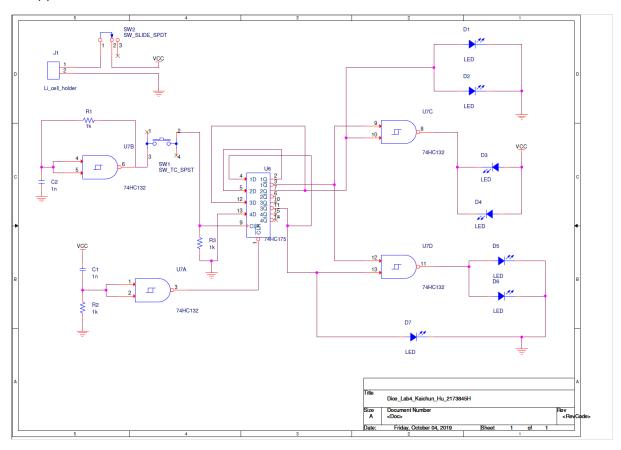
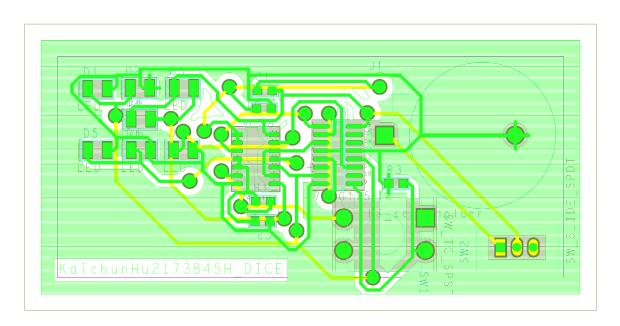
Laboratory 4

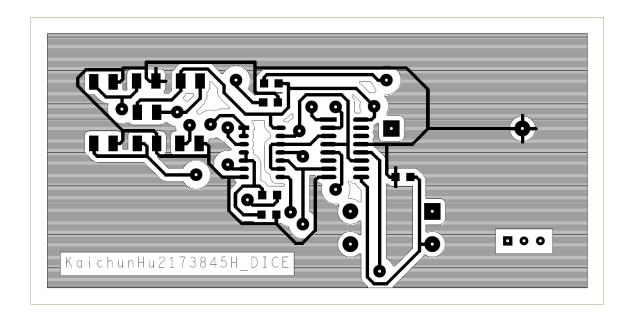
1.Copy of the schematic



2. PCB design



3.Top layer photo mask



4. Bottom layer photo mask

