











Front End

Compiler and

**Code Generation** 

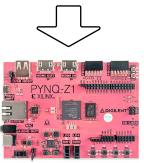
# Graph Optimizer

## Tensor Optimizer

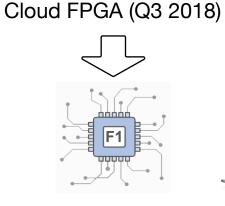
### VTA JIT Runtime

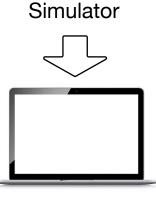
#### **VTA ISA**

### VTA Micro-Architecture



Edge FPGA





Hardware Design

Hardware Deployment