Tensor Expression	Schedule primitives	CPU	GPU	Accel.
V	used in various hardware backends	Schedule	Schedule	Schedule
Select Schedule Primitives	[Halide] Loop Transformations		V	V
Tillitives	[Halide] Thread Binding	V	\checkmark	\checkmark
Final Schedule	[Halide] Compute Locality	V	√	\checkmark
	[TVM] Special Memory Scope		√	√
Code Lowering	[TVM] Tensorization	√	√	√
Low level code	[TVM] Latency Hiding			V