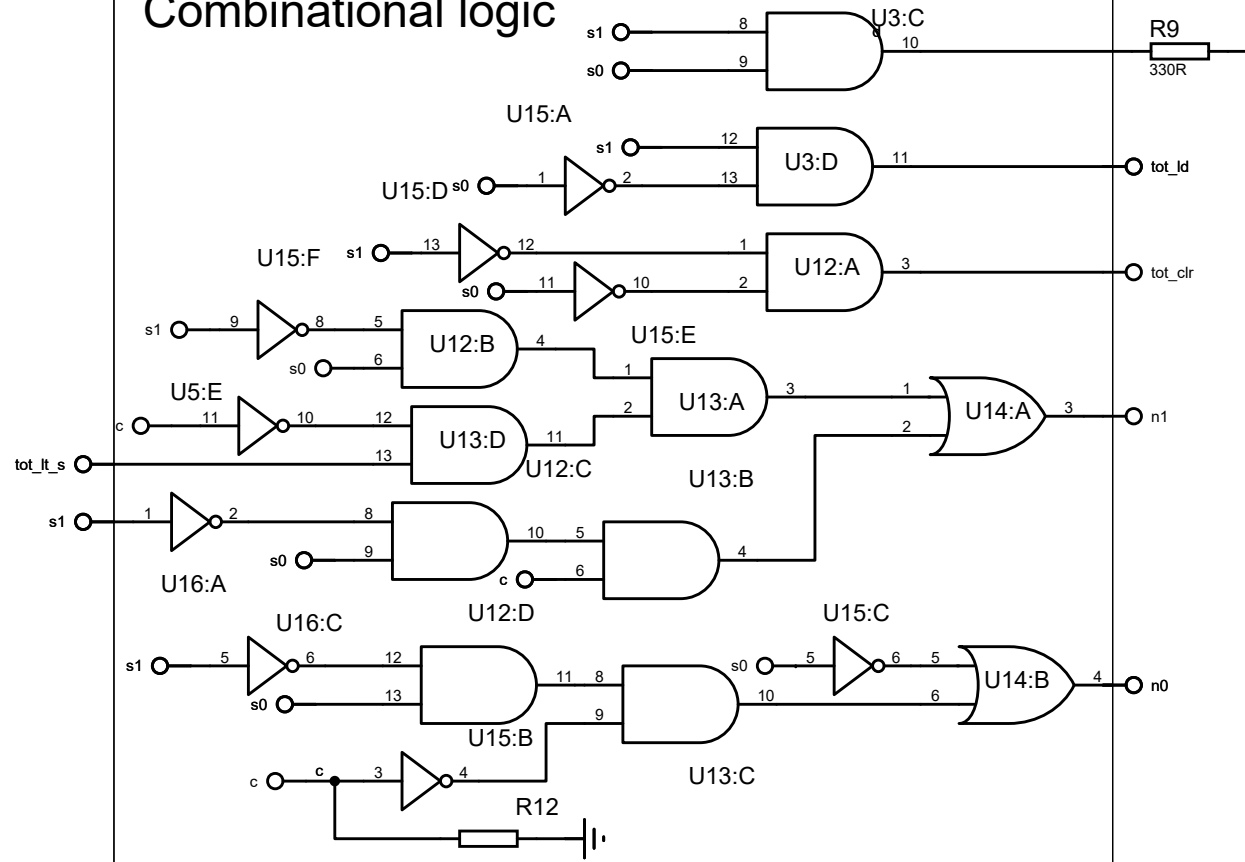
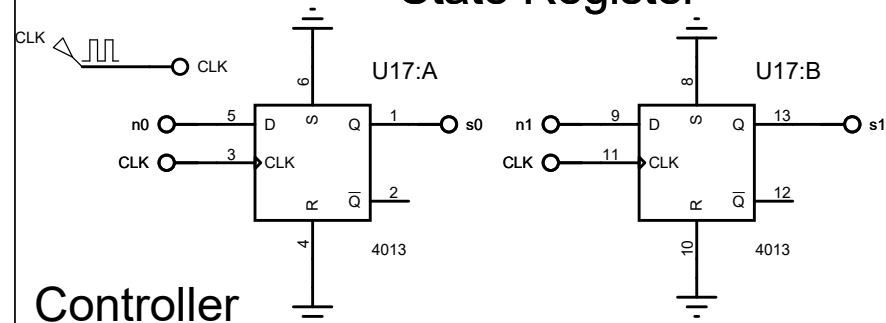


Combinational logic



State Register



Controller

