Search

## [PATCH 2/6] ARM: OMAP2+: Remove board-omap4panda.c

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- Subject: [PATCH 2/6] ARM: OMAP2+: Remove board-omap4panda.c
- From: Tony Lindgren < tony@xxxxxxxxxxxx
- Date: Fri, 17 May 2013 12:17:53 -0700
- *In-reply-to*: <20130517191304.468.73487.stgit@localhost>
- *User-agent*: StGit/0.16-1-ga54b

0

We can now boot with device tree. If you don't want to update u-boot, you can boot with appended DTB with the following instructions:

1. Make sure you have the appended DTB support in .config

```
CONFIG_ARM_APPENDED_DTB=y
CONFIG_ARM_ATAG_DTB_COMPAT=y
CONFIG_ARM_ATAG_DTB_COMPAT_CMDLINE_EXTEND=y
```

- 2. Build the zImage
  - \$ ARCH=arm CROSS\_COMPILE=... make zImage
- 3. Build the device tree blobs
  - \$ ARCH=arm CROSS COMPILE=... make dtbs
- 4. Append the correct panda dtb to zImage

Depending on your hardware it's omap4-panda.dtb, omap4-panda-a4.dtb or omap4-panda-es.dtb.

- \$ cat arch/arm/boot/zImage arch/arm/boot/dts/omap4-panda-es.dtb > /tmp/appended
- 5. Use mkimage to produce the appended device tree uImage
  - \$ mkimage -A arm -O linux -T kernel -C none -a 0x80008000 -e 0x80008000 \
    -n "Linux" -d /tmp/appended /tmp/uImage

```
Signed-off-by: Tony Lindgren <tony@xxxxxxxxxx
```

3 files changed, 464 deletions(-)

delete mode 100644 arch/arm/mach-omap2/board-omap4panda.c

```
diff --git a/arch/arm/mach-omap2/Kconfig b/arch/arm/mach-omap2/Kconfig
index 465edd1..1f941c4 100644
--- a/arch/arm/mach-omap2/Kconfig
+++ b/arch/arm/mach-omap2/Kconfig
@@ -378,14 +378,6 @@ config MACH_TI8148EVM
        depends on SOC TI81XX
        default y
-config MACH_OMAP4_PANDA
        bool "OMAP4 Panda Board"
        default y
        depends on ARCH OMAP4
        select OMAP PACKAGE CBL
        select OMAP_PACKAGE_CBS
        select REGULATOR FIXED VOLTAGE if REGULATOR
 config OMAP3 EMU
        bool "OMAP3 debugging peripherals"
        depends on ARCH OMAP3
diff --git a/arch/arm/mach-omap2/Makefile b/arch/arm/mach-omap2/Makefile
index 875d61d..2cbf3ef 100644
--- a/arch/arm/mach-omap2/Makefile
+++ b/arch/arm/mach-omap2/Makefile
                                                         += board-cm-t35.o
@@ -251,7 +251,6 @@ obj-$(CONFIG_MACH_CM_T35)
 obj-$(CONFIG_MACH_CM_T3517)
                                    += board-cm-t3517.o
 obj-$(CONFIG MACH IGEP0020)
                                        += board-igep0020.o
 obj-$(CONFIG_MACH_TOUCHBOOK)
                                        += board-omap3touchbook.o
-obj-$(CONFIG MACH OMAP4 PANDA)
                                        += board-omap4panda.o
 obj-$(CONFIG_MACH_OMAP3517EVM)
                                        += board-am3517evm.o
diff --git a/arch/arm/mach-omap2/board-omap4panda.c b/arch/arm/mach-omap2/board-omap4panda.c
deleted file mode 100644
index 1e2c75e..0000000
--- a/arch/arm/mach-omap2/board-omap4panda.c
+++ /dev/null
@@ -1,455 +0,0 @@
-/*
- * Board support file for OMAP4430 based PandaBoard.

    * Copyright (C) 2010 Texas Instruments

   Author: David Anders <x0132446@xxxxxx>
 * Based on mach-omap2/board-4430sdp.c
- * Author: Santosh Shilimkar <santosh.shilimkar@xxxxxx>
- * Based on mach-omap2/board-3430sdp.c
- * This program is free software; you can redistribute it and/or modify
- * it under the terms of the GNU General Public License version 2 as
- * published by the Free Software Foundation.
-#include <linux/kernel.h>
-#include <linux/init.h>
-#include <linux/platform device.h>
-#include <linux/clk.h>
-#include <linux/io.h>
```

```
-#include <linux/leds.h>
-#include <linux/gpio.h>
-#include ux/usb/otg.h>
-#include <linux/i2c/twl.h>
-#include ux/mfd/twl6040.h>
-#include linux/regulator/machine.h>
-#include ux/regulator/fixed.h>
-#include ux/ti wilink st.h>
-#include <linux/usb/musb.h>
-#include <linux/usb/phy.h>
-#include usb/nop-usb-xceiv.h>
-#include ux/wl12xx.h>
-#include <linux/irqchip/arm-gic.h>
-#include ux/platform_data/omap-abe-twl6040.h>
-#include <asm/mach-types.h>
-#include <asm/mach/arch.h>
-#include <asm/mach/map.h>
-#include "common.h"
-#include "soc.h"
-#include "mmc.h"
-#include "hsmmc.h"
-#include "control.h"
-#include "mux.h"
-#include "common-board-devices.h"
-#include "dss-common.h"
-#define GPIO HUB POWER
                                1
-#define GPIO_HUB_NRESET
                                        62
-#define GPIO_WIFI PMENA
                                        43
-#define GPIO_WIFI_IRQ
                                53
-/* wl127x BT, FM, GPS connectivity chip */
-static struct ti_st_plat_data wilink_platform_data = {
        .nshutdown_gpio = 46,
        .dev_name
                        = "/dev/tty01",
        .flow cntrl
                        = 1,
                        = 3000000.
        .baud_rate
        .chip enable
                        = NULL,
        .suspend
                        = NULL,
        .resume
                        = NULL,
-static struct platform device wl1271 device = {
        .name
                = "kim",
        .id
                = -1,
        .dev
                = {
                .platform data = &wilink platform data,
        },
-};
-static struct gpio_led gpio_leds[] = {
        {
                                        = "pandaboard::status1",
                .name
                                        = "heartbeat",
                .default trigger
                .gpio
                                        = 7,
        },
                                        = "pandaboard::status2",
                .name
                                        = "mmc0",
                .default trigger
```

```
.gpio
                                         = 8,
        },
-};
-static struct gpio led platform data gpio led info = {
        .leds
                        = gpio leds,
        .num leds
                        = ARRAY SIZE(gpio leds),
-};
-static struct platform_device leds_gpio = {
                = "leds-gpio",
        .name
        .id
                = -1,
        .dev
                = {
                .platform_data = &gpio_led_info,
        },
-};
-static struct omap_abe_twl6040_data panda_abe_audio_data = {
        /* Audio out */
        .has_hs
                        = ABE_TWL6040_LEFT | ABE_TWL6040_RIGHT,
        /* HandsFree through expansion connector */
        .has hf
                        = ABE_TWL6040_LEFT | ABE_TWL6040_RIGHT,
        /* PandaBoard: FM TX, PandaBoardES: can be connected to audio out */
        .has_aux
                        = ABE_TWL6040_LEFT | ABE_TWL6040_RIGHT,
        /* PandaBoard: FM RX, PandaBoardES: audio in */
                        = ABE_TWL6040_LEFT | ABE_TWL6040_RIGHT,
        .has_afm
        /* No jack detection. */
        .jack_detection = 0,
        /* MCLK input is 38.4MHz */
        .mclk_freq
                        = 38400000,
-};
-static struct platform_device panda_abe_audio = {
        .name
                        = "omap-abe-tw16040",
        .id
        .dev = {
                .platform data = &panda abe audio data,
        },
-};
-static struct platform_device panda_hdmi_audio_codec = {
                = "hdmi-audio-codec",
        .name
        .id
                = -1,
-};
-static struct platform_device btwilink_device = {
               = "btwilink",
        .name
        .id
                = -1,
-};
-/* PHY device on HS USB Port 1 i.e. nop_usb_xceiv.1 */
-static struct nop_usb_xceiv_platform_data hsusb1_phy_data = {
        /* FREF CLK3 provides the 19.2 MHz reference clock to the PHY */
        .clk rate = 19200000,
-};
-static struct usbhs_phy_data phy_data[] __initdata = {
        {
                .port = 1,
                .reset_gpio = GPIO_HUB_NRESET,
```

```
.vcc gpio = GPIO HUB POWER,
                 .vcc polarity = 1,
                .platform_data = &hsusb1_phy_data,
        },
-};
-static struct platform_device *panda_devices[] __initdata = {
        &leds_gpio,
        &wl1271_device,
        &panda_abe_audio,
        &panda hdmi audio codec,
        &btwilink_device,
-};
-static struct usbhs_omap_platform_data usbhs_bdata __initdata = {
        .port mode[0] = OMAP EHCI PORT MODE PHY,
-};
-static void __init omap4_ehci_init(void)
-{
        int ret;
        /* FREF_CLK3 provides the 19.2 MHz reference clock to the PHY */
        ret = clk_add_alias("main_clk", "nop_usb_xceiv.1", "auxclk3_ck", NULL);
        if (ret)
                pr_err("Failed to add main_clk alias to auxclk3_ck\n");
        usbhs_init_phys(phy_data, ARRAY_SIZE(phy_data));
        usbhs_init(&usbhs_bdata);
-}
-static struct omap_musb_board_data musb_board_data = {
        .interface type
                                 = MUSB INTERFACE UTMI,
        .mode
                                 = MUSB_OTG,
        .power
                                 = 100,
-};
-static struct omap2 hsmmc info mmc[] = {
        {
                 .mmc
                                 = MMC_CAP_4_BIT_DATA | MMC_CAP_8_BIT_DATA,
                 .caps
                .gpio_wp
                                 = -EINVAL,
                                 = -EINVAL,
                 .gpio cd
        },
                                 = "wl1271",
                 .name
                .mmc
                                 = MMC CAP 4 BIT DATA | MMC CAP POWER OFF CARD,
                 .caps
                                 = -EINVAL,
                .gpio_wp
                                 = -EINVAL,
                .gpio_cd
                .ocr_mask
                                 = MMC_VDD_165_195,
                .nonremovable
                                 = true,
        },
                /* Terminator */
        {}
-};
-static struct regulator_consumer_supply omap4_panda_vmmc5_supply[] = {
        REGULATOR_SUPPLY("vmmc", "omap_hsmmc.4"),
-};
-static struct regulator init data panda vmmc5 = {
```

```
.constraints = {
                .valid ops mask = REGULATOR CHANGE STATUS,
        .num_consumer_supplies = ARRAY_SIZE(omap4_panda_vmmc5_supply),
        .consumer supplies = omap4 panda vmmc5 supply,
-};
-static struct fixed_voltage_config panda_vwlan = {
        .supply_name = "vwl1271",
        .microvolts = 1800000, /* 1.8V */
        .gpio = GPIO WIFI PMENA,
        .startup delay = 70000, /* 70msec */
        .enable high = 1,
        .enabled_at_boot = 0,
        .init_data = &panda_vmmc5,
-};
-static struct platform device omap vwlan device = {
                        = "reg-fixed-voltage",
        .name
        .id
        .dev = {
                .platform data = &panda vwlan,
        },
-};
-static struct wl12xx_platform_data omap_panda_wlan_data __initdata = {
        .board ref clock = WL12XX REFCLOCK 38, /* 38.4 MHz */
-};
-static struct twl6040_codec_data twl6040_codec = {
        /* single-step ramp for headset and handsfree */
        .hs_left_step
                        = 0x0f,
        .hs right step = 0x0f,
        .hf left step
                        = 0x1d
        .hf right step = 0x1d,
-};
-static struct twl6040_platform_data twl6040_data = {
        .codec
                        = &tw16040 codec,
        .audpwron_gpio = 127,
-};
-static struct i2c board info initdata panda i2c 1 boardinfo[] = {
        {
                I2C BOARD INFO("twl6040", 0x4b),
                .irq = 119 + OMAP44XX_IRQ_GIC_START,
                .platform data = &twl6040 data,
        },
-};
-/* Panda board uses the common PMIC configuration */
-static struct twl4030 platform data omap4 panda twldata;
-/*
- * Display monitor features are burnt in their EEPROM as EDID data. The EEPROM
- * is connected as I2C slave device, and can be accessed at address 0x50
-static struct i2c board info initdata panda i2c eeprom[] = {
        {
                I2C BOARD INFO("eeprom", 0x50),
        },
```

```
-};
-static int __init omap4_panda_i2c_init(void)
-{
        omap4 pmic get config(&omap4 panda twldata, TWL COMMON PDATA USB,
                        TWL_COMMON_REGULATOR_VDAC |
                        TWL COMMON REGULATOR VAUX2 |
                        TWL_COMMON_REGULATOR_VAUX3 |
                        TWL COMMON REGULATOR VMMC
                        TWL_COMMON_REGULATOR_VPP |
                        TWL COMMON REGULATOR VANA
                        TWL COMMON REGULATOR VCXIO
                        TWL_COMMON_REGULATOR_VUSB |
                        TWL_COMMON_REGULATOR_CLK32KG |
                        TWL_COMMON_REGULATOR_V1V8 |
                        TWL_COMMON_REGULATOR V2V1);
        omap4_pmic_init("twl6030", &omap4_panda_twldata, panda_i2c_1_boardinfo,
                        ARRAY SIZE(panda i2c 1 boardinfo));
        omap_register_i2c_bus(2, 400, NULL, 0);
        /*
        * Bus 3 is attached to the DVI port where devices like the pico DLP
         * projector don't work reliably with 400kHz
        omap_register_i2c_bus(3, 100, panda_i2c_eeprom,
                                        ARRAY_SIZE(panda_i2c_eeprom));
        omap_register_i2c_bus(4, 400, NULL, 0);
        return 0;
-}
-#ifdef CONFIG OMAP MUX
-static struct omap_board_mux board_mux[] __initdata = {
        /* WLAN IRQ - GPIO 53 */
        OMAP4 MUX(GPMC NCS3, OMAP MUX MODE3 | OMAP PIN INPUT),
        /* WLAN POWER ENABLE - GPIO 43 */
        OMAP4_MUX(GPMC_A19, OMAP_MUX_MODE3 | OMAP_PIN_OUTPUT),
        /* WLAN SDIO: MMC5 CMD */
        OMAP4 MUX(SDMMC5 CMD, OMAP MUX MODE0 | OMAP PIN INPUT PULLUP),
        /* WLAN SDIO: MMC5 CLK */
        OMAP4 MUX(SDMMC5 CLK, OMAP MUX MODE0 | OMAP PIN INPUT PULLUP),
        /* WLAN SDIO: MMC5 DAT[0-3] */
        OMAP4 MUX(SDMMC5 DAT0, OMAP MUX MODE0 |
                                                OMAP PIN INPUT PULLUP),
                                                OMAP_PIN_INPUT_PULLUP),
        OMAP4_MUX(SDMMC5_DAT1, OMAP_MUX_MODE0
        OMAP4 MUX(SDMMC5 DAT2, OMAP MUX MODE0 |
                                                OMAP PIN INPUT PULLUP),
        OMAP4 MUX(SDMMC5 DAT3, OMAP MUX MODE0 | OMAP PIN INPUT PULLUP),
        /* gpio 0 - TFP410 PD */
        OMAP4_MUX(KPD_COL1, OMAP_PIN_OUTPUT | OMAP_MUX_MODE3),
        /* dispc2 data23 */
        OMAP4 MUX(USBB2 ULPITLL STP, OMAP PIN OUTPUT | OMAP MUX MODE5),
        /* dispc2 data22 */
        OMAP4 MUX(USBB2 ULPITLL DIR, OMAP PIN OUTPUT | OMAP MUX MODE5),
        /* dispc2 data21 */
        OMAP4_MUX(USBB2_ULPITLL_NXT, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
        /* dispc2 data20 */
        OMAP4 MUX(USBB2 ULPITLL DATO, OMAP PIN OUTPUT | OMAP MUX MODE5),
        /* dispc2 data19 */
        OMAP4 MUX(USBB2 ULPITLL DAT1, OMAP PIN OUTPUT | OMAP MUX MODE5),
        /* dispc2 data18 */
        OMAP4_MUX(USBB2_ULPITLL_DAT2, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
        /* dispc2 data15 */
        OMAP4 MUX(USBB2 ULPITLL DAT3, OMAP PIN OUTPUT | OMAP MUX MODE5),
        /* dispc2 data14 */
```

```
OMAP4_MUX(USBB2_ULPITLL_DAT4, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data13 */
OMAP4_MUX(USBB2_ULPITLL_DAT5, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2_data12 */
OMAP4 MUX(USBB2 ULPITLL DAT6, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data11 */
OMAP4_MUX(USBB2_ULPITLL_DAT7, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data10 */
OMAP4_MUX(DPM_EMU3, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2_data9 */
OMAP4_MUX(DPM_EMU4, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data16 */
OMAP4_MUX(DPM_EMU5, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data17 */
OMAP4_MUX(DPM_EMU6, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 hsync */
OMAP4_MUX(DPM_EMU7, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 pclk */
OMAP4_MUX(DPM_EMU8, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 vsync */
OMAP4_MUX(DPM_EMU9, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 de */
OMAP4 MUX(DPM_EMU10, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data8 */
OMAP4 MUX(DPM_EMU11, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data7 */
OMAP4 MUX(DPM EMU12, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data6 */
OMAP4 MUX(DPM EMU13, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data5 */
OMAP4_MUX(DPM_EMU14, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data4 */
OMAP4 MUX(DPM EMU15, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data3 */
OMAP4_MUX(DPM_EMU16, OMAP_PIN_OUTPUT | OMAP_MUX_MODE5),
/* dispc2 data2 */
OMAP4 MUX(DPM EMU17, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data1 */
OMAP4 MUX(DPM EMU18, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* dispc2 data0 */
OMAP4 MUX(DPM EMU19, OMAP PIN OUTPUT | OMAP MUX MODE5),
/* NIRQ2 for twl6040 */
OMAP4_MUX(SYS_NIRQ2, OMAP MUX MODE0 |
          OMAP PIN INPUT PULLUP | OMAP PIN OFF WAKEUPENABLE),
/* GPIO_127 for twl6040 */
OMAP4_MUX(HDQ_SIO, OMAP_MUX_MODE3 | OMAP_PIN_OUTPUT),
/* McPDM */
OMAP4 MUX(ABE PDM UL DATA, OMAP MUX MODE0 | OMAP PIN INPUT PULLDOWN),
OMAP4 MUX(ABE PDM DL DATA, OMAP MUX MODE0 | OMAP PIN INPUT PULLDOWN),
OMAP4 MUX(ABE PDM FRAME, OMAP MUX MODE0 | OMAP PIN INPUT PULLUP),
OMAP4 MUX(ABE PDM LB CLK, OMAP MUX MODE0 | OMAP PIN INPUT PULLDOWN),
OMAP4 MUX(ABE CLKS, OMAP MUX MODE0 | OMAP PIN INPUT PULLDOWN),
/* McBSP1 */
OMAP4 MUX(ABE MCBSP1 CLKX, OMAP MUX MODE0 | OMAP PIN INPUT),
OMAP4 MUX(ABE MCBSP1 DR, OMAP MUX MODE0 | OMAP PIN INPUT PULLDOWN),
OMAP4 MUX(ABE MCBSP1_DX, OMAP_MUX_MODE0 | OMAP_PIN_OUTPUT |
          OMAP_PULL_ENA),
OMAP4_MUX(ABE_MCBSP1_FSX, OMAP_MUX_MODE0 | OMAP_PIN_INPUT),
/* UART2 - BT/FM/GPS shared transport */
OMAP4_MUX(UART2_CTS,
                        OMAP_PIN_INPUT | OMAP_MUX_MODE0),
```

```
OMAP4 MUX(UART2 RTS,
                                OMAP PIN OUTPUT | OMAP MUX MODE0),
        OMAP4 MUX(UART2 RX,
                                OMAP PIN INPUT
                                                   OMAP_MUX_MODE0),
        OMAP4_MUX(UART2_TX,
                                OMAP_PIN_OUTPUT | OMAP_MUX_MODE0),
        { .reg_offset = OMAP_MUX_TERMINATOR },
-};
-#else
-#define board mux
                        NULL
-#endif
-static void omap4 panda init rev(void)
-{
        if (cpu_is_omap443x()) {
                /* PandaBoard 4430 */
                /* ASoC audio configuration */
                panda abe audio data.card name = "PandaBoard";
                panda_abe_audio_data.has_hsmic = 1;
        } else {
                /* PandaBoard ES */
                /* ASoC audio configuration */
                panda_abe_audio_data.card_name = "PandaBoardES";
        }
-}
-static void init omap4 panda init(void)
-{
        int package = OMAP PACKAGE CBS;
        int ret;
        if (omap rev() == OMAP4430 REV ES1 0)
                package = OMAP PACKAGE CBL;
        omap4 mux init(board mux, NULL, package);
        omap panda wlan data.irq = gpio to irq(GPIO WIFI IRQ);
        ret = wl12xx set platform data(&omap panda wlan data);
        if (ret)
                pr err("error setting wl12xx data: %d\n", ret);
        omap4 panda init rev();
        omap4_panda_i2c_init();
        platform add devices(panda devices, ARRAY SIZE(panda devices));
        platform device register(&omap vwlan device);
        omap serial init();
        omap_sdrc_init(NULL, NULL);
        omap4 twl6030 hsmmc init(mmc);
        omap4 ehci init();
        usb_bind_phy("musb-hdrc.2.auto", 0, "omap-usb2.3.auto");
        usb musb init(&musb board data);
        omap4 panda display init();
-}
-MACHINE START(OMAP4 PANDA, "OMAP4 Panda board")
        /* Maintainer: David Anders - Texas Instruments Inc */
        .atag_offset
                        = 0 \times 100
                        = smp_ops(omap4_smp_ops),
        .smp
                        = omap_reserve,
        .reserve
                        = omap4 map io,
        .map io
                        = omap4430_init_early,
        .init early
        .init_irq
                        = gic_init_irq,
```

```
- .init_machine = omap4_panda_init,
- .init_late = omap4430_init_late,
- .init_time = omap4_local_timer_init,
- .restart = omap44xx_restart,
-MACHINE_END
```

- -

24/09/2015

- Follow-Ups:
  - Re: [PATCH 2/6] ARM: OMAP2+: Remove board-omap4panda.c
    - From: Ming Lei
- References:
  - [PATCH 0/6] Drop legacy booting for omap4 for v3.11, boot with device tree only
    - From: Tony Lindgren
- Prev by Date: [PATCH 1/6] ARM: OMAP2+: Remove board-4430sdp.c
- Next by Date: [PATCH 3/6] ARM: OMAP2+: Remove legacy mux data for omap4
- Previous by thread: Re: debugging options (was: Re: [PATCH 1/6] ARM: OMAP2+: Remove board-4430sdp.c)
- Next by thread: Re: [PATCH 2/6] ARM: OMAP2+: Remove board-omap4panda.c
- Index(es):
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