

Model Checking

CS511

Foundations of Model-Checking

Linear Temporal Logic

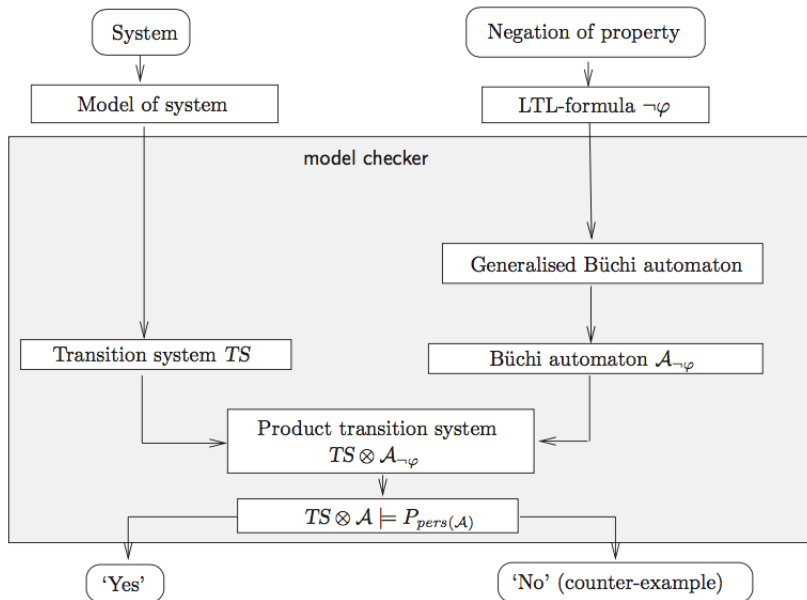
Using LTL Formula to Specify and Verify Properties in Spin

Model-Checking

An introduction to the foundations of Model-Checking

- ▶ We shall use the whiteboard
- ▶ Principles of Model Checking, Christel Baier, Joost-Pieter Katoen and Kim Guldstrand Larsen, The MIT Press, 2008.

Map



Foundations of Model-Checking

Linear Temporal Logic

Using LTL Formula to Specify and Verify Properties in Spin

LTL

On the board

Foundations of Model-Checking

Linear Temporal Logic

Using LTL Formula to Specify and Verify Properties in Spin

States in Spin

- ▶ Spin uses DFS to explore the complete state space of the model
 - ▶ Old states are stored on a stack
- ▶ States are:
 - ▶ Stored in a hash table
 - ▶ Generated on-the-fly
 - ▶ Represented using **state vectors** (compressed)
 - ▶ global variables
 - ▶ contents of the channels
 - ▶ for each process in the system: local variables and process counter

Spin Verification Report

```
1 (Spin Version 6.4.6 -- 2 December 2016)
2   + Partial Order Reduction
3 Full statespace search for:
4   never claim           + (ltl_0)
5   assertion violations  + (if within scope of claim)
6   cycle checks         - (disabled by -DSAFETY)
7   invalid end states   - (disabled by never claim)
8 State-vector 28 (size of a state) byte, depth
9 reached 149 (longest path), --- errors: 0 ---
10  484 (total number of states) states, stored
11  937 states, matched
12  1421 transitions (= stored+matched)
13    0 atomic steps
14 hash conflicts:          0 (resolved)
15 Stats on memory usage (in Megabytes):
16   0.026      equivalent memory usage for states (stored*(State-
17   0.286      actual memory usage for states
18  128.000     memory used for hash table (-w24)
19   0.107      memory used for DFS stack (-m2000)
20  128.302 (memory used) total actual memory usage
```

Modalities in Spin

- ▶ $\Box\phi$ is written `[] ϕ`
- ▶ $\Diamond\phi$ is written `<> ϕ`
- ▶ $\phi_1\mathcal{U}\phi_2$ is written `ϕ_1 U ϕ_2` (ϕ_1 is true until ϕ_2 becomes true)
- ▶ $\neg\phi$ is written `! ϕ`
- ▶ $\phi_1 \wedge \phi_2$ is written `ϕ_1 && ϕ_2`
- ▶ $\phi_1 \vee \phi_2$ is written `ϕ_1 || ϕ_2`

Invariance

- ▶ Properties of the form

$$\Box \phi$$

“ ϕ holds at every state”

- ▶ A particular case of **safety properties**
- ▶ They can also be checked using assertions, like we have already done
- ▶ Example:

`[] !(csp && csq)`

Safety Properties

```
1 bool wantP = false, wantQ = false;
2 bool csp, csq;
3
4 active proctype P() {
5     do :: wantP = true;
6         !wantQ;
7         csp = true; csp = false;
8         wantP = false
9     od
10 }
11
12 active proctype Q() {
13     do :: wantQ = true;
14         !wantP;
15         csq = true; csq = false;
16         wantQ = false
17     od
18 }
19
20 spin -a -f '[]!(csp && csq)' third-safety.pml
21 gcc -DSAFETY -o pan pan.c
22 ./pan
```

No errors reported

Avoiding Ghost Variables

```
1 bool wantP = false, wantQ = false;
2 #define mutex !(P@cs && Q@cs)
3
4 active proctype P() {
5     do :: wantP = true;
6         !wantQ;
7     cs:    wantP = false
8     od
9 }
10
11 active proctype Q() {
12     do :: wantQ = true;
13         !wantP;
14     cs:    wantQ = false
15     od
16 }

```



```
1 spin -a -f '[]mutex' third-safety-no-ghost.pml
2 gcc -DSAFETY -o pan pan.c
3 ./pan
```

Never Claims

- ▶ The name given by Spin to the NBA constructed from ϕ (called $\mathcal{A}_{\neg\psi}$ in the previous slide).
- ▶ Spin transforms a formula in temporal logic into a PROMELA construct called a never claim.
- ▶ Just as a PROMELA program specifies an automaton whose state space is searched by the verifier (its TS), so a never claim specifies an automaton whose state space is searched in parallel with the one that is defined by the PROMELA program.

Never Claims

```
1 $ spin -f '![[]mutex'
2 never {      /* ![[]mutex */
3 T0_init:
4     do
5         :: atomic { (! ((mutex))) -> assert(!(! ((mutex)))) }
6         :: (1) -> goto T0_init
7     od;
8 accept_all:
9     skip
10 }
```

```
1 i$ spin -f '[[]!(csp && csq)'
2 never {      /* [[]!(csp && csq) */
3 accept_init:
4 T0_init:
5     do
6         :: (! ((csp && csq))) -> goto T0_init
7     od;
8 }
```

Liveness Properties

$$\Diamond \phi$$

“ ϕ will eventually hold”

- Called a **liveness property** because it specifies that something “good” eventually happens in the computation.

Liveness Properties

- ▶ They are important
 - ▶ Safety properties are vacuously satisfied by an empty program that does nothing!
- ▶ For example, a solution to the critical section problem in which neither process tries to enter its critical section trivially fulfils the correctness properties of mutual exclusion and absence of deadlock:

```
1 start:
2 do
3   :: printf("Noncritical section\n");
4     goto start;
5     wantP = true; /* Try to enter the critical section */
6     printf("Critical section\n")
7 od
```

Attempt IV with Back Out

```
1 global boolean wantP = false;
2 global boolean wantQ = false;

1 thread P: {
2     while (true) {
3         // non-critical section
4         wantP = true;
5         while wantQ {
6             wantP = false;
7             wantP = true;
8         }
9         // CRITICAL SECTION
10        wantP = false;
11        // non-critical section
12    }
13 }

1 thread Q: {
2     while (true) {
3         // non-critical section
4         wantQ = true;
5         while wantP {
6             wantQ = false;
7             wantQ = true;
8         }
9         // CRITICAL SECTION
10        wantQ = false;
11        // non-critical section
12    }
13 }
```

- ▶ Mutex: Yes
- ▶ Absence deadlock: Yes
- ▶ Free from starvation: No

Attempt IV with Back Out

```
1  bool wantP = false, wantQ = false;
2
3  active proctype P() {
4      do
5          :: wantP = true;
6              do
7                  :: wantQ -> wantP = false; wantP = true
8                  :: else -> break
9              od;
10         wantP = false
11     od
12 }
13
14 active proctype Q() {
15     do
16         :: wantQ = true;
17             do
18                 :: wantP -> wantQ = false; wantQ = true
19                 :: else -> break
20             od;
21         wantQ = false
22     od
23 }
```

Starvation in Attempt IV

```
1 s0 = (5. wantP=1, 18. wantQ=1, 0,0)
2 s1 = (5. wantP=1, 20. wantP,0,1)
3 s2 = (5. wantP=1, 25. wantQ=0, 0,0)
4 s3 = (7. wantQ, 25. wantQ=0,1,1)
5 s4 = (8. wantP=0, 25. wantQ=0,1,1)
6 s5 = (9. wantP=1, 25. wantQ=0,0,1)
7 s6 = (9. wantP=1, 18. wantQ=1,0,0)
8 s7 = (9. wantP=1, 20. wantP,0,1)
9 s8 = (9. wantP=1, 25. wantQ=0,0,1)
10 s9 = (7. wantQ, 25. wantQ=0,1,1)
```

P starves

Verifying Liveness in Spin

```
1 bool wantP = false, wantQ = false;
2 bool csp;
3 ltl p1 {<> csp}
4 active proctype P() {
5     do
6         :: wantP = true;
7         do
8             :: wantQ -> wantP = false; wantP = true
9             :: else -> break
10        od;
11        csp = true; csp = false;
12        wantP = false
13    od
14 }
15 active proctype Q() {
16     do
17         :: wantQ = true;
18         do
19             :: wantP -> wantQ = false; wantQ = true
20             :: else -> break
21        od;
22        wantQ = false
23    od
24 }
```

Starvation

<>csp

- ▶ Expresses absence of starvation for process P.
- ▶ Verification similar to what we did before (i.e. safety properties), except that it must be performed in a mode called searching for *acceptance cycles*
- ▶ *Weak fairness*, explained soon, must also be specified when this program is verified.

Note: Must set jSpin to Acceptance so that it works in Acceptance cycle mode rather than Safety

```

1 pan:1: acceptance cycle (at depth 14)
2 pan: wrote attemptIV.pml.trail
3 (Spin Version 6.4.6 -- 2 December 2016)
4 Warning: Search not completed
5         + Partial Order Reduction
6 Full statespace search for:
7         never claim                + (p1)
8         assertion violations        + (if within scope of claim)
9         acceptance cycles           + (fairness enabled)
10        invalid end states          - (disabled by never claim)
11 State-vector 36 byte, depth reached 51, --- errors: 1 ---
12        26 states, stored (52 visited)
13        18 states, matched
14        70 transitions (= visited+matched)
15        0 atomic steps
16 hash conflicts:                    0 (resolved)
17 Stats on memory usage (in Megabytes):
18        0.002      equivalent memory usage for states (stored*(State-
19        0.289      actual memory usage for states
20        128.000    memory used for hash table (-w24)
21        0.107      memory used for DFS stack (-m2000)
22        128.302    total actual memory usage
23 pan: elapsed time 0 seconds

```

Command Line Alternative

Two options:

- ▶ Do not include the line `ltl p1 <> csp` and run:

```
1 spin -a -f '!<>csp' fourth-liveness.pml
2 gcc -o pan pan.c
3 pan -a -f
```

- ▶ Note absence of `-DSAFETY` option

- ▶ Include the line `ltl p1 <> csp` and run:

```
1 spin -a fourth-liveness.pml
2 gcc -o pan pan.c
3 pan -a -f
```


Verifying Liveness in Spin

That liveness does not hold is indicated by the third line below:

```
1 warning: for p.o. reduction to be valid the never claim must be st
2 (never claims generated from LTL formulae are stutter-invariant)
3 pan:1: acceptance cycle (at depth 14)
4 pan: wrote fourth-liveness-2.pml.trail
5
6 (Spin Version 6.4.5 -- 1 January 2016)
7 Warning: Search not completed
8         + Partial Order Reduction
9
10 Full statespace search for:
11         never claim                + (never_0)
12         assertion violations        + (if within scope of claim)
13         acceptance   cycles        + (fairness enabled)
14         invalid end states         - (disabled by never claim)
15
16 State-vector 36 byte, depth reached 51, errors: 1
17 ...
```

Executing the Offending Trail

- Use “Guided” execution in jSpin

```
1 ltl p1: <> (csp)
2 starting claim 2
3 using statement merging
4 Never claim moves to line 4      [(! (csp))]
5 1 Q:1    1)  wantQ = 1
6 Process Statement                wantQ
7 1 Q:1    1)  else                1
8 1 Q:1    1)  wantQ = 0            1
9 0 P:1    1)  wantP = 1            0
10 Process Statement               wantP      wantQ
11 1 Q:1    1)  wantQ = 1            1          0
12 1 Q:1    1)  wantP                1          1
13 0 P:1    1)  wantQ                1          1
14 <<<<<START OF CYCLE>>>>>
15 ...
```

- START OF CYCLE indicates that the subsequent states form a cycle that can be repeated indefinitely
- Absence of column for `csp` means `csp` has never been assigned to and hence that starvation occurs in this computation

Revisiting Attempt IV

```
1  bool wantP = false, wantQ = false;
2
3  active proctype P() {
4      do
5          :: wantP = true;
6              do
7                  :: wantQ -> wantP = false; wantP = true
8                  :: else -> break
9              od;
10         wantP = false
11     od
12 }
13
14 active proctype Q() {
15     do
16         :: wantQ = true;
17             do
18                 :: wantP -> wantQ = false; wantQ = true
19                 :: else -> break
20             od;
21     wantQ = false
22 od
23 }
```

Fairness

Is the following computation a counterexample for the property of absence of starvation?

```
1 s0 = (5. wantP=1, 18. wantQ=1, 0, 0)
2 s1 = (5. wantP=1, 20. wantP, 0, 1)
3 s2 = (5. wantP=1, 25. wantQ=0, 0, 1)
4 s3 = (5. wantP=1, 18. wantQ=1, 0, 0)
```

- ▶ It is a counterexample to a claim that $\langle \rangle_{\text{csp}}$ is true
- ▶ But is unsatisfactory because it doesn't give process P a "fair" chance to try to enter its critical section.
 - ▶ A computation is **weakly fair** if and only if the following condition holds: if a statement is always executable, then it is eventually executed as part of the computation.
- ▶ The computation described above is not weakly fair: Although like all assignment statements, 5. wantP = **true** is always executable, it is never executed in the computation.

Counterexample produced without Weak Fairness

```
1 ltl p1: <> (csp)
2 starting claim 2
3 using statement merging
4 <<<<START OF CYCLE>>>>
5 Never claim moves to line 4      [(! (csp))]
6 1 Q:1    1)  wantQ = 1
7 Process Statement                wantQ
8 1 Q:1    1)  else                1
9 1 Q:1    1)  wantQ = 0            1
10 spin: trail ends after 6 steps
11 #processes: 2
12   6:      proc  1 (Q:1) attemptIV.pml:16 (state 11)
13   6:      proc  0 (P:1) attemptIV.pml:5 (state 13)
14 MSC: ~G line 3
15   6:      proc  - (p1:1) _spin_nvr.tmp:3 (state 3)
16 2 processes created
17 Exit-Status 0
```

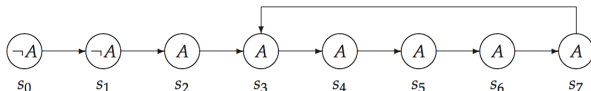
More on Fairness

```
1 byte n = 0;
2 bool flag = false;
3
4 active proctype p() {
5     do
6         :: flag -> break;
7         :: else -> n = 1 - n;
8     od
9 }
10
11 active proctype q() {
12     flag = true
13 }
```

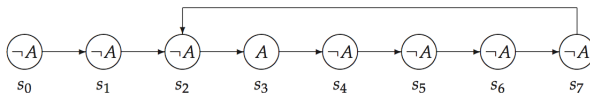
- ▶ The assignment in Q is always enabled, so in a weakly fair computation it will eventually be executed
- ▶ This causes the loop in process P to terminate.
- ▶ Thus the correctness property “the program always terminates” holds if and only if computations are required to be weakly fair.

More LTL Formulae

- ▶ $\Diamond\Box\phi$ expresses a **latching property**: ϕ may not be true initially in a trace, but eventually it becomes true and remains true:



- ▶ $\Box\Diamond\phi$ expresses the property that ϕ is true **infinitely often**: ϕ need not always be true, but at any state in the trace s , ϕ will be true in s or in some state that comes after s :



Example

```
1 active proctype P() {
2   do
3     :: /* Try to enter critical section */
4       csp = true;
5       csp = false;
6       /* Leave critical section */
7   od
8 }
```

- ▶ If an algorithm is free from starvation, we can verify the program for the temporal formula $\Box \langle \rangle_{csp}$.
- ▶ This states that a process can enter its critical section repeatedly.