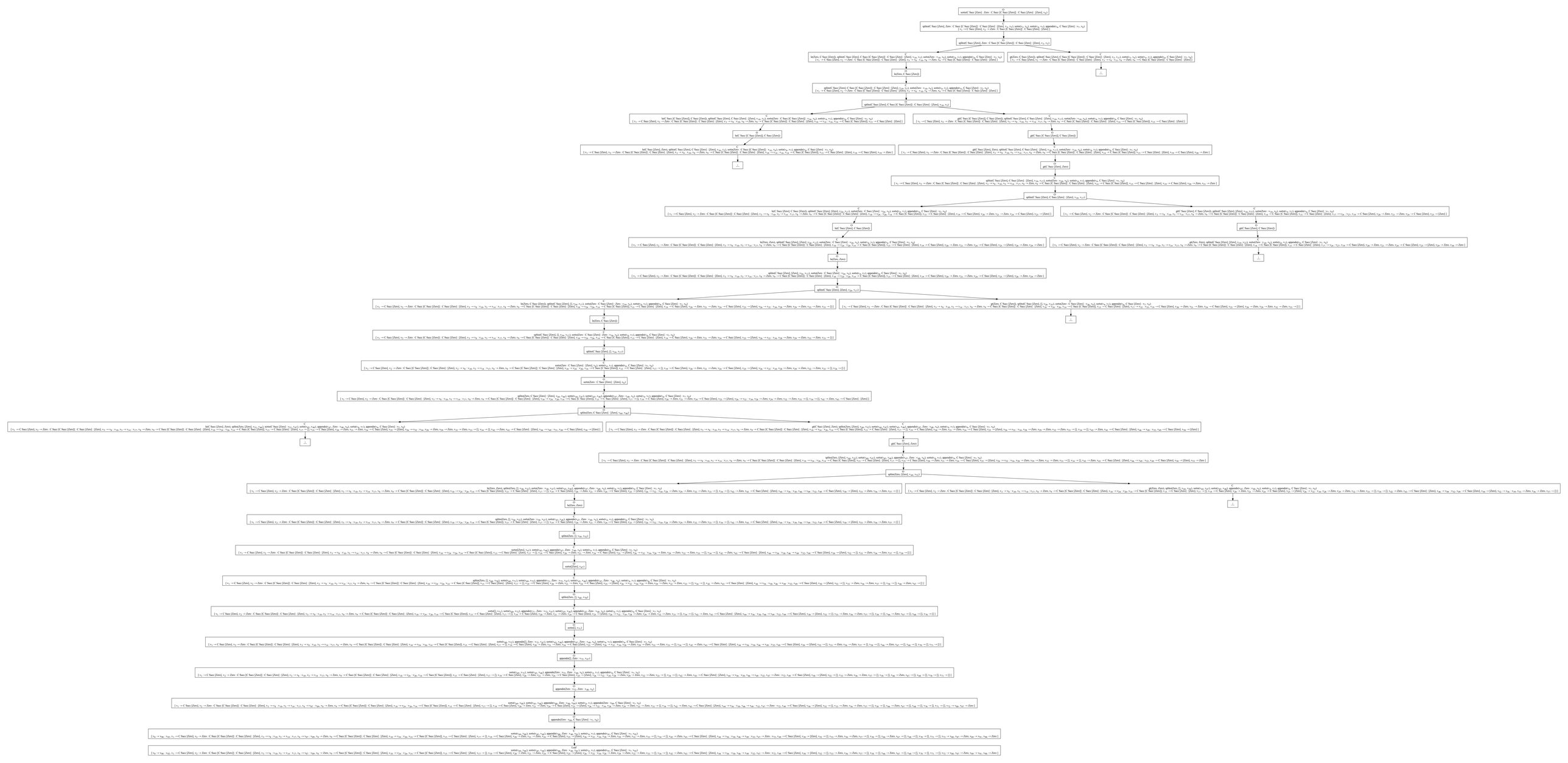
U
setsnd: Succ [Zero] . Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v2}$
U
splsnd: Succ [Zero], Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ setsnd: $_{v1}$ $_{v2}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$]
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splsnd: Succ [Zero], Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$]
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lsZero, C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$]
gtZero, C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$]
⊥
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lsZero, C Succ [Zero]
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splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$]
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[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$]
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lsC: Succ [C Succ [Zero]], C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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gtC: Succ [C Succ [Zero]], C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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lsC: Succ [C Succ [Zero]], C Succ [Zero]
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lsC: Succ [C Succ [Zero]], C Succ [Zero]
C
lsC: Succ [Zero], Zero, splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
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gtC: Succ [C Succ [Zero]], C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
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gtC: Succ [C Succ [Zero]], C Succ [Zero]
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splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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lsC: Succ [Zero], Zero, splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
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gtC: Succ [Zero], C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
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splsnd: Succ [Zero], C Succ [Zero] : [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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splsnd: Succ [Zero], C Succ [Zero] : [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$]
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lsC: Succ [Zero], Zero, splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
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gtC: Succ [Zero], C Succ [Zero], splsnd: Succ [Zero], C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ setsnd: $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$ appendsnd: $_{v1}$ C Succ [Zero] : $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$
[$_{v1}$ → C Succ [Zero], $_{v2}$ → Zero : C Succ [C Succ [Zero]] : C Succ [Zero] . [Zero] $_{v1}$ $_{v2}$ $_{v3}$ $_{v4}$ $_{v5}$]
⊥