

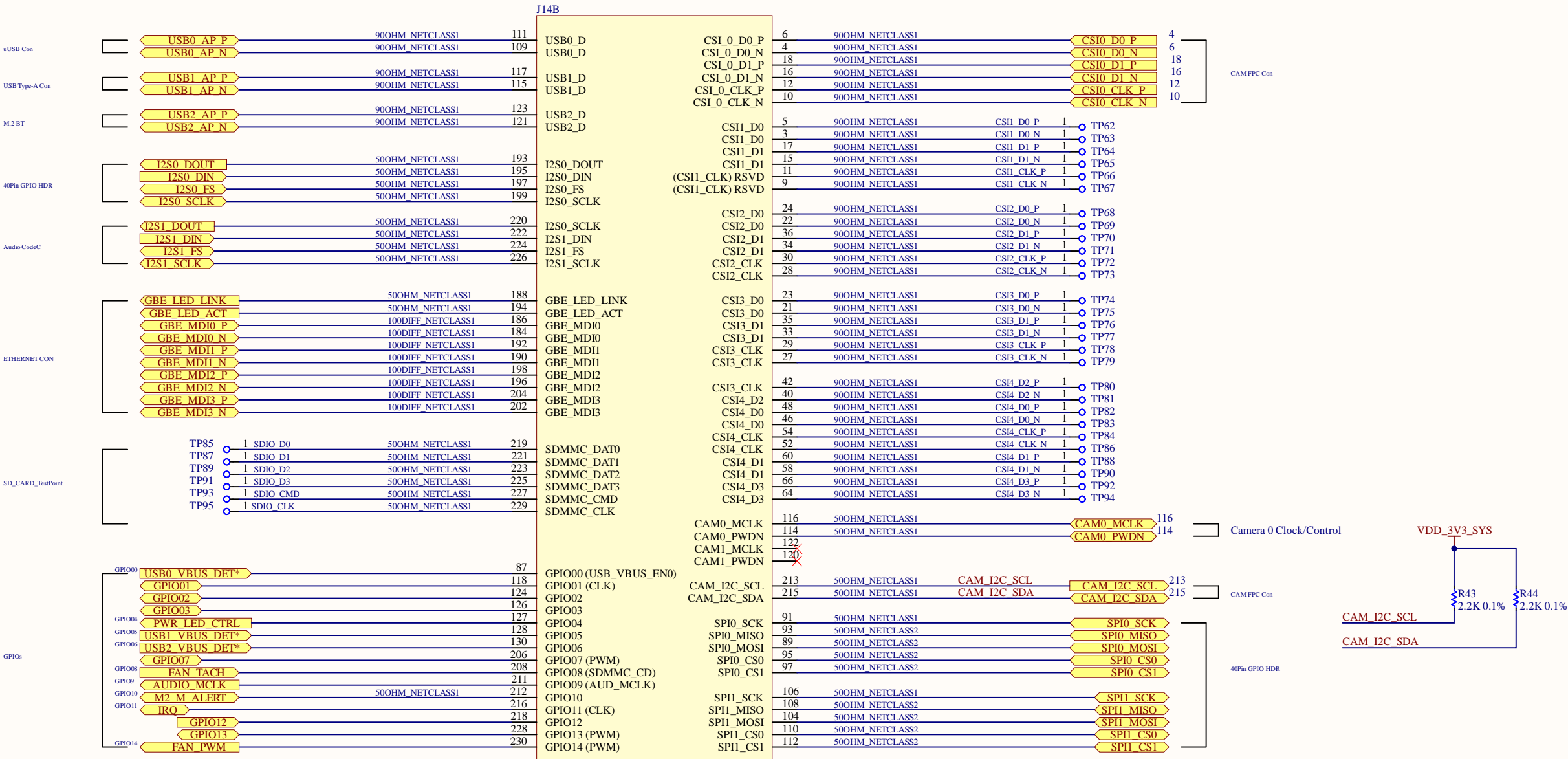
# NVIDIA Jetson Orin Nano



Title		
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A3		
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File:	C:\Users\...\Jetson_Orin_Nano.SchDoc	Drawn By:

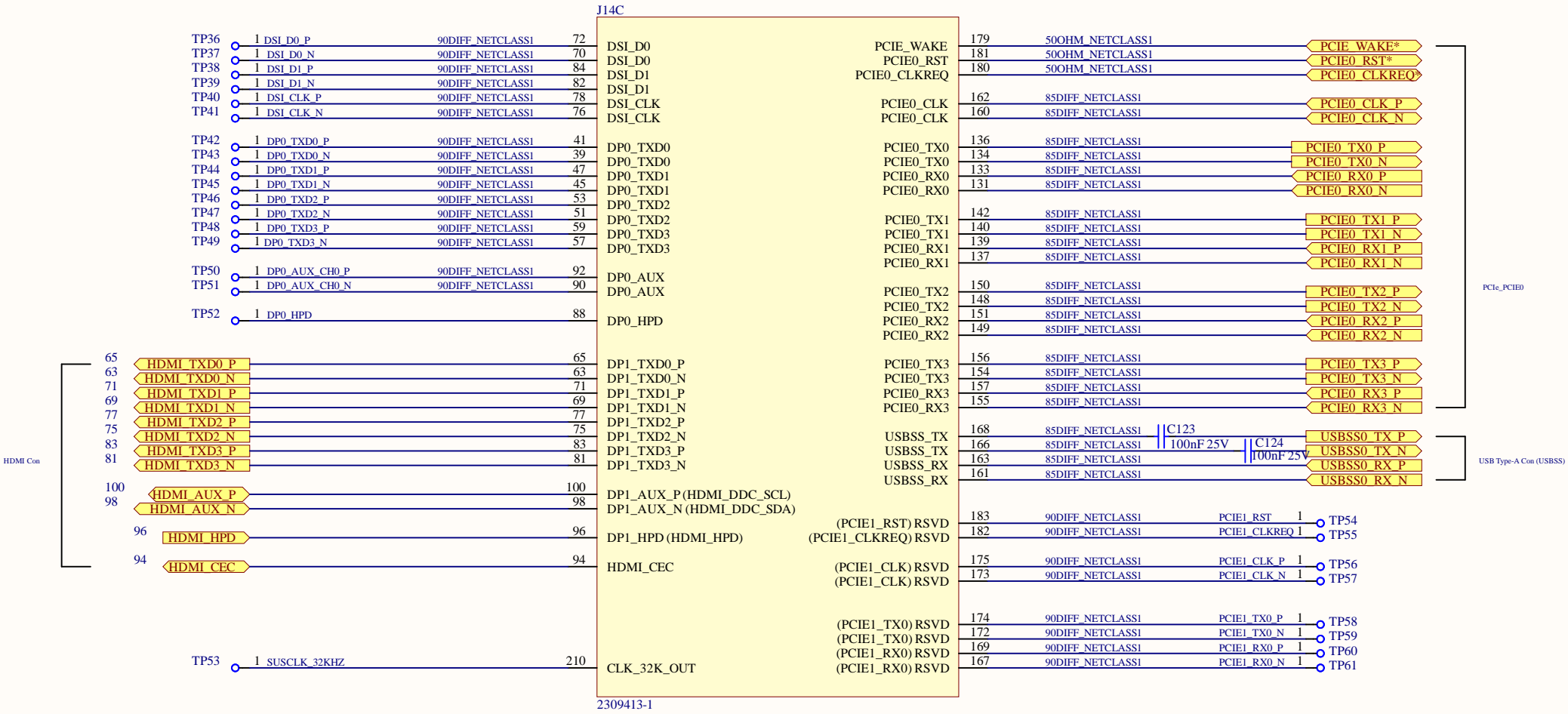


GPIOs, USB2.0, I2S, SPI, GBe and CSI



Title		
Size	Number	Revision
A3		
Date:	2/27/2023	Sheet of
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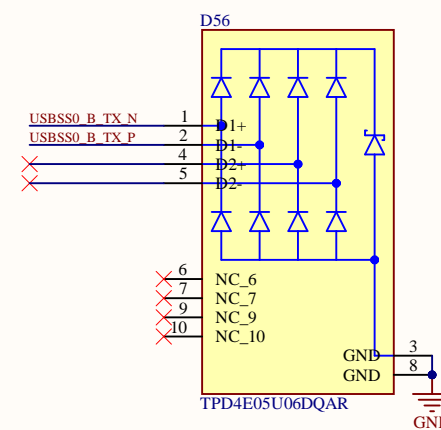
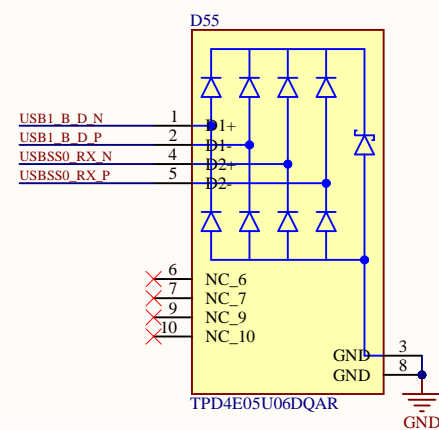
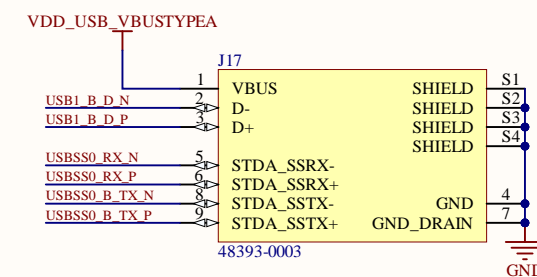
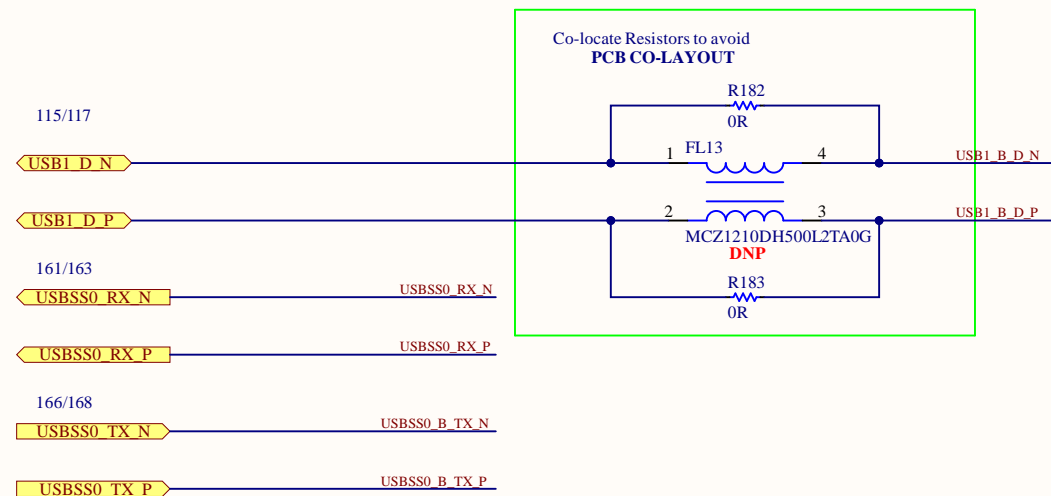
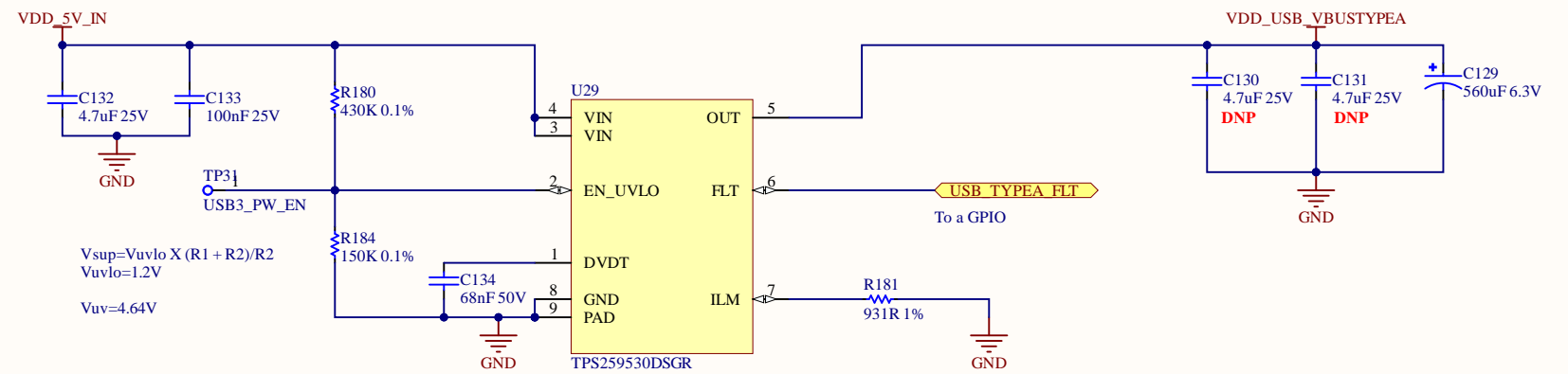
HDMI, DP, DSI, USBSS and PCIe



Title		
Size	Number	Revision
A3		
Date:	2/27/2023	Sheet of
File:	C:\Users\...\Block_HDMI, DP, DSI, USBSS and PCIe	SchDoc

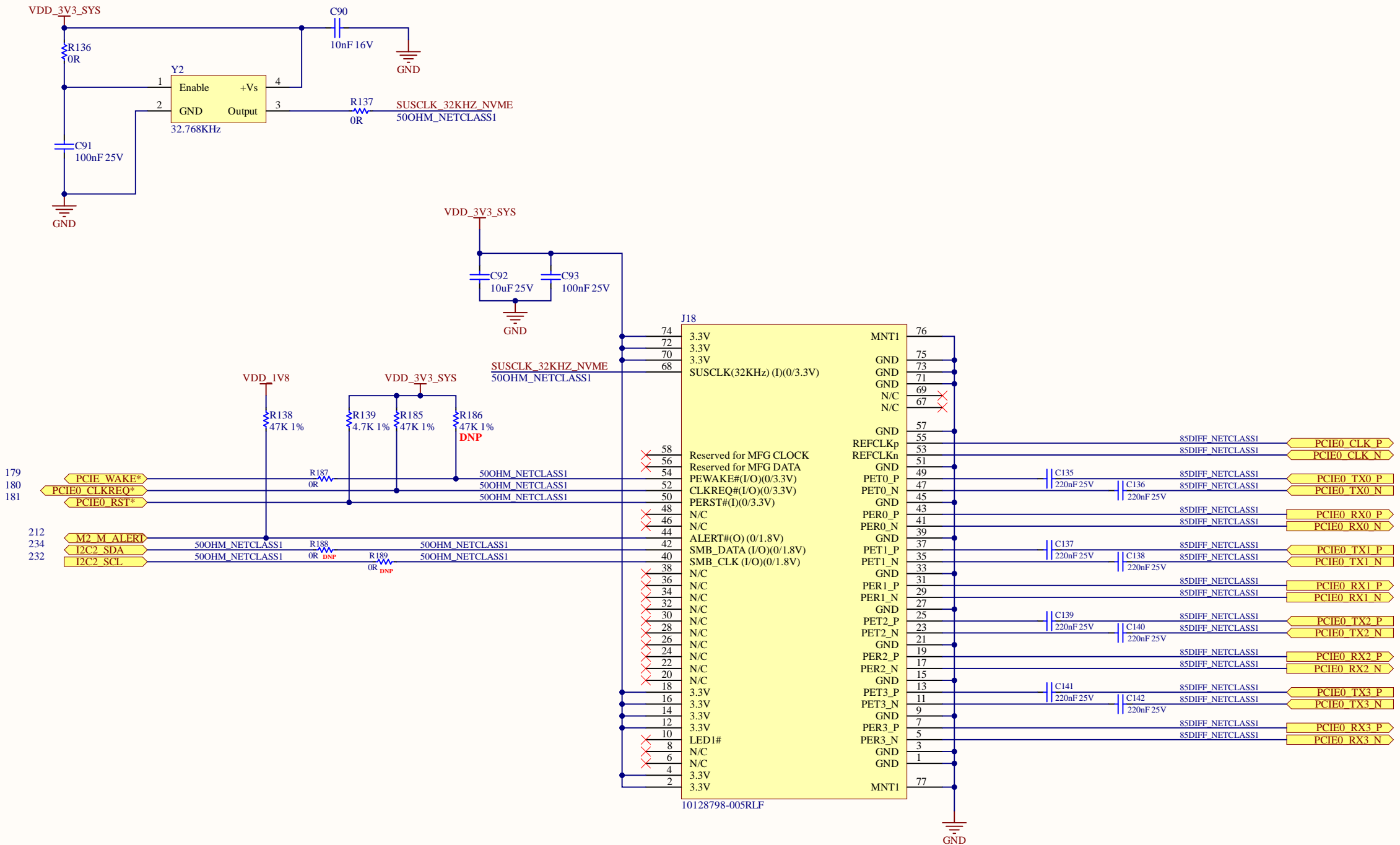


## USB 3.2 TYPE A



Title		
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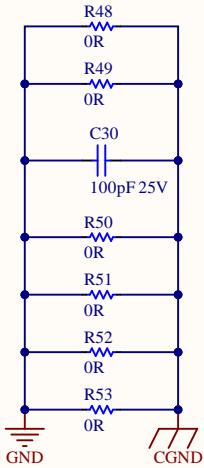
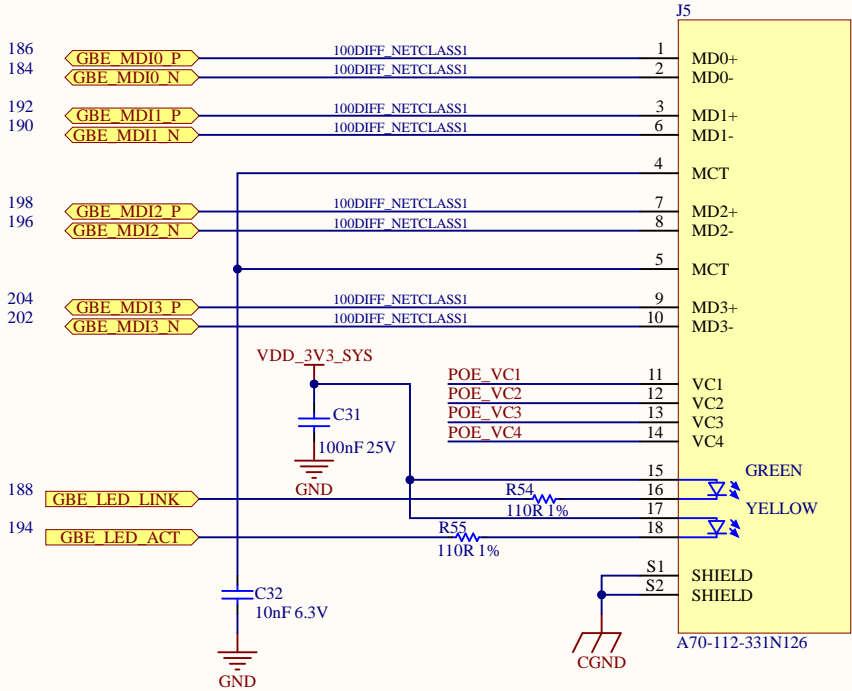
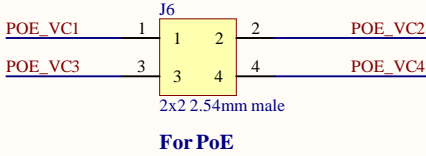
M.2\_M\_NVMe



Title		
Size	Number	Revision
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Date:	2/27/2023	Sheet of
File:	C:\Users\...\M.2_M_NVMe.SchDoc	Drawn By:

Ethernet

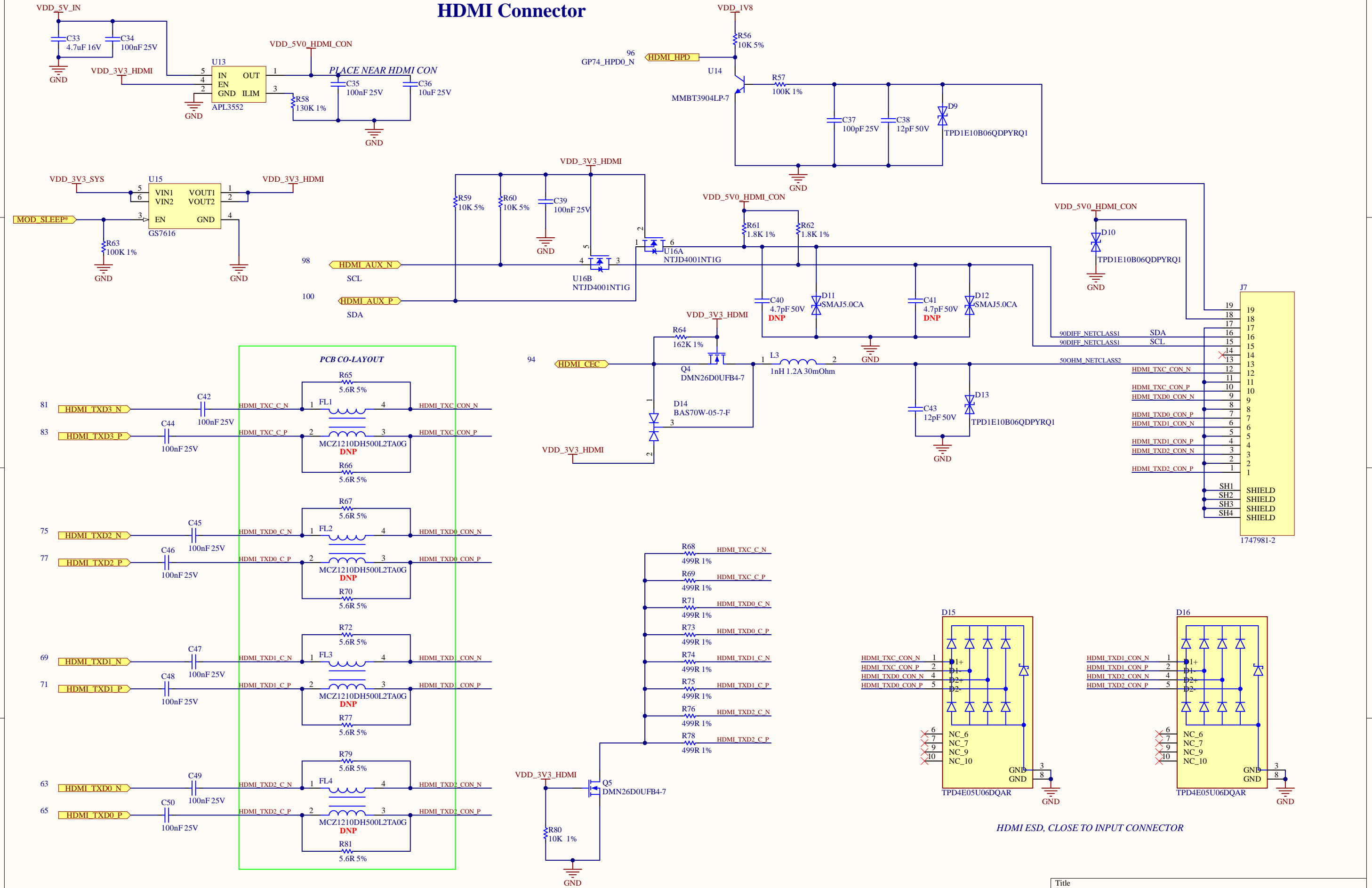
±15%. Differential impedance target is 100Ω.  
90Ω can be used if 100Ω is not achievable



Title		
Size A3	Number	Revision
Date: 2/27/2023	Sheet of	
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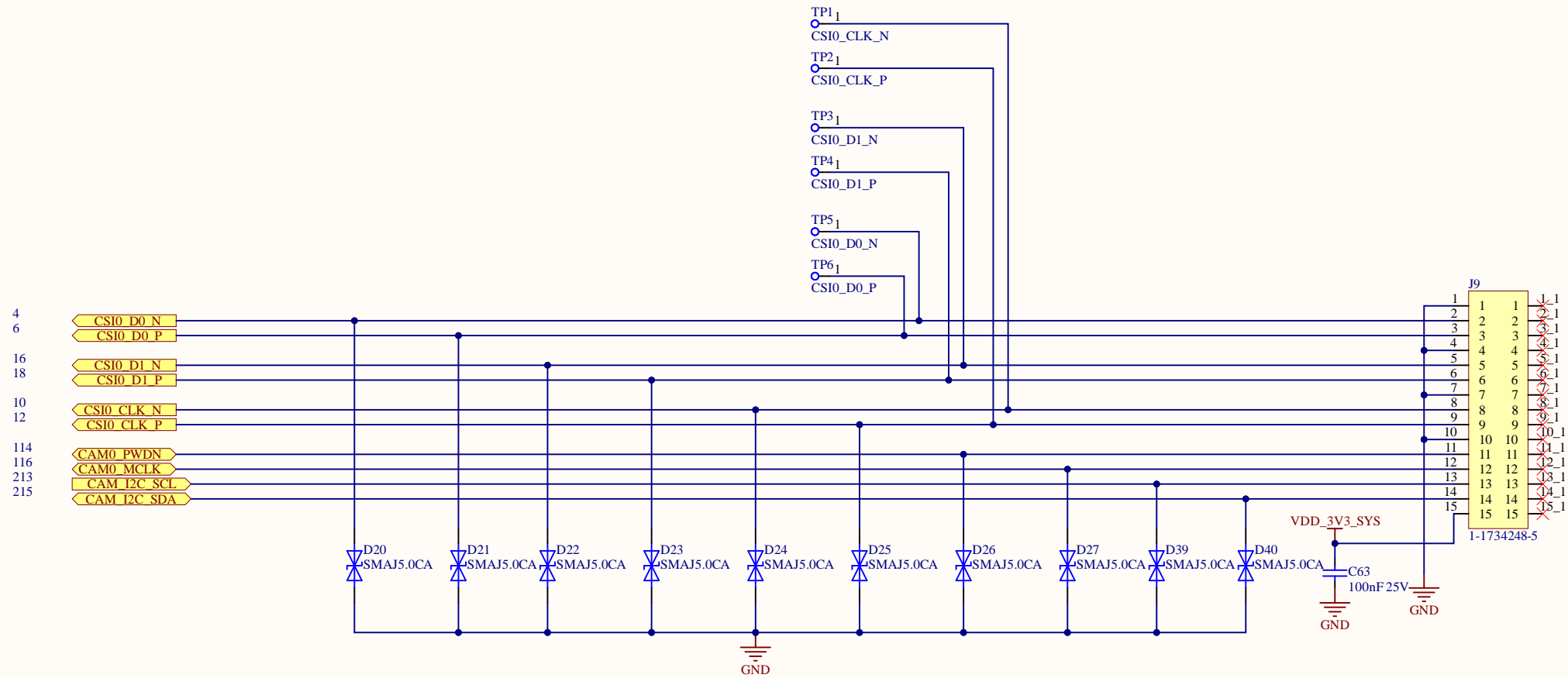


# HDMI Connector



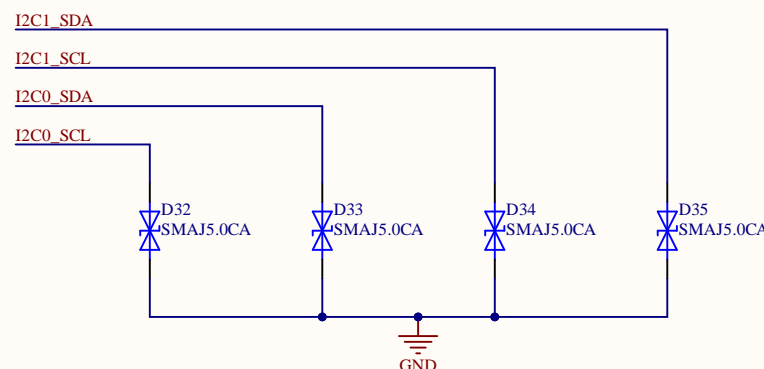
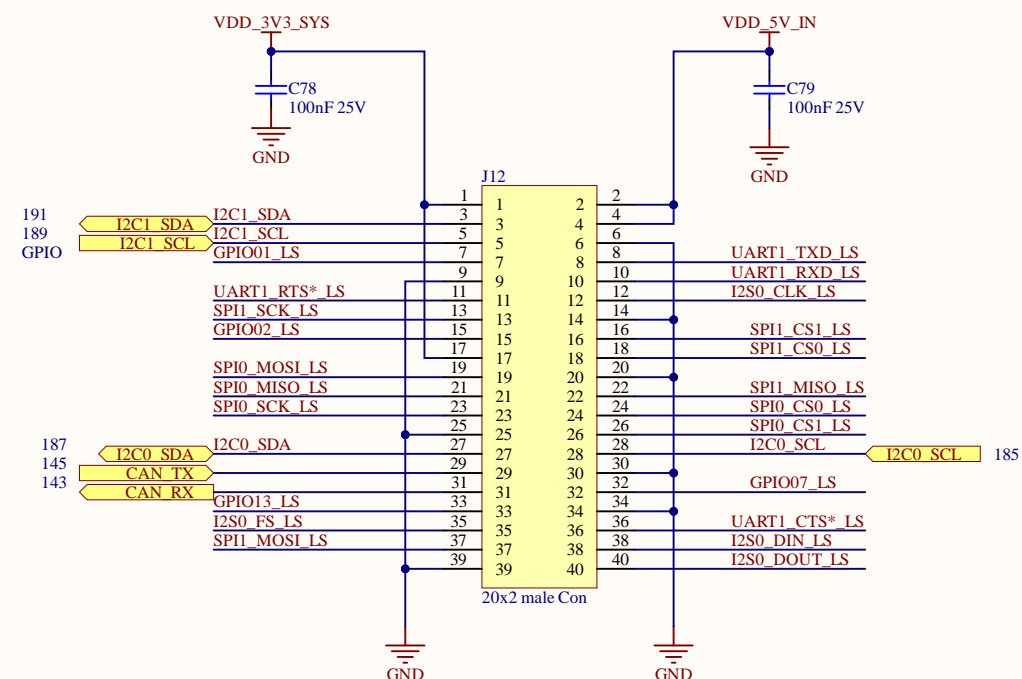
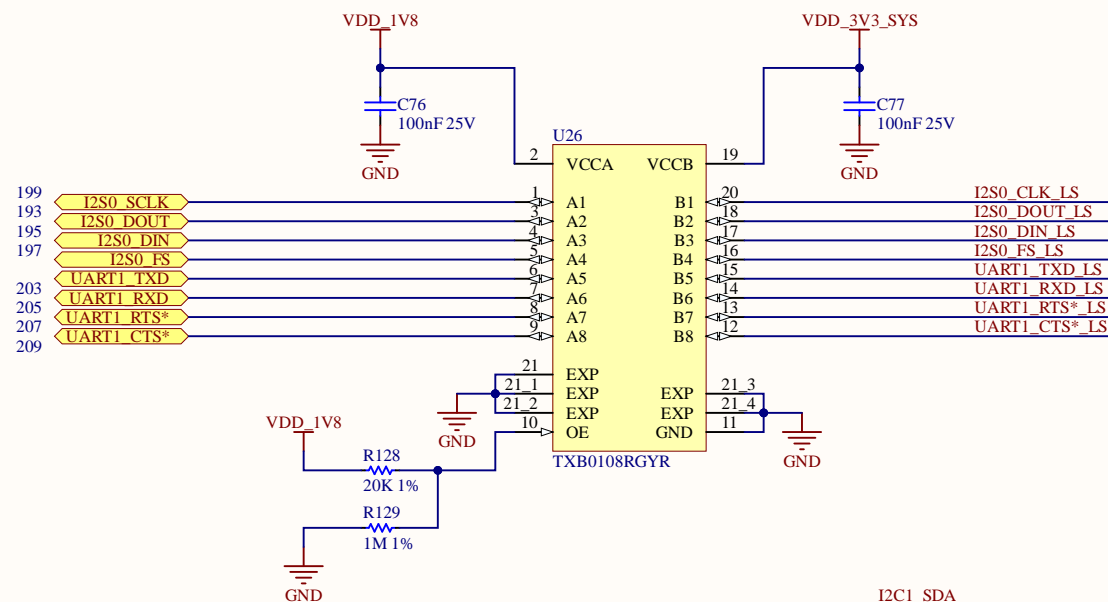
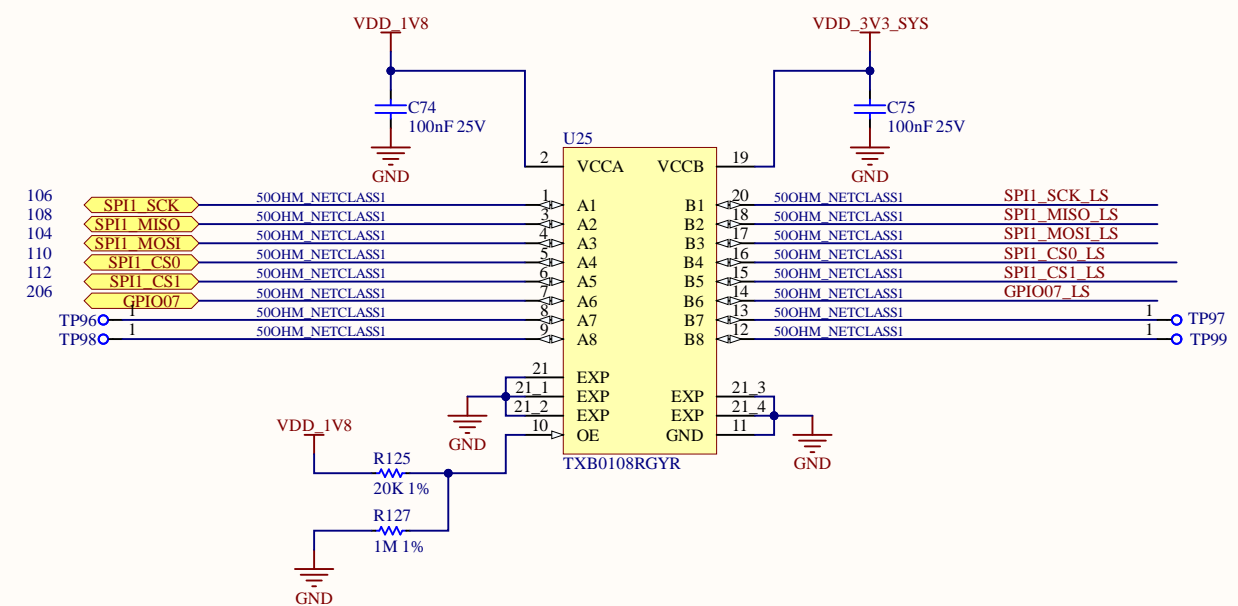
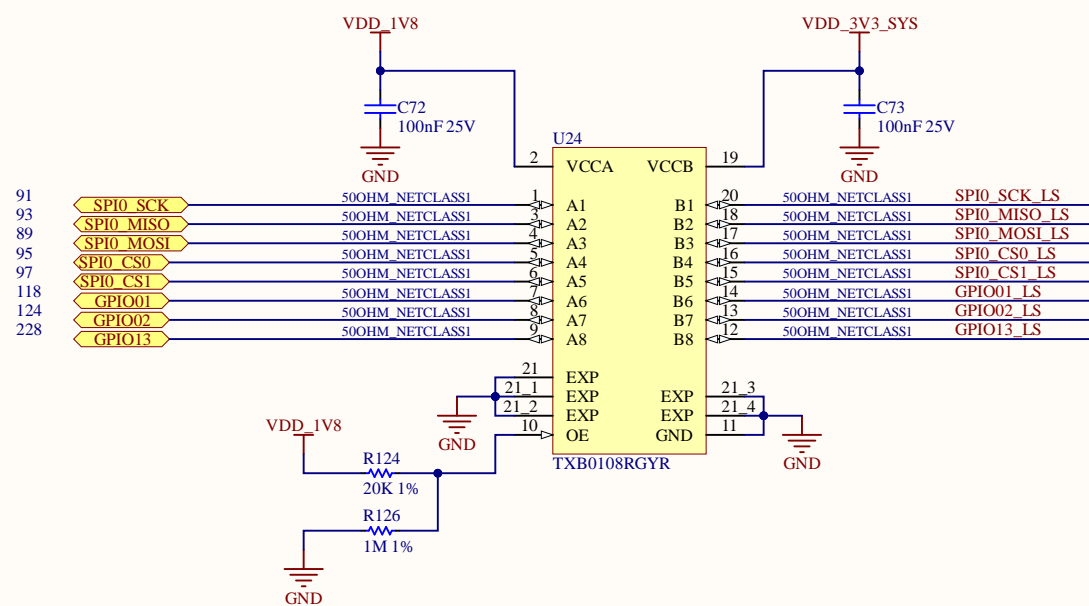
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CSI CAM CONNECTOR



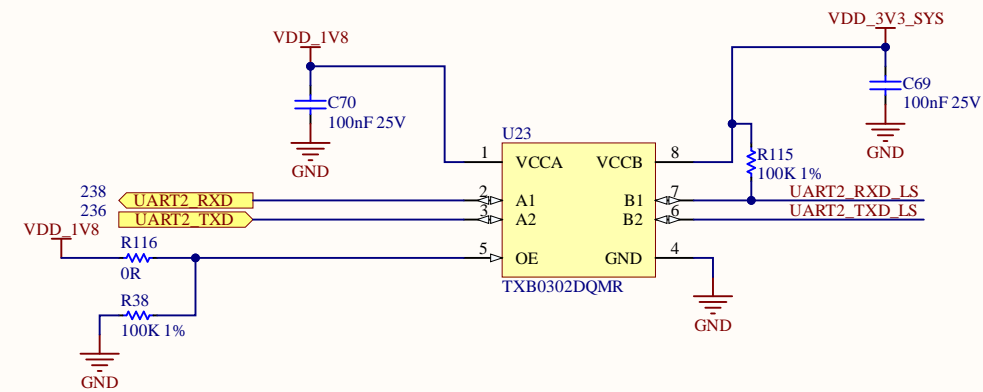
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Size A3	Number	Revision
Date: 2/27/2023	Sheet of	
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40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS

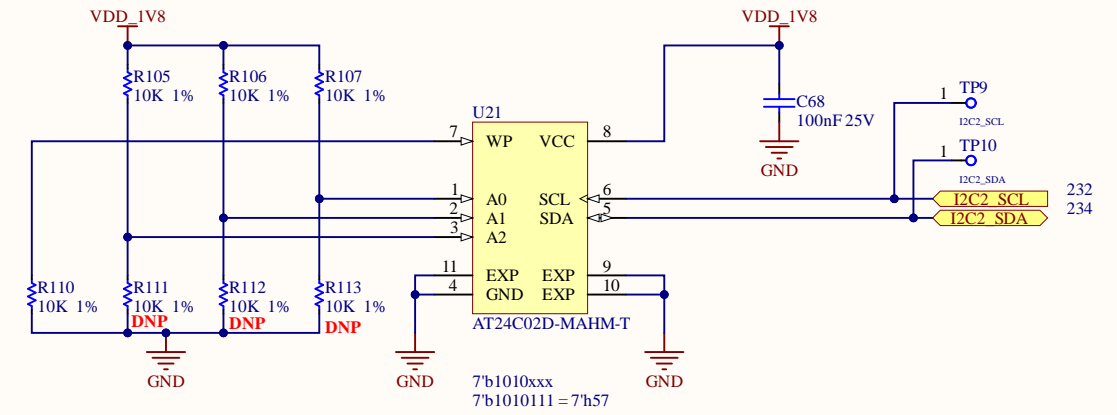


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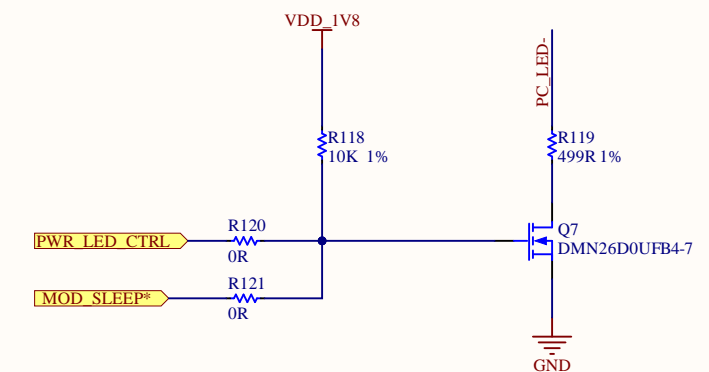
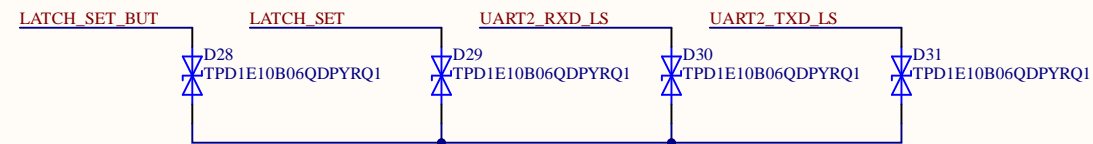
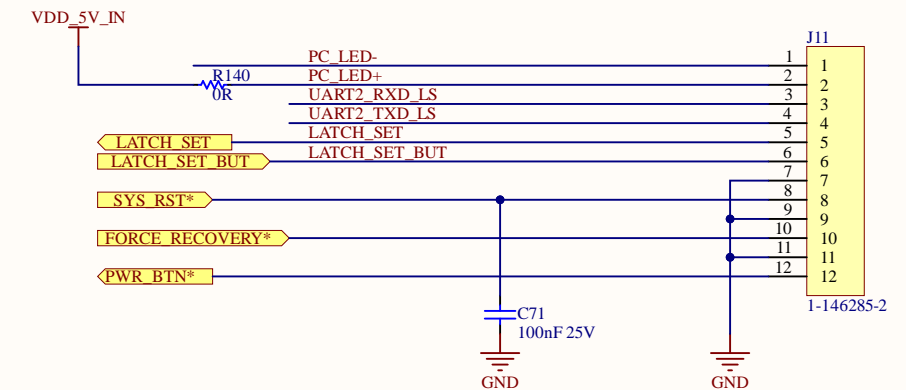
## Serial Port



## Carrier board config

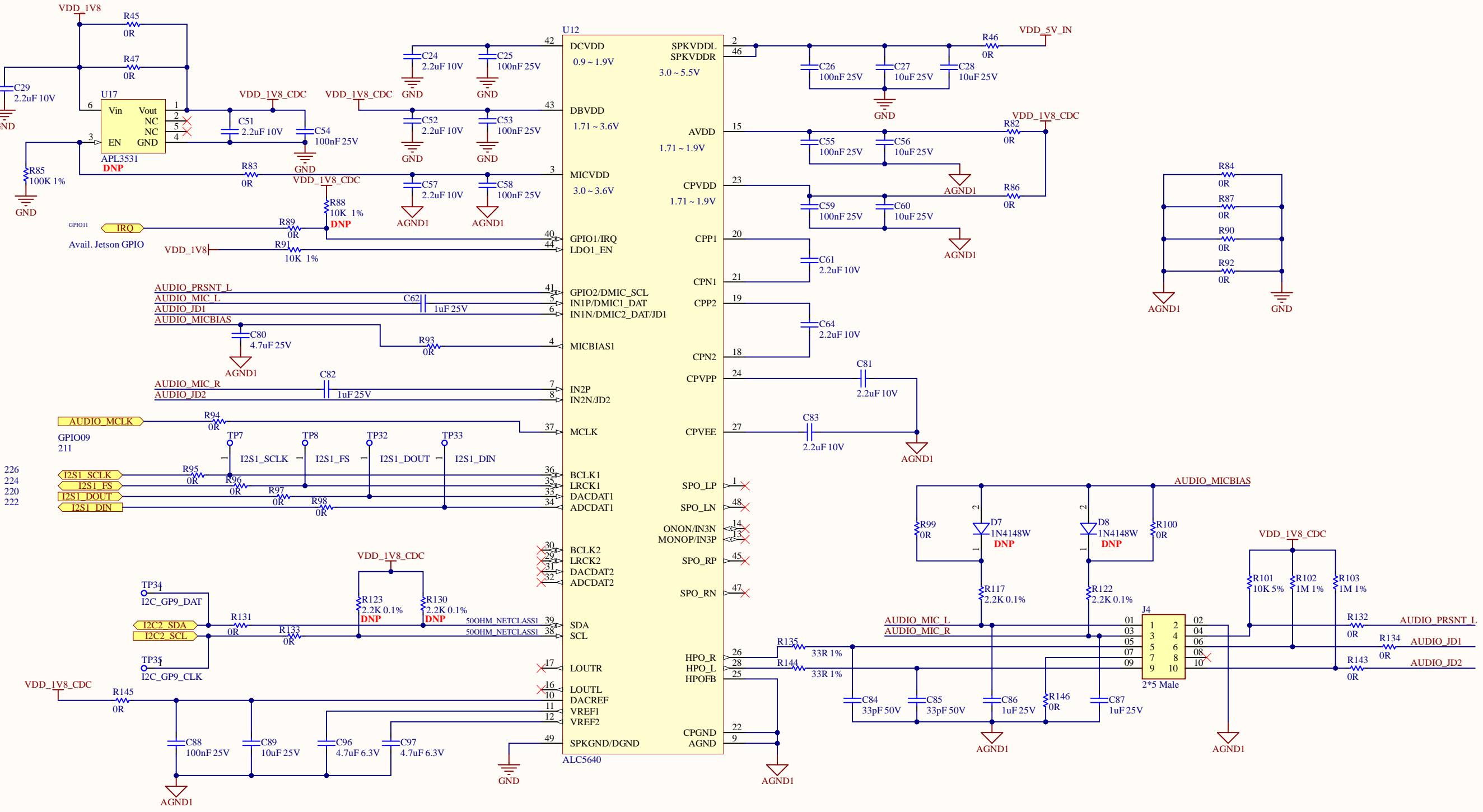


## Optional Button Header Pinout



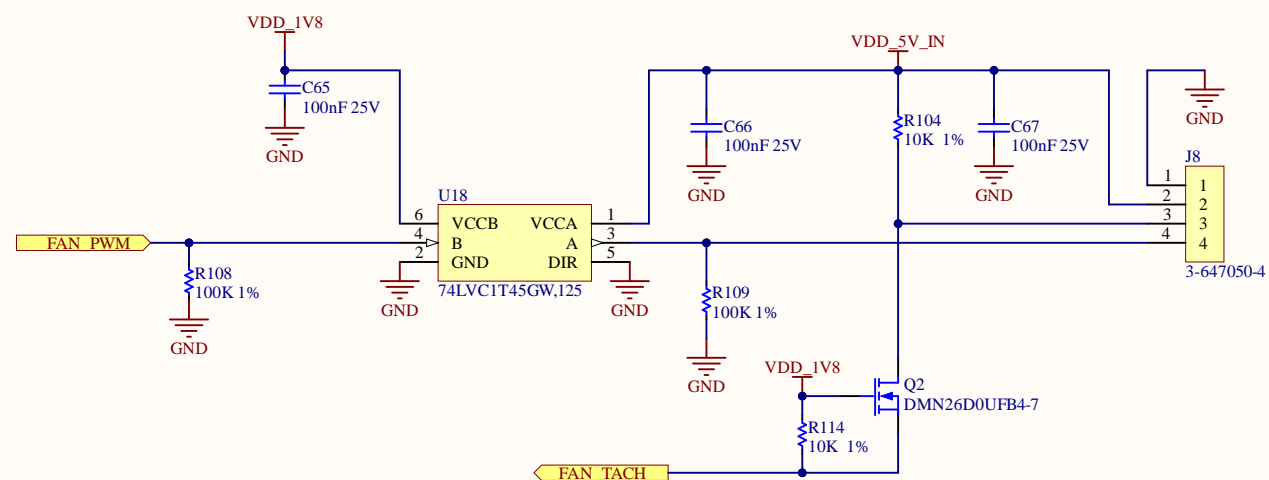
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Size A3	Number	Revision
Date:	2/27/2023	Sheet of
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AUDIO CODEC



Title		
Size	Number	Revision
A3		
Date:	2/27/2023	Sheet of
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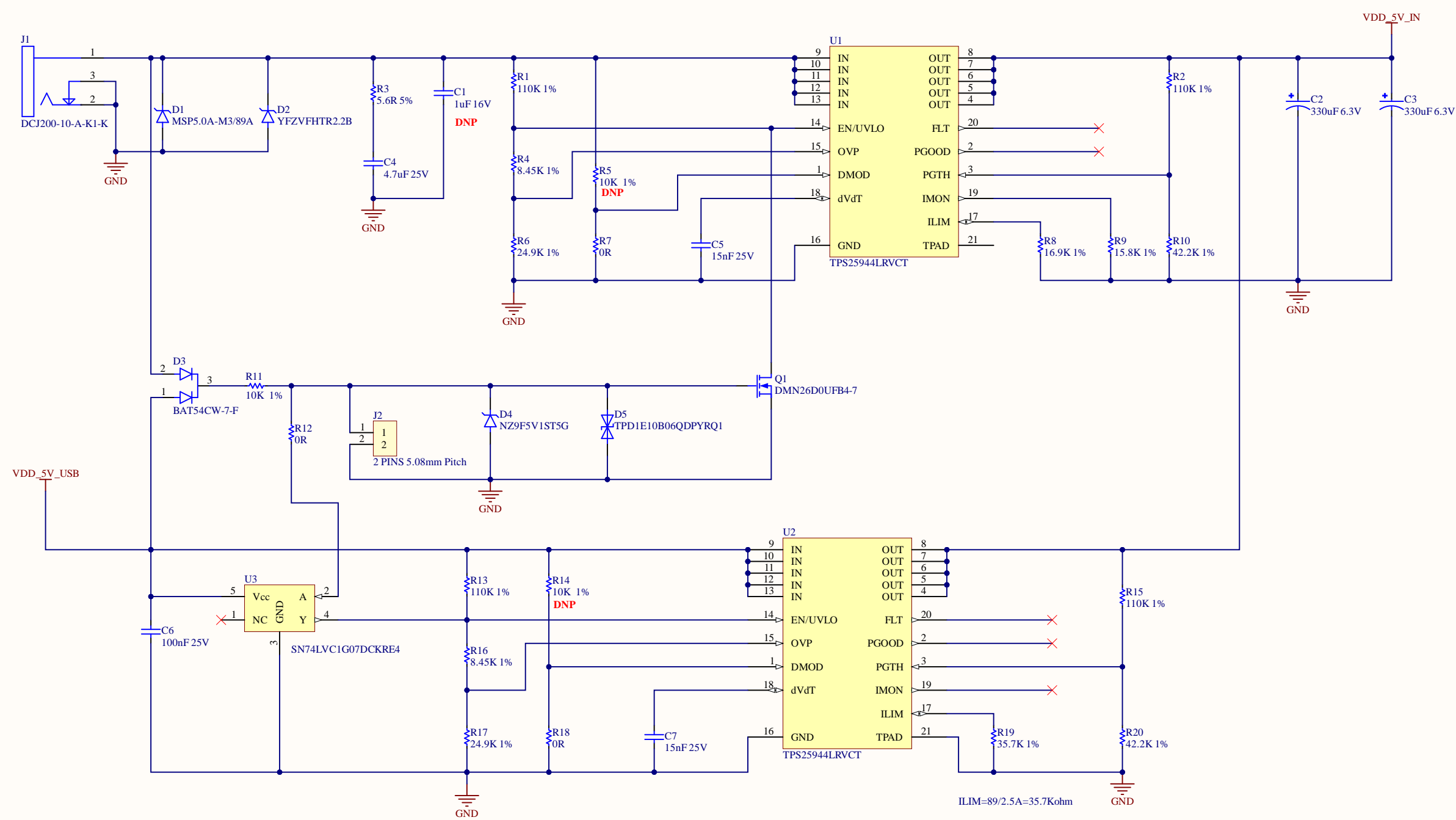
## SoC Fan Header 4pin



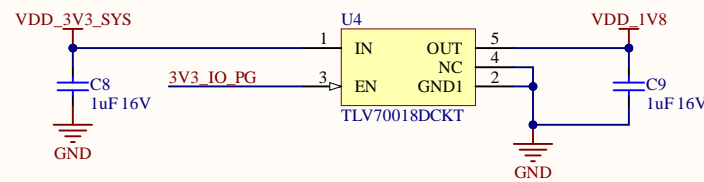
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Size A3	Number	Revision
Date:	2/27/2023	Sheet of
File:	C:\Users\...\SoC Fan Header.SchDoc	Drawn By:

DC JACK AND uUSB POWER SOURCE SELECTION

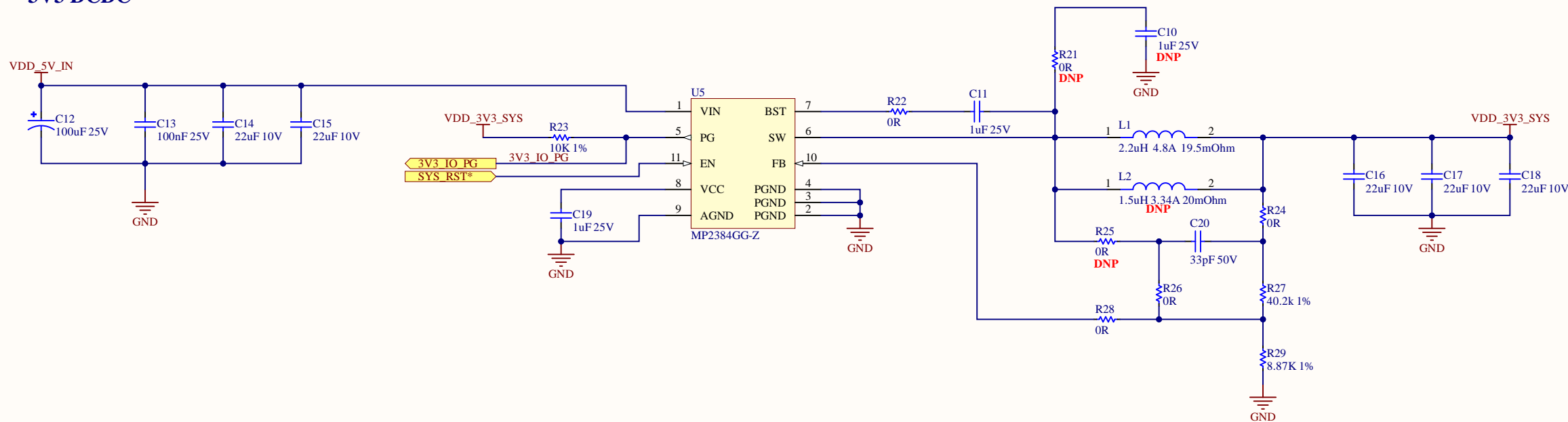
Reverse Polarity Protection



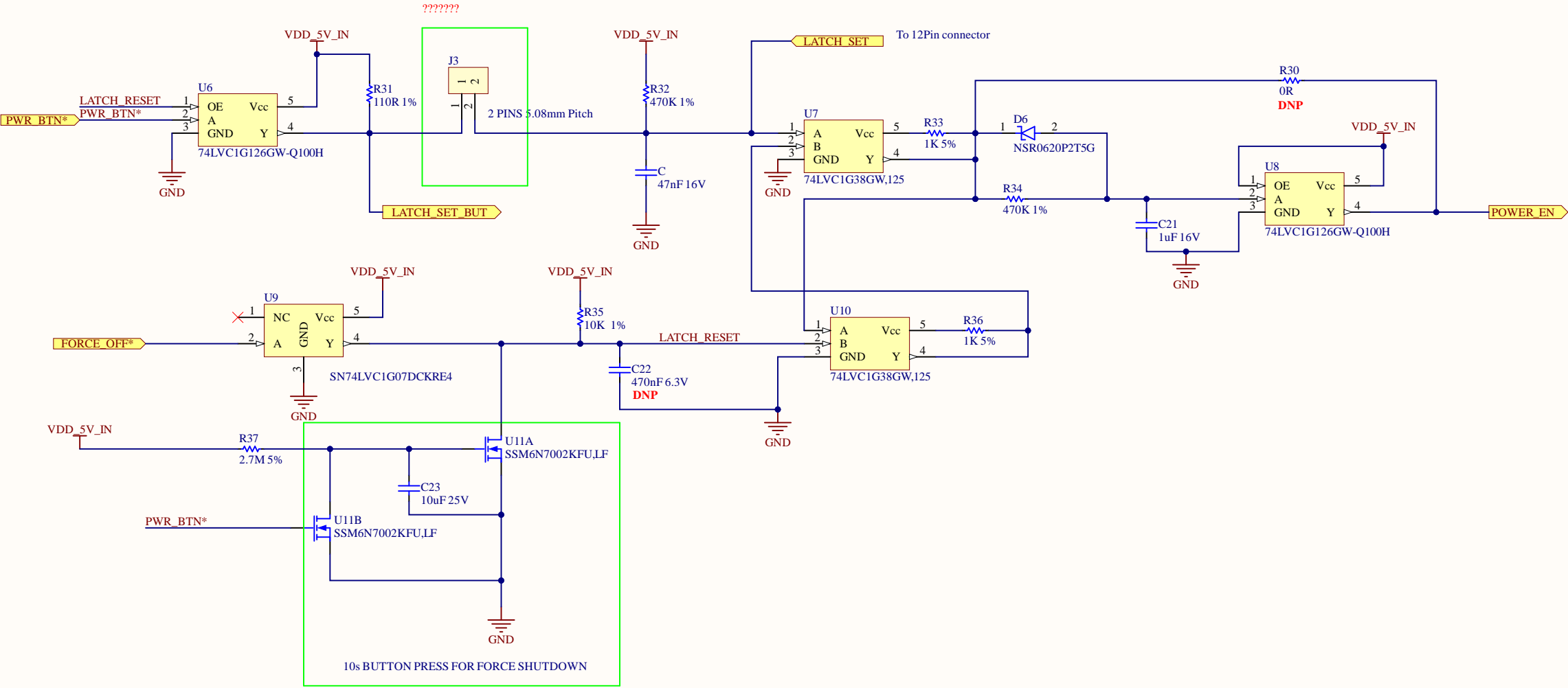
1V8 LDO



3V3 DCDC



Module Power ON/OFF logic



Title		
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File: C:\Users\...\ON_OFF_Circuit.SchDoc	Drawn By:	