

61. Printing statistics.

=== mkBooth_wallace_4 ===

Number of wires: 28926

Number of wire bits: 29179

Number of public wires: 3306

Number of public wire bits: 3559

Number of memories: 0

Number of memory bits: 0

Number of processes: 0

Number of cells: 29047

sky130_fd_sc_hd__a211o_2 214

sky130_fd_sc_hd__a211oi_2 4

sky130_fd_sc_hd__a21bo_2 7

sky130_fd_sc_hd__a21boi_2 117

sky130_fd_sc_hd__a21o_2	1242
sky130_fd_sc_hd__a21oi_2	232
sky130_fd_sc_hd__a221o_2	1799
sky130_fd_sc_hd__a22o_2	155
sky130_fd_sc_hd__a22oi_2	1
sky130_fd_sc_hd__a31o_2	2
sky130_fd_sc_hd__a31oi_2	1
sky130_fd_sc_hd__a32o_2	2
sky130_fd_sc_hd__a32oi_2	1
sky130_fd_sc_hd__and2_2	2612
sky130_fd_sc_hd__and2b_2	196
sky130_fd_sc_hd__and3_2	853
sky130_fd_sc_hd__and3b_2	232
sky130_fd_sc_hd__and4_2	3
sky130_fd_sc_hd__and4_4	1

sky130_fd_sc_hd__and4b_2	1
sky130_fd_sc_hd__buf_1	3533
sky130_fd_sc_hd__buf_2	1
sky130_fd_sc_hd__buf_4	1
sky130_fd_sc_hd__conb_1	2
sky130_fd_sc_hd__dfxtp_2	3425
sky130_fd_sc_hd__inv_2	1701
sky130_fd_sc_hd__mux2_2	32
sky130_fd_sc_hd__nand2_2	1064
sky130_fd_sc_hd__nand2b_2	9
sky130_fd_sc_hd__nand3_2	99
sky130_fd_sc_hd__nand3b_2	1
sky130_fd_sc_hd__nand4_2	1
sky130_fd_sc_hd__nor2_2	1932
sky130_fd_sc_hd__nor2b_2	3
sky130_fd_sc_hd__nor3_2	62

sky130_fd_sc_hd__nor3b_2	3
sky130_fd_sc_hd__o211a_2	1402
sky130_fd_sc_hd__o211a_4	123
sky130_fd_sc_hd__o211ai_2	2
sky130_fd_sc_hd__o21a_2	196
sky130_fd_sc_hd__o21a_4	1
sky130_fd_sc_hd__o21ai_2	69
sky130_fd_sc_hd__o21ba_2	1455
sky130_fd_sc_hd__o21bai_2	52
sky130_fd_sc_hd__o221a_2	599
sky130_fd_sc_hd__o22a_2	3
sky130_fd_sc_hd__o311a_2	3
sky130_fd_sc_hd__o31a_2	5
sky130_fd_sc_hd__o31ai_2	3
sky130_fd_sc_hd__o32a_2	1
sky130_fd_sc_hd__or2_2	2359

sky130_fd_sc_hd__or2_4	1
sky130_fd_sc_hd__or2b_2	922
sky130_fd_sc_hd__or3_2	83
sky130_fd_sc_hd__or3b_2	986
sky130_fd_sc_hd__or4_2	11
sky130_fd_sc_hd__or4b_2	1
sky130_fd_sc_hd__xnor2_2	960
sky130_fd_sc_hd__xor2_2	266

Chip area for module '\mkBooth_wallace_4': 274638.400000