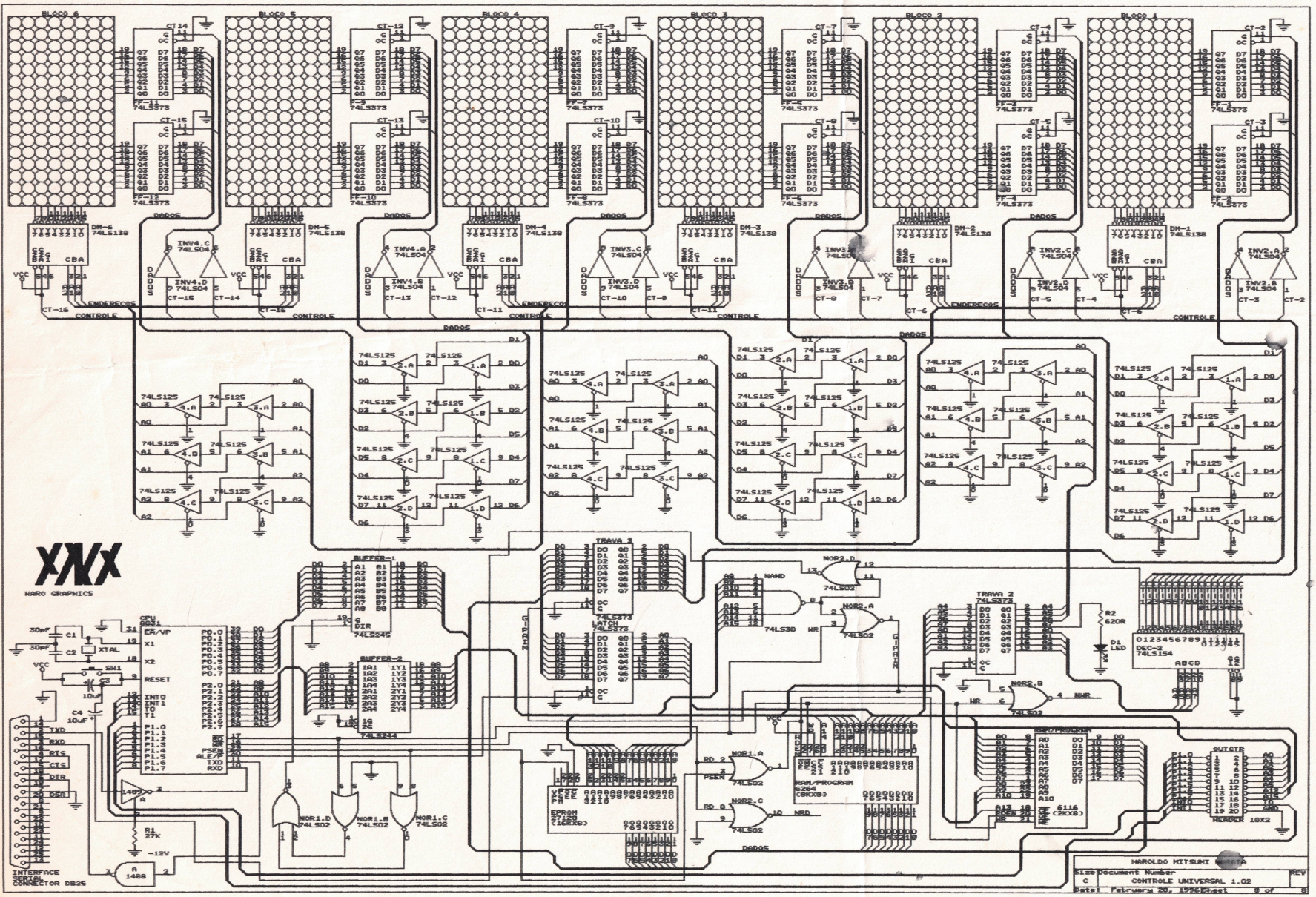

BLOCO 6
BLOCO 5
BLOCO 4
BLOCO 3
BLOCO 2
BLOCO 1
DADOS
ENDEREÇOS
CONTROLE
XNX
HARD GRAPHICS
CPU 8051
BUFFER-1
BUFFER-2
TRAVA 2
TRAVA 3
LATCH
RAM/PROGRAM
EPROM
INTERFACE SERIAL CONNECTOR DB25
HAROLDO MITSUMI
CONTROLE UNIVERSAL 1.02
February 28, 1996
Sheet 8 of 8

POSITION OF WIRE-WRAP THE COMPONENTS

HARD GRAPHICS ON SALE

C.M. 6 PIN

1	DM 1
2	DM 2
3	FF-1
4	FF-4
5	FF-3
6	FF-2

C.M 6 PIN

1	DM 3
2	DM 4
3	FF-5
4	FF-8
5	FF-7
6	FF-6

C.M 6 PIN

1	DM 5
2	DM 6
3	FF-9
4	FF-12
5	FF-11
6	FF-10

C.M 3 PIN (FONT)

1	—
2	GND
3	+5A

C.M 3 PIN (FONT)

1	+5
2	-12
3	+12

C.M 3 PIN (BOTON)

1	—
2	PUSHR
3	PUSHV

C.M 3 PIN (SERIAL)

1	TXDS
2	RXDS
3	GND

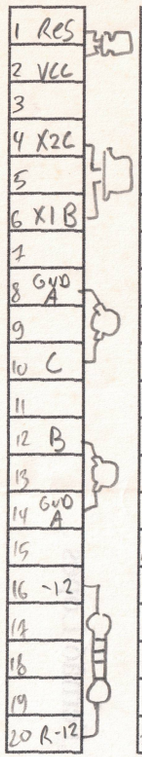
Conector Molex 10 PIN Fém.

1	2	3	4	5	6	7	8	9	10
+7	GND	+5A	A1	A0	A2	A5	A3	A4	|

Conector Molex 10 pin Fém.

1	2	3	4	5	6	7	8	9	10
|	+5	D0	D1	D2	D3	D4	D5	D6	D7

CPU



8031

1	P1.0	VCC	40
2	P1.1	P0.0	39
3	P1.2	P0.1	38
4	P1.3	P0.2	37
5	P1.4	P0.3	36
6	P1.5	P0.4	35
7	P1.6	P0.5	34
8	P1.7	P0.6	33
9	RESET	P0.7	32
10	RXD	EA/VP	31
11	TXD	ALE/P	30
12	INT0	PSEN	29
13	INT1	P2.7	28
14	T0	P2.6	27
15	T1	P2.5	26
16	WR	P2.4	25
17	RD	P2.3	24
18	X2	P2.2	23
19	X1	P2.1	22
20	GND	P2.0	21

BUFFER-2

1	1G	VCC	20
2	1A1	2G	19
3	2Y4	1Y1	18
4	1A2	2A4	17
5	2Y3	1Y2	16
6	1A3	2A3	15
7	2Y2	1Y3	14
8	1A4	2A2	13
9	2Y1	1Y4	12
10	GND	2A1	11

74LS244

TRAVA 3

1	OC	VCC	20
2	Q0	Q7	19
3	D0	D7	18
4	D1	D6	17
5	Q1	Q6	16
6	Q2	Q5	15
7	D2	D5	14
8	D3	D4	13
9	Q3	Q4	12
10	GND	G	11

74LS373

BUFFER-1

1	DIR	VCC	20
2	A1	G	19
3	A2	B1	18
4	A3	B2	17
5	A4	B3	16
6	A5	B4	15
7	A6	B5	14
8	A7	B6	13
9	A8	B7	12
10	GND	B8	11

74LS245

CONV 89

1489

1	RXDS	VCC	14
2	R-12		13
3	RXD		12
4			11
5			10
6			9
7	GND		8

6264 RAM

1		VCC	28
2	A12	WE	27
3	A7	CS2	26
4	A6	A8	25
5	A5	A9	24
6	A4	A11	23
7	A3	OE	22
8	A2	A10	21
9	A1	CS1	20
10	A0	D7	19
11	D0	D6	18
12	D1	D5	17
13	D2	D4	16
14	GND	D3	15

RAM 1 — EPROM

27128

1	VPP	VCC	28
2	A12	PGN	27
3	A7	A13	26
4	A6	A8	25
5	A5	A9	24
6	A4	A11	23
7	A3	OE	22
8	A2	A10	21
9	A1	CE	20
10	A0	D7	19
11	D0	D6	18
12	D1	D5	17
13	D2	D4	16
14	GND	D3	15

1488 CONV88

1	-12	+12 14
2	TXD	13
3	TXDS	12
4		11
5		10
6		9
7	GND	8

TRAVA 2

1	OC	VCC	20
2	Q0	Q7	19
3	D0	D7	18
4	D1	D6	17
5	Q1	Q6	16
6	Q2	Q5	15
7	D2	D5	14
8	D3	D4	13
9	Q3	Q4	12
10	GND	G	11

74LS373

LATCH

1	OC	VCC	20
2	Q0	Q7	19
3	D0	D7	18
4	D1	D6	17
5	Q1	Q6	16
6	Q2	Q5	15
7	D2	D5	14
8	D3	D4	13
9	Q3	Q4	12
10	GND	G	11

NAO OU 2

74LS02

1	G-PAIN OUT	VCC	14
2	D IN	OUT CRESET	13
3	WR IN	IN CT-1	12
4	NWR OUT	IN D	11
5	GND IN	OUT NRD	10
6	WR IN	IN GND	9
7	GND	IN RD	8

NAO OU 1

74LS02

1	RDEM OUT	VCC	14
2	RD IN	OUT DIR	13
3	PSEN IN	IN A	12
4	A OUT	IN B	11
5	GND IN	OUT B	10
6	RD IN	IN GND	9
7	GND	IN PSEN	8

NAO E

74LS30

1	A8	VCC	14
2	A9		13
3	A10	A15	12
4	A11	A14	11
5	A12		10
6	A13		9
7	GND	D	8

RESET CRESET

74LS154 DEC-2

1	G	VCC	24
2	1	A	23
3	2	B	22
4	3	C	21
5	4	D	20
6	5	G2	19
7	6	G1	18
8	7	15	17
9	8	14	16
10	9	13	15
11	10	12	14
12	GND	11	13

DEC-2

RAM 2

6116 RAM

1	A7	VCC	24
2	A6	A8	23
3	A5	A9	22
4	A4	WE	21
5	A3	OE	20
6	A2	A10	19
7	A1	CE	18
8	A0	D7	17
9	D0	D6	16
10	D1	D5	15
11	D2	D4	14
12	GND	D3	13

OUT CTR

1	P1.0	A0	2
3	P1.1	A1	4
5	P1.2	A2	6
7	P1.3	A3	8
9	P1.4	A4	10
11	P1.5	A5	12
13	P1.6	A12	14
15	P1.7	A15	16
17	INT0	T0	18
19	INT1	GND	20

SLOT 20 PIN