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# Chapter 1: Introduction

Electronic design automation tools played **Thu Feb 19 15:17:08 MST 2015** a central role in enabling the Moore's Law miniaturization of VLSI integrated circuits. Designers' productivity in the digital design domains has increased dramatically, because of continuous improvements in the software tool algorithms and the machines that run them.

## 1.1 Example Procedure

```
proc generate_report {filename} {
    set 0 [open /tmp/tmp.html w]
    puts $0 <html>
    puts $0 <head>
    puts $0 "<style type=\"text/css\">"
    foreach style_key [array names ::Style *,code] {
        set style [lindex [split $style_key ,] 0]
        puts $0 "    p.$style \{"
        foreach key [array names ::Style $style,*] {
            skip {$key=="$style,code"}
            set field [lindex [split $key ,] end]
            puts $0 "        $field: $::Style($key);"
        }
        puts $0 "    \}"
    }
    puts $0 </style>
    puts $0 </head>
    puts $0 <body>
    puts $0 $::html_body
    puts $0 </body>
    puts $0 </html>
    close $0
    if {[catch {exec /usr/bin/wkhtmltopdf-amd64 /tmp/tmp.html $filename} msg]} {
        Info: $msg
    }
}
```

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