

REPORT

The overall time spent to work through all the instructions is less at part 2 than at part 1. Therefore overall time reduced when having a cache with main memory.

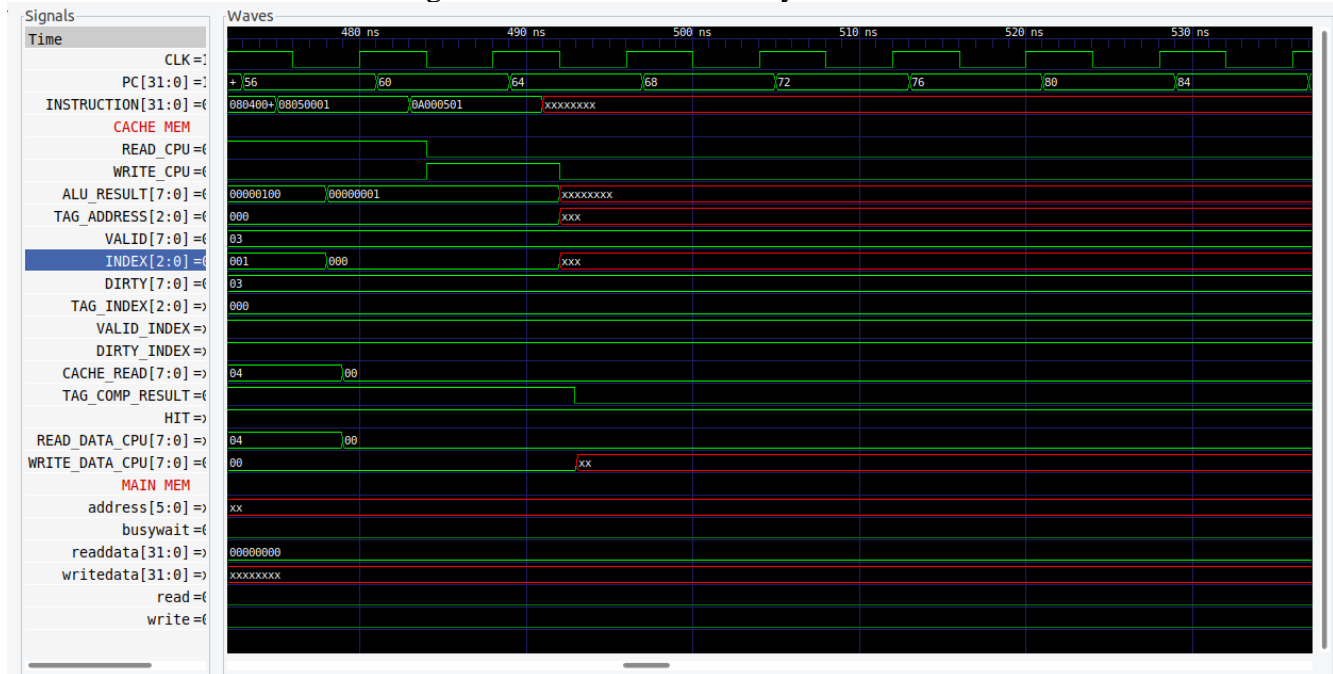


Figure:wave form for last instruction when using cache



Figure:wave form for last instruction when cache is not using

For some instructions at the beginning of the instruction-set spent more time at part2 than part1. After the reset signal ,instructions spent more time at part2 to get cache blocks valid .But for these instructions in part1 has spent less time.

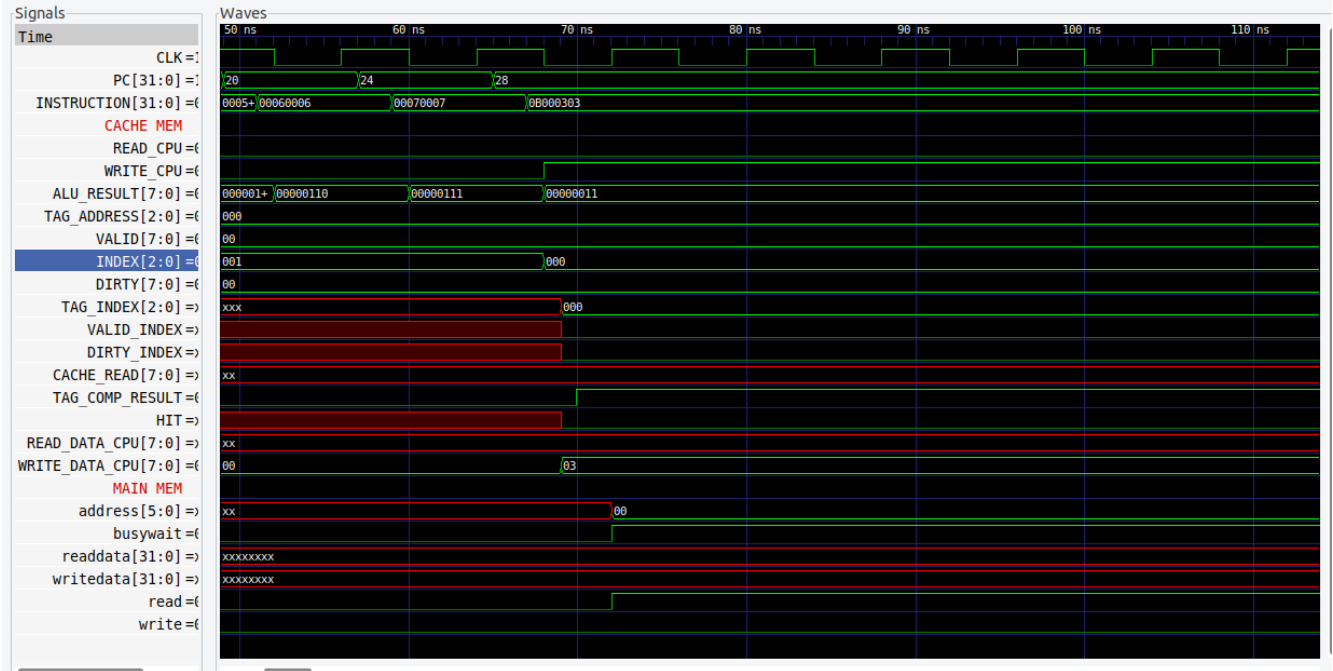


Figure:instruction beginning from pc=28 in part2

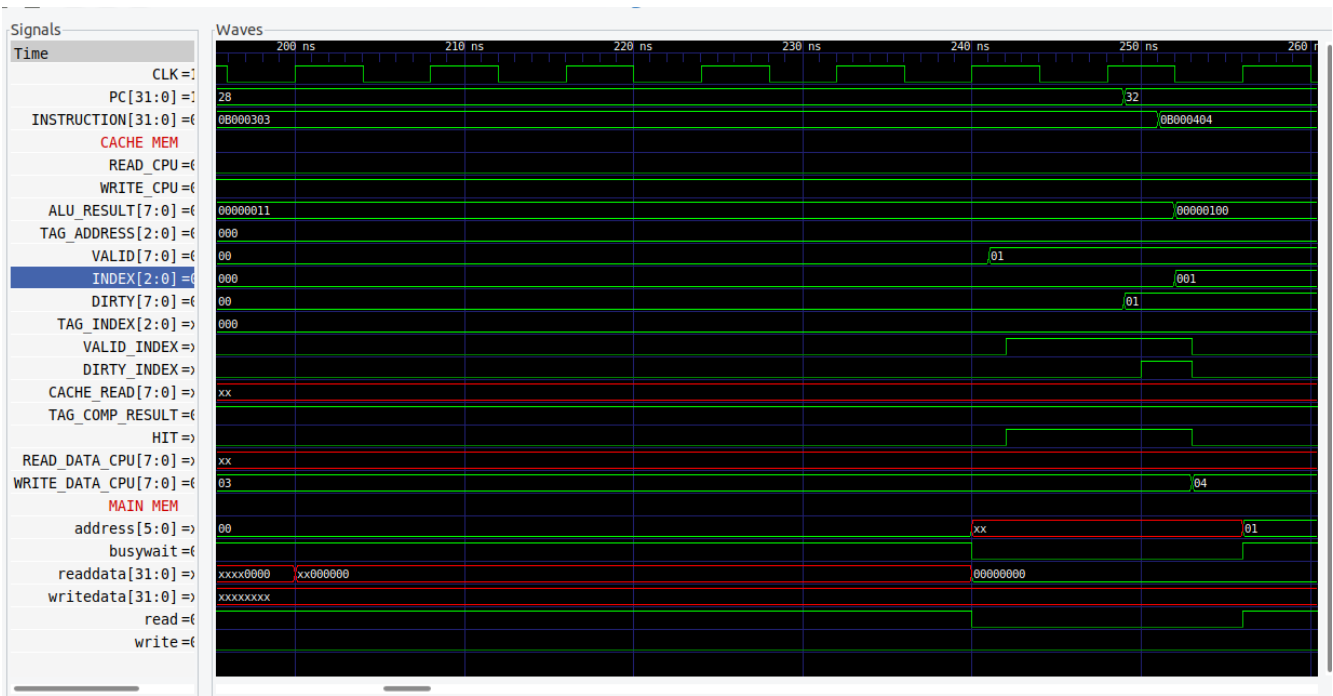


Figure:ends at pc=32 in part2

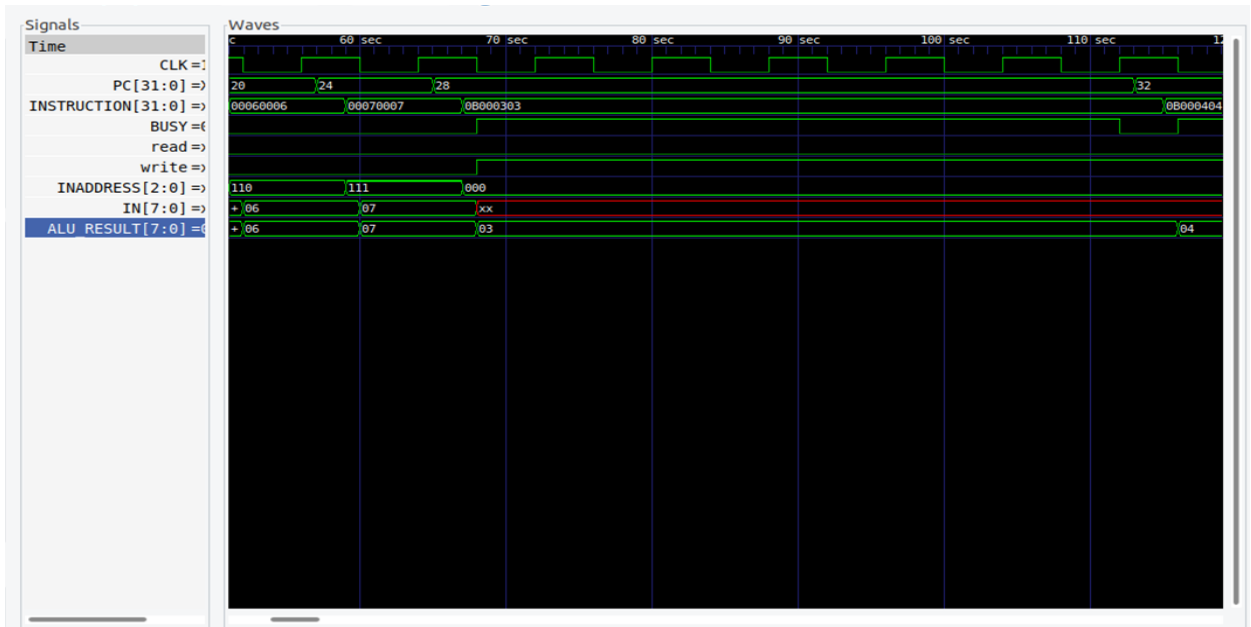


Figure: instruction that begins at pc=28 and ends at pc=32 in part1

The last instructions in instruction set has spent less time at part2 than part1.

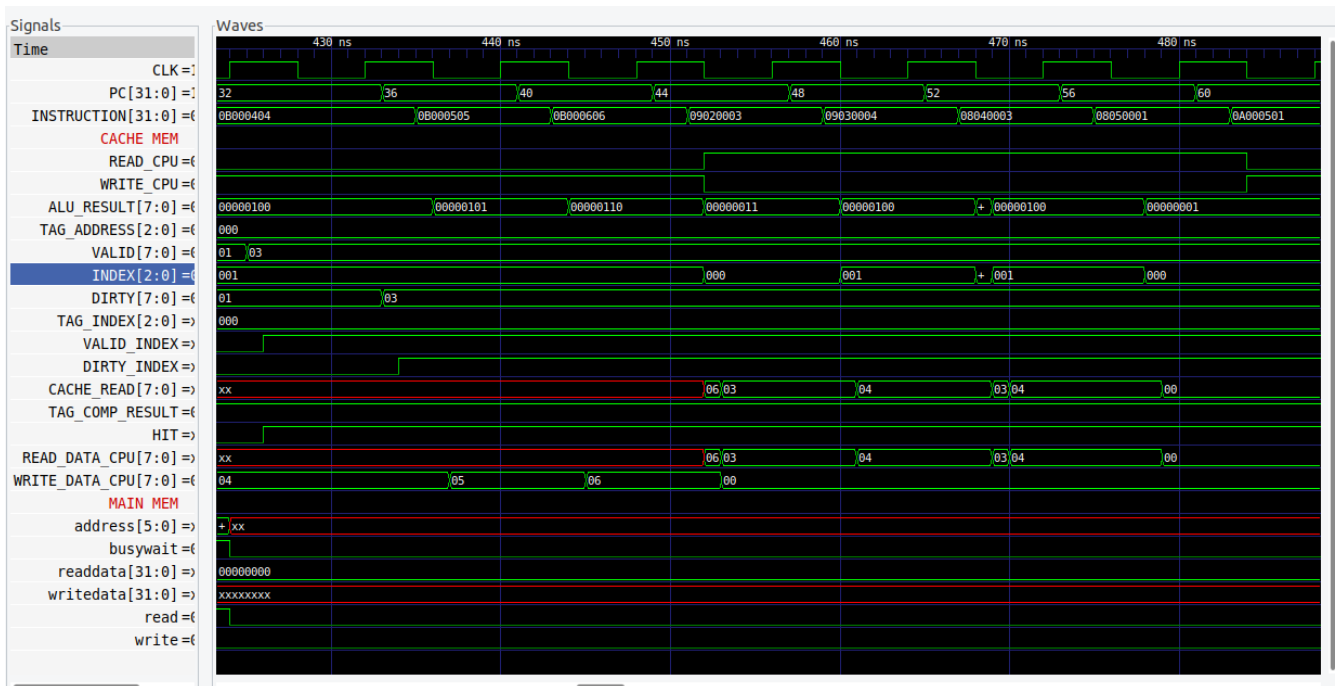


Figure: Last instructions for part2

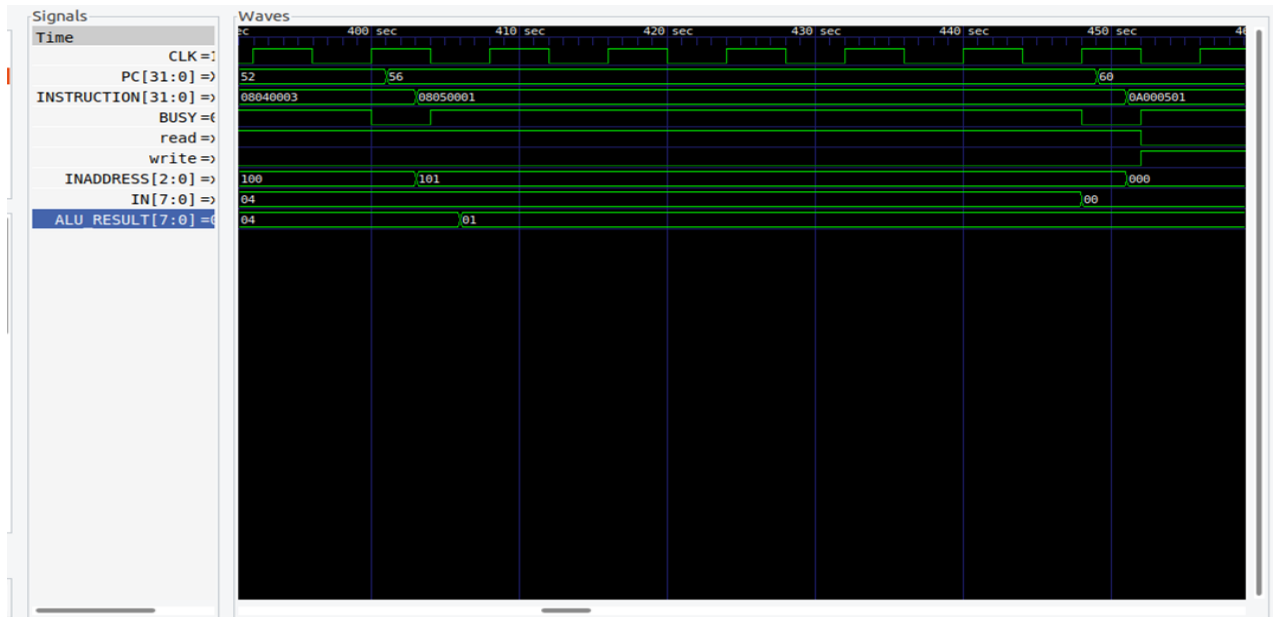


Figure: Last instructions for part1

In overall cache with memory has performed better than without a cache for this selected instruction set .After cache get stable it reacts faster than main memory without cache .But main memory without cache has performed well in instructions at the beginning .To perform write back procedure some instructions in part2 has spent more time than at part1.