## Digitaltechnik - Übung 3

1.a

1. 
$$Z = \left(A \wedge \overline{B \wedge C}\right) \vee A \vee (B \wedge C)$$

$$= A \vee \left((A \vee (B \wedge C)) \wedge \left(\overline{B \wedge C} \vee (B \wedge C)\right)\right)$$

$$= A \vee (A \vee (B \wedge C))$$

$$= A \vee (B \wedge C)$$
2. 
$$Z = \left(A \wedge \overline{B \wedge C}\right) \vee \left(A \wedge \overline{B \vee C}\right)$$

$$= \left(A \wedge \overline{B \wedge C}\right) \vee (A \wedge B \wedge C) = A$$
3. 
$$Z = (A \wedge (B \vee C)) \vee (A \wedge \neg C)$$

$$= A \wedge ((B \vee C) \vee \neg C)$$

$$= A$$
4. 
$$Z = ((B \wedge \neg C) \wedge (A \vee \neg A)) \vee ((\neg B \wedge C) \wedge (A \vee \neg A))$$

$$= (B \wedge \neg C) \vee (\neg B \wedge C)$$
5. 
$$(A \wedge (D \vee (\neg D \wedge \neg C) \vee (C \wedge \neg D))) \vee ((B \wedge C) \wedge (D \vee \neg A))$$

$$A \vee ((B \wedge C) \wedge (D \vee \neg A))$$

$$(A \vee (B \wedge C)) \wedge ((A \vee D) \vee (A \vee \neg A))$$

$$A \vee (B \wedge C)$$

b)

i.

$$Z = (A \land B) \lor (B \land C)$$

$$\overline{(A \land B) \lor (B \land C)}$$

$$NAND : \overline{A \land B} \land \overline{B \land C}$$

$$\overline{(\neg A \lor \neg B) \land (\neg B \lor \neg C)}$$

$$\overline{\neg B \lor (\neg A \land \neg C)}$$

$$NOR : \overline{\neg B \lor \overline{A \lor C}}$$

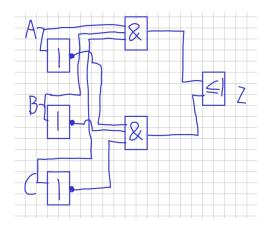
ii.

$$Z = A \oplus B$$
$$(\neg A \land B) \lor (A \land \neg B)$$
$$\text{NAND} : \overline{\neg A \land B} \land \overline{A \land \neg B}$$
$$(A \lor B) \land (\neg A \lor \neg B)$$
$$\text{NOR} : \overline{\overline{A \lor B} \lor \overline{\neg A \lor \neg B}}$$

c)

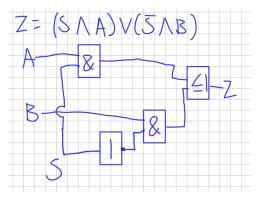
Minterms:

$$(\neg A \wedge \neg B \wedge \neg C) \vee (A \wedge B \wedge C)$$



 $\text{Error:} \geq 1$ 

## 2.a

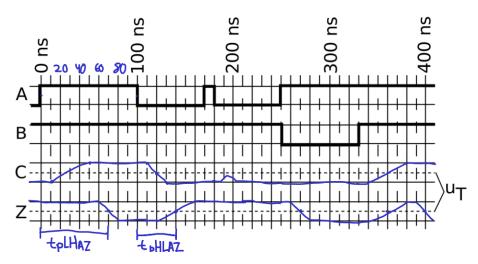


b

Man müsste zumindest eine dritte state zu dem Steuereinheit hinzufügen, zum Beispiel durch 2 Bit Binary Kodierung.

3

a)



b) 
$$t_{pLHAZ} = 70 \text{ns}$$
  
 $t_{pHLAZ} = 40 \text{ns}$