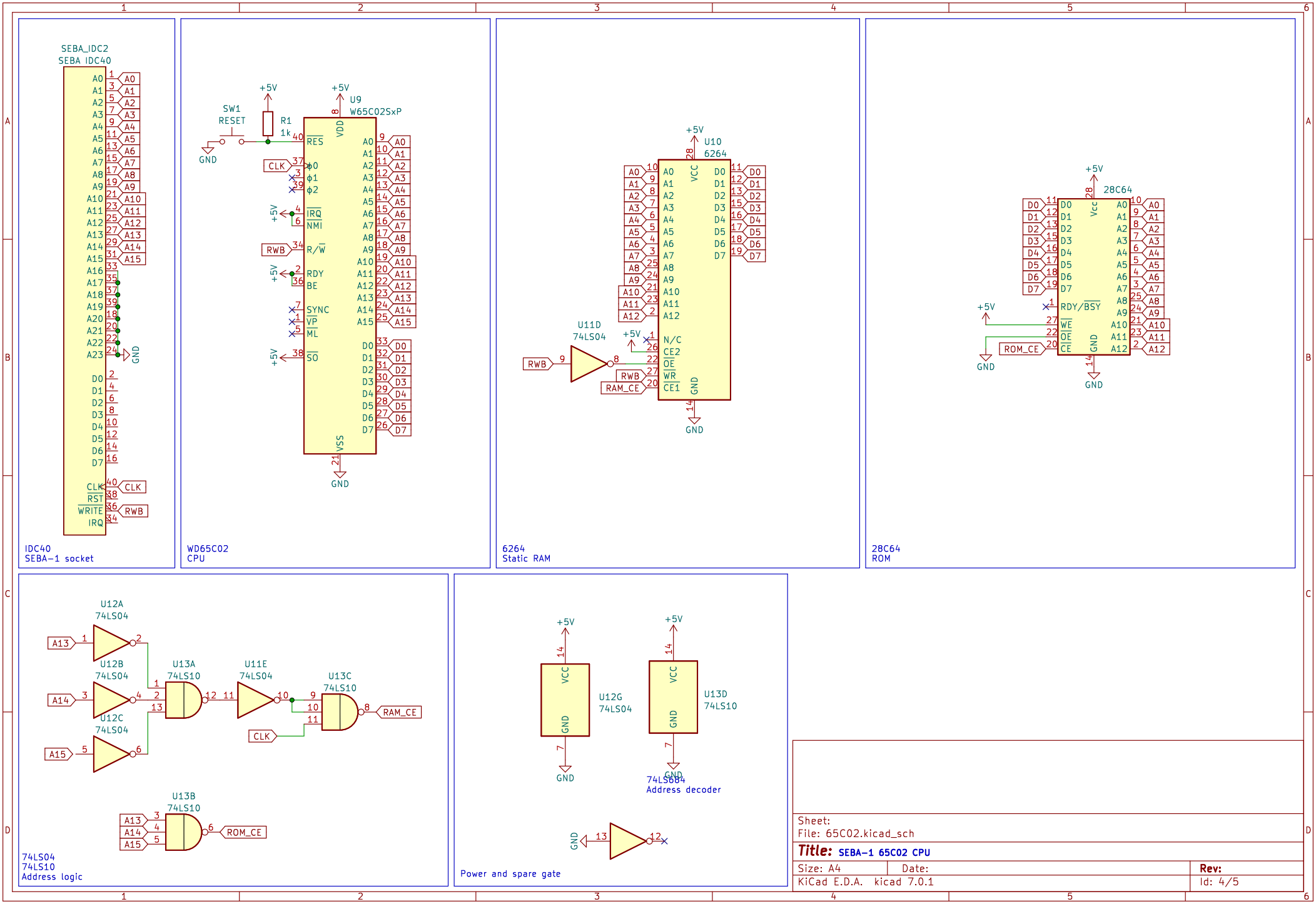
SEBA_IDC2
SEBA IDC40
IDC40
SEBA-1 socket
SW1
RESET
R1
1k
U9
W65C02SxP
WD65C02
CPU
U10
6264
6264
Static RAM
U11D
74LS04
28C64
28C64
ROM
U12A
74LS04
U12B
74LS04
U12C
74LS04
U13A
74LS10
U11E
74LS04
U13C
74LS10
U13B
74LS10
74LS04
74LS10
Address logic
U12G
74LS04
U13D
74LS10
74LS684
Address decoder
Power and spare gate
Sheet:
File: 65C02.kicad_sch
Title: SEBA-1 65C02 CPU
Size: A4
Date:
Rev:
KiCad E.D.A. kicad 7.0.1
Id: 4/5