

## *Vlsi Objective Questions With Answers*

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VLSI DESIGN \_ Due to absence of bulks transistor structures are denser 2 MARK QUESTIONS &  
ANSWERS than bulk silicon. 1.What are four generations of Integration Circuits?

### **VLSI Design- Questions with Answers for Electronics / VLSI ...**

Multiple Choice Questions and Answers on VLSI Design & Technology Multiple Choice Questions and  
Answers By Sasmita January 13, 2017 1) The utilization of CAD tools for drawing timing waveform  
diagram and transforming it into a network of logic gates is known as \_\_\_\_\_.

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delay through the chain of inverters. Lets assume the input ...

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Digital system design and micro-electronics. The Evolution of VLSI, MOS transistor theory, MOS  
structure, enhancement &

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Physical Design Objective Type of Questions and Answers. 1) Chip utilization depends on \_\_\_\_\_. a.  
Only on standard cells ... Your articles can reach hundreds of VLSI professionals. Send your articles,  
thesis, research papers to: ... Physical Design Objective Type of Questions and An... Dynamic and  
Internal Power

### **Physical Design Objective Type of Questions and Answers**

1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is

denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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