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VLSI interview questions answered.

1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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VLSI Design- Questions with Answers for Electronics / VLSI ...

EC2354-VLSI DESIGN 2 MARK QUESTIONS & ANSWERS 1.What are four generations of Integration Circuits? SSI(Small Scale Integration) MSI(Medium Scale Integration) LSI(Large Scale Integration) VLSI(Very Large Scale Integration) 2.Give the advantages of IC? Size is less High Speed Less Power Dissipation 3.Give the variety of Integrated Circuits? More Specialized Circuits

EC2354-VLSI DESIGN 2 MARK QUESTIONS & ANSWERS

Multiple Choice Questions and Answers on VLSI Design & Technology Multiple Choice Questions and Answers By Sasmita January 13, 2017 1) The utilization of CAD tools for drawing timing waveform diagram and transforming it into a network of logic gates is known as _____.

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FLOORPLANING INTERVIEW QUESTIONS AND ANSWERS

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