

RECAP JOINS

Joins are implicit in a business question

Business question

Counts the number of orders in a given quarter of a given year in which at least one lineitem was received by the customer later than its committed date. The guery lists the count of such orders for each order priority sorted in ascending priority order

SQL

Database Operators

aggregate

predicate (filter)

join
predicate (filter)

aggregate

sort



TPC-H SCHEMA part (p_) PARTKEY NAME order (o_) lineitem (l_) **MFGR** ORDERKEY ORDERKEY CATEGORY LINENUMBER CUSTKEY BRAND PARTKEY ORDERDATE SUPPKEY ORDPRIORITY COMMITDATE ORDERSTATUS supplier (s_) RECEIPTDATE SUPPKEY NAME ADDRESS nation (n_) CITY NATIONKEY NATIONKEY NAME

customer (c_)

CUSTKEY

NAME

ADDRESS

CITY

••

RELATIONAL JOIN

Lineitem¹ Order² o_orderpriority l_orderkey o_orderkey 23 11 14 23 27 56 11 29 39 27 Primary Key **Payload** 23 Foreign Key

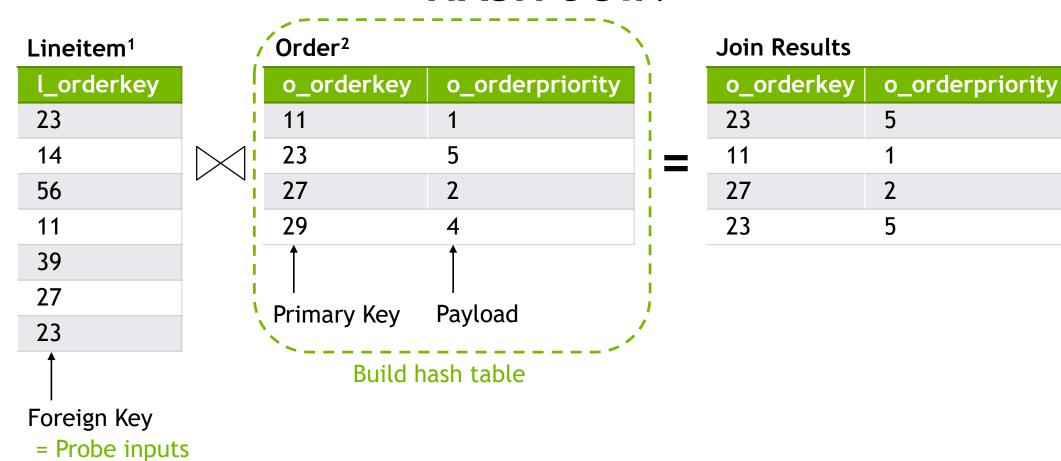
Join Results

o_orderkey	o_orderpriority
23	5
11	1
27	2
23	5

¹ after applying predicate "l_commitdate < l_receiptdate"

² after applying predicates "o_orderdate >= date '[DATE]' and o_orderdate < date '[DATE]' + interval '3' month"

HASH JOIN

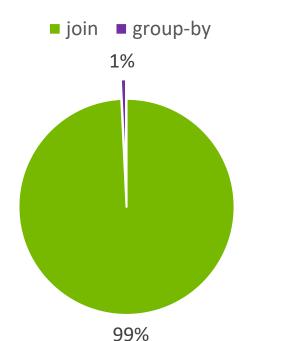


¹ after applying predicate "l_commitdate < l_receiptdate"

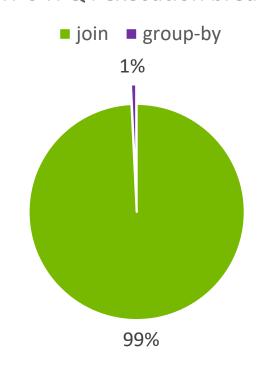
² after applying predicates "o_orderdate >= date '[DATE]' and o_orderdate < date '[DATE]' + interval '3' month"

JOINS & E2E PERFORMANCE

CPU TPC-H Q4 execution breakdown



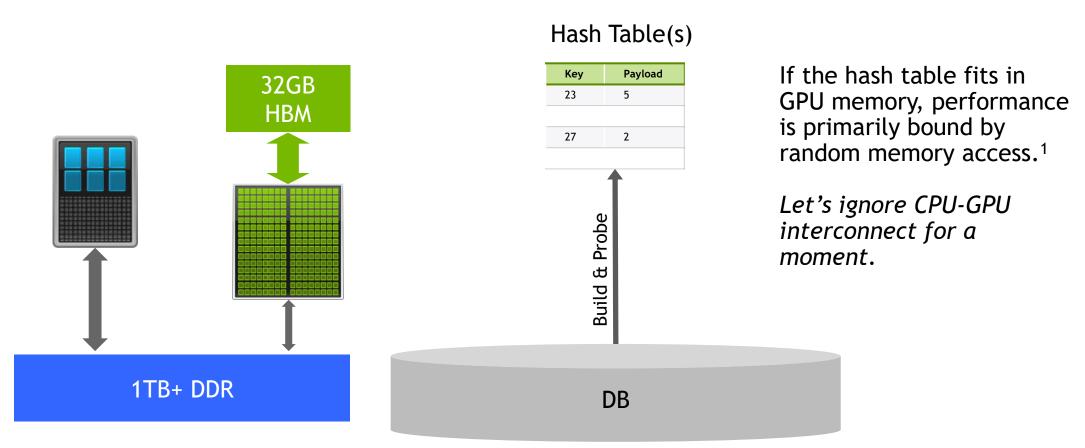
GPU TPC-H Q4 execution breakdown



18/22 TPC-H Queries involve Joins and are the longest running ones¹

IMPLEMENTING GPU JOINS

In Heterogeneous Systems



PERFORMANCE

	Peak memory bandwidth ¹	Random 8B access ¹	
High-end CPU (6-channel DDR4)	120 GB/s	6GB/s	10x
NVIDIA Tesla V100	900 GB/s	60GB/s	

PERFORMANCE VS. CAPACITY

	Peak memory bandwidth¹	Random 8B access ¹	Memory capacity	
High-end CPU (6-channel DDR4)	120 GB/s	6GB/s	1 TB+	1/32
NVIDIA Tesla V100	900 GB/s	60GB/s	32GB	

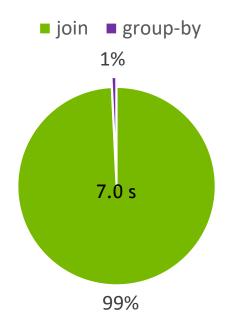
PERFORMANCE VS. CAPACITY

	Peak memory bandwidth ¹	Random 8B access ¹	Memory capacity	
High-end CPU (6-channel DDR4)	120 GB/s	6GB/s	1 TB+	
NVIDIA Tesla V100	900 GB/s	60GB/s	32GB	1/2
NVIDIA DGX-2 (16x V100)	16 x 900 GB/s	16x 60GB/s	512 GB	

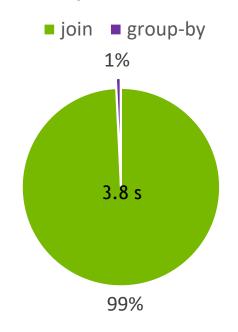
IS A SINGLE V100 FAST/LARGE ENOUGH?

TPC-H query 4 @SF1000 = 1000GB data warehouse

GPU execution breakdown



GPU execution breakdown, compressed data

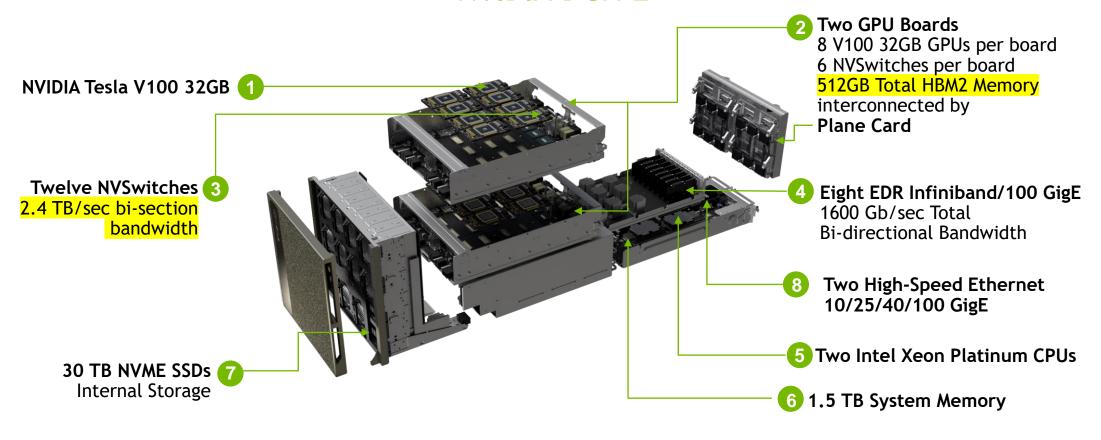


Hash table sizes

Query	SF1K	SF3K	SF10K
Q4	1.5 GB	4.5 GB	15 GB
Q18	21 GB	63 GB	210 GB
Q21	10.5 GB	31.5 GB	105 GB

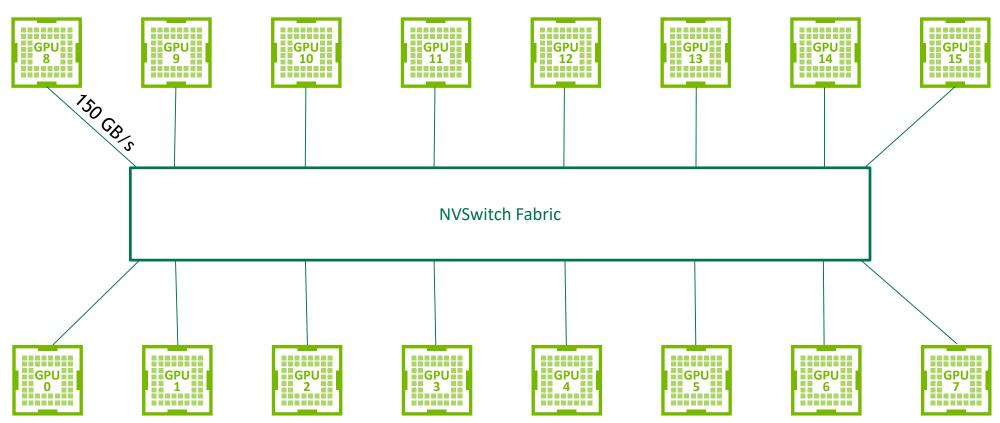
DESIGNED TO TRAIN THE PREVIOUSLY IMPOSSIBLE

NVIDIA DGX-2



POTENTIAL DGX-2 IMPLEMENTATION

Use 2.4TB/s bisection BW to exchange FT chunks





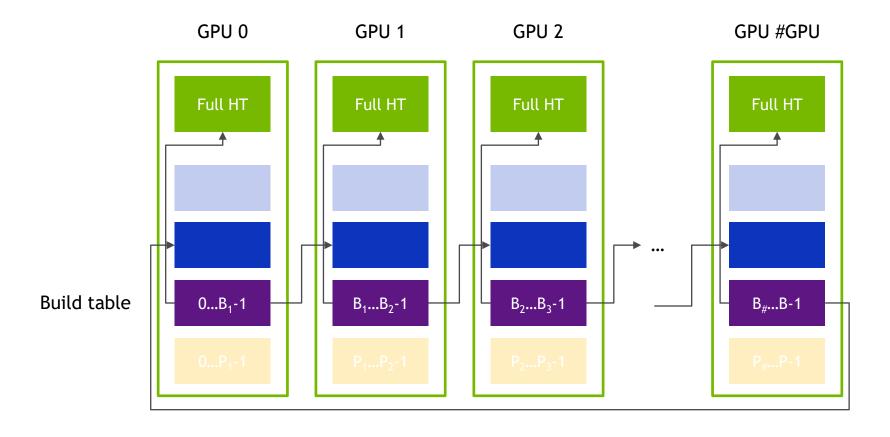
DISCLAIMER

This investigation is ongoing

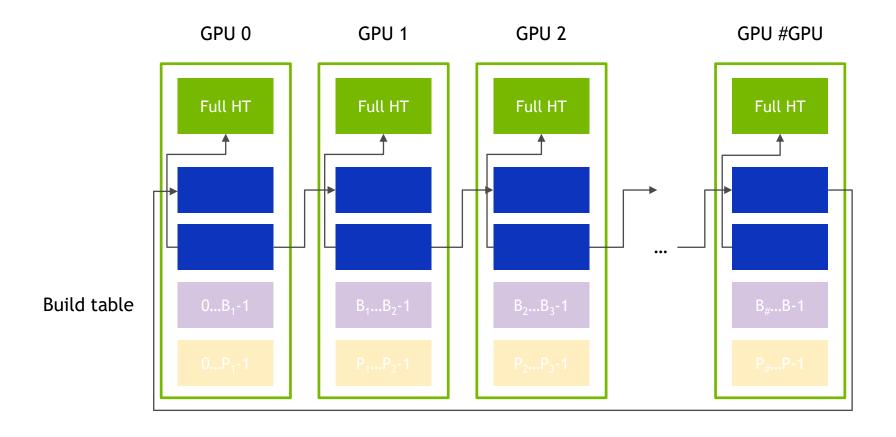
For a production system some additional aspects need to be considered:

- Data Skew
- Cardinality estimation
- Query optimizer

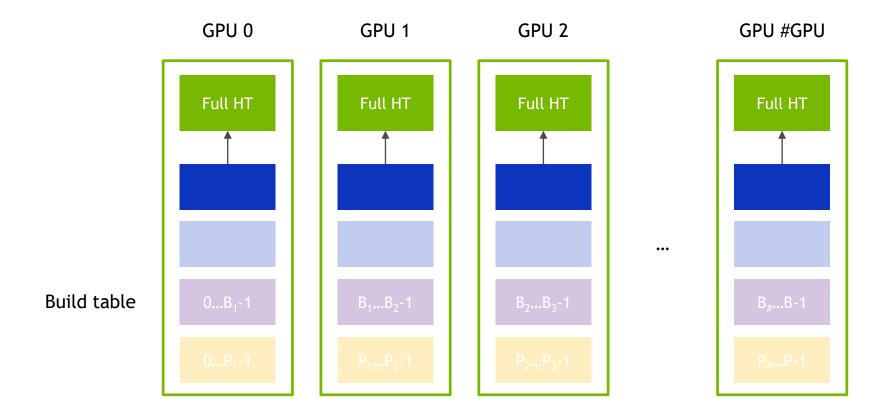
redundant build of replicated HT (step 0)



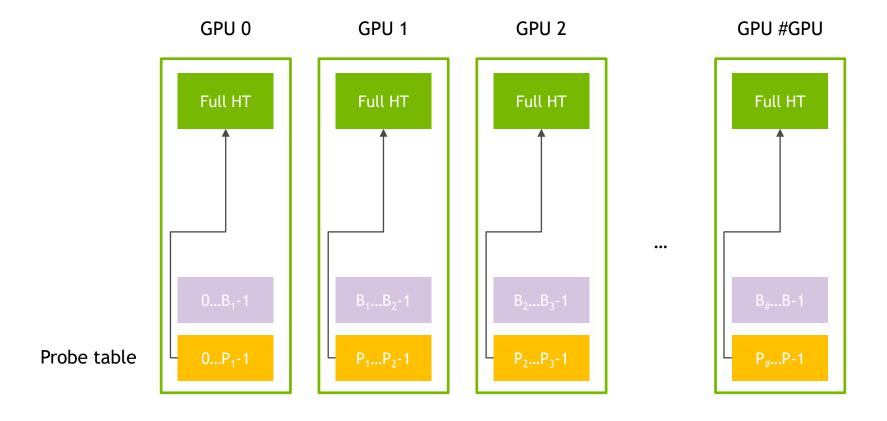
redundant build of replicated HT (step 1..#GPU-1)



redundant build of replicated HT (step #GPU)



parallel probe of replicated HT



Benchmark Problem

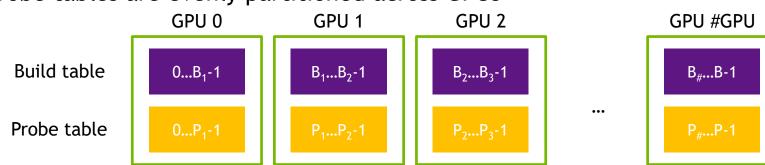
randomly generated 8 bytes keys

build table size = probe table size = 335544320 rows (worst case for HT creation fitting in the memory of a single GPU: 2x 2.5GiB for tables, 2x10GiB for HT + staging buffers (for strong scaling experiment))

HT occupancy = 50%

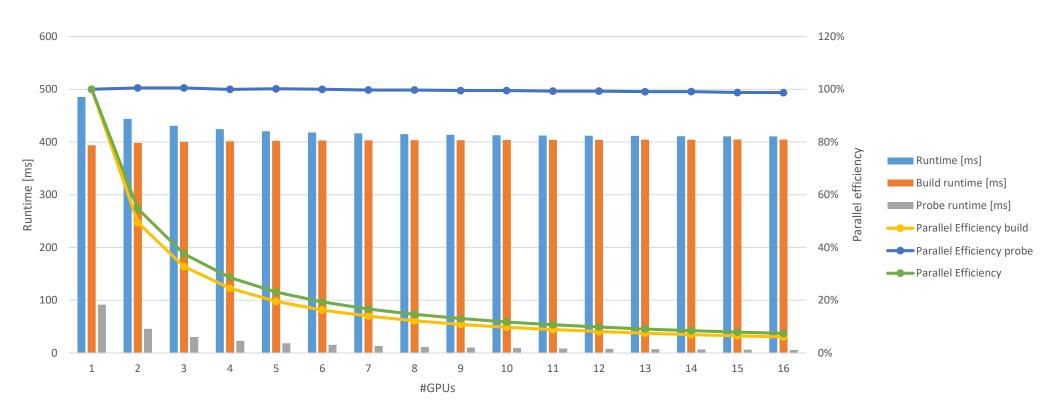
selectivity = 0 for analytical purposes we will look at a real problem later

build and probe tables are evenly partitioned across GPUs



SCALING OF INNER JOIN ON DGX-2

with redundant build of replicated HT

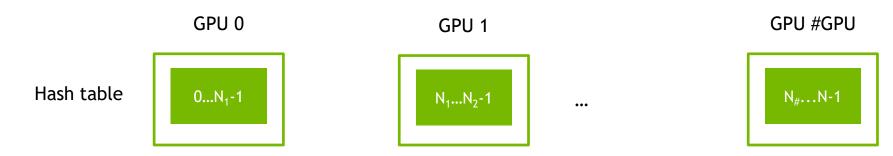




Basic Idea

Open addressing hash table with N buckets

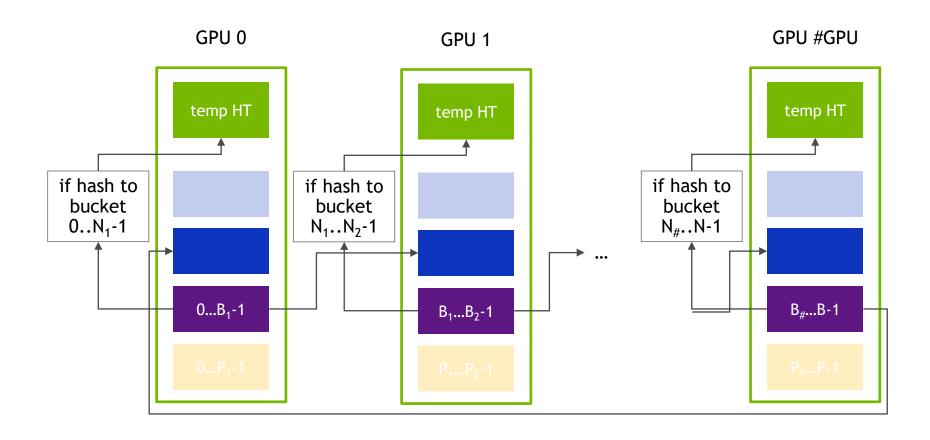
Partition N hash table buckets equally onto GPUs:



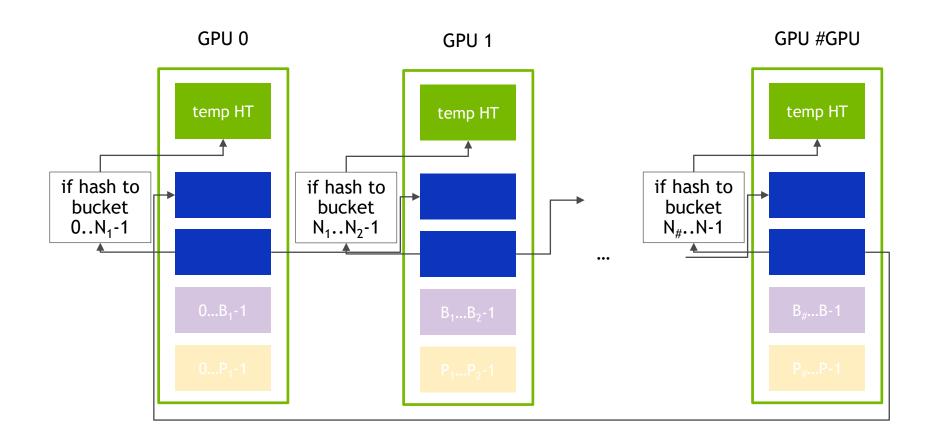
The bucket_idx and target HT partition can be computed locally from the key



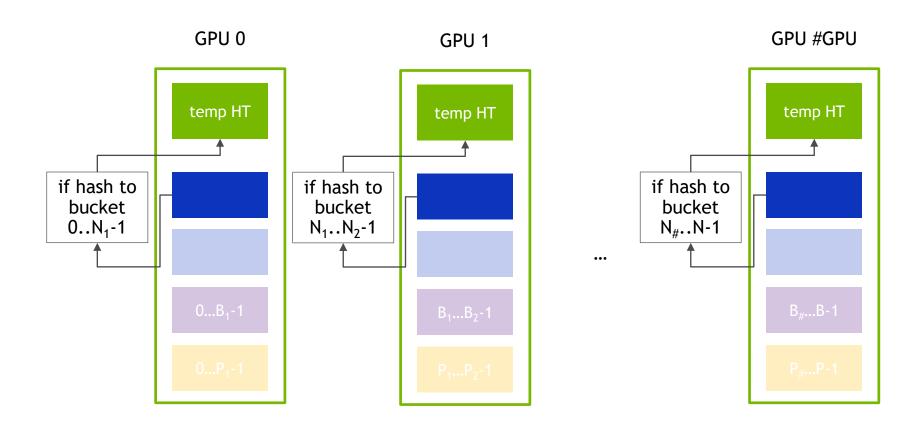
parallel build of a replicated HT (step 0 of phase 1)



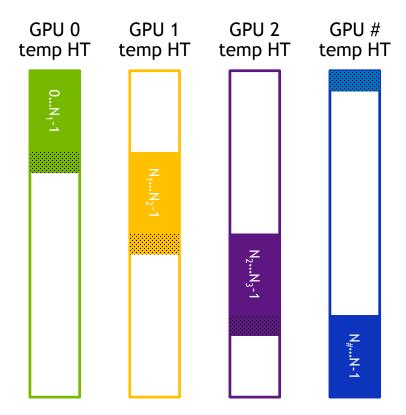
parallel build of a replicated HT (step 1..#GPU-1 of phase 1)



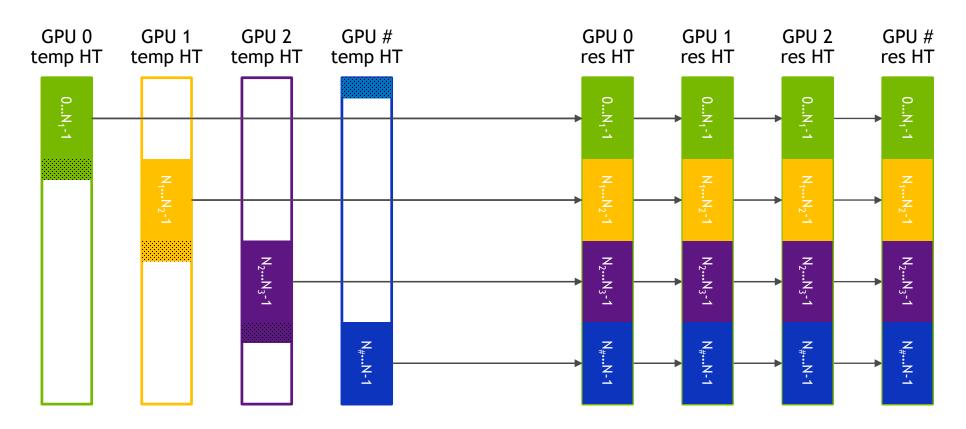
parallel build of a replicated HT (step #GPU of phase 1)



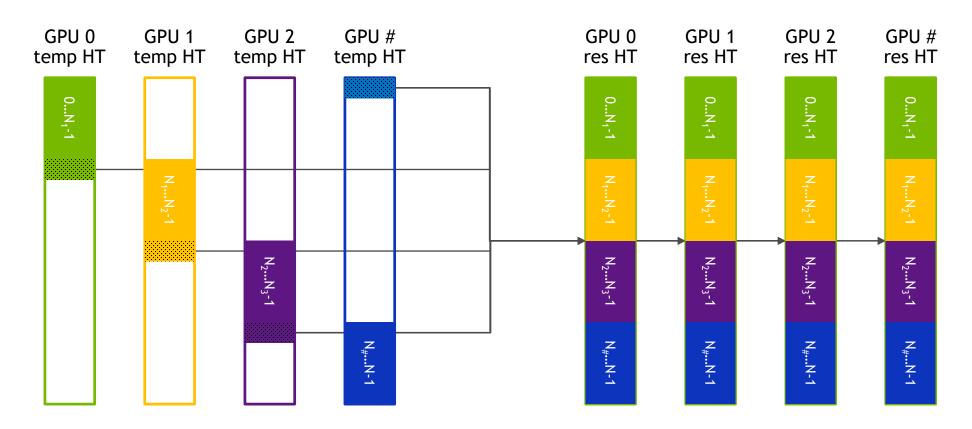
parallel build of a replicated HT (phase 2 - merge step)



parallel build of a replicated HT (phase 2 - merge step)

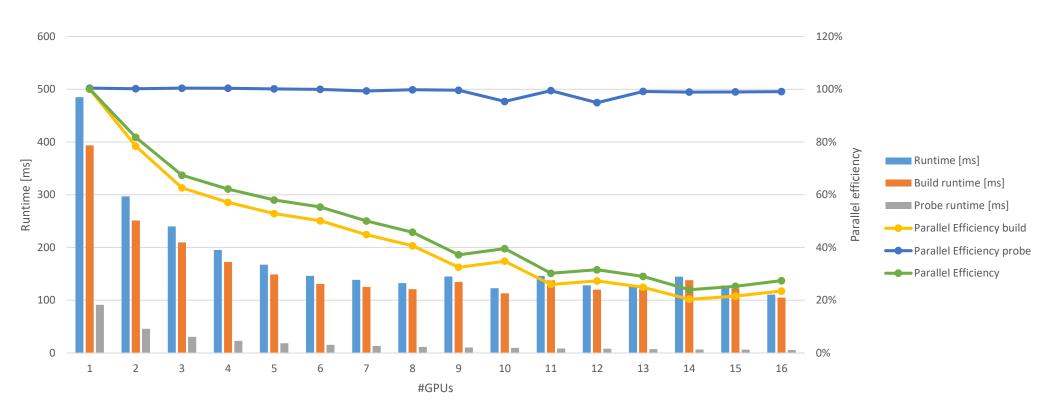


parallel build of a replicated HT (phase 2 - merge step)



SCALING OF INNER JOIN ON DGX-2

with parallel build of replicated HT



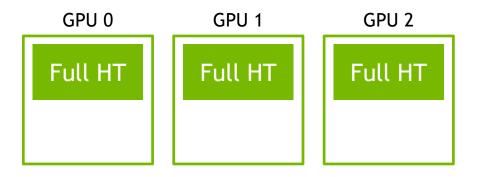


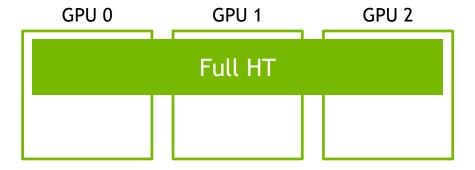
SCALING OF INNER JOIN ON DGX-2

with parallel build of replicated HT



parallel build of partitioned HT and parallel probe





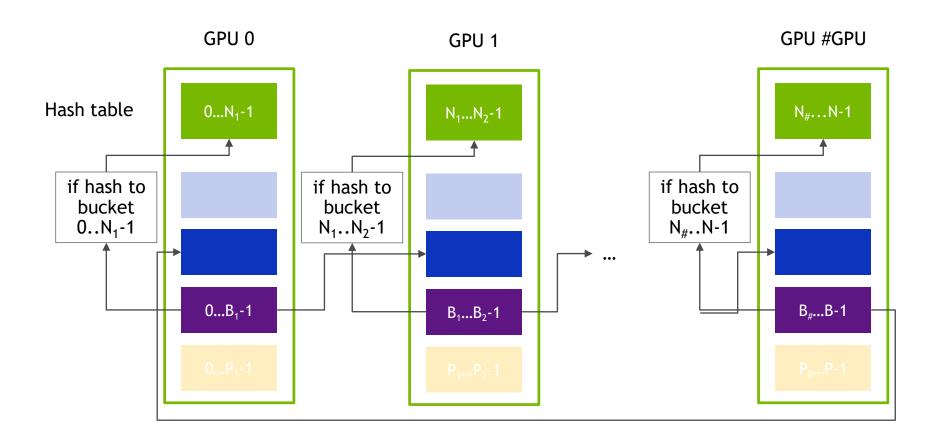
Replicated:

- Limited capacity
- Slower building
 - Need to merge HT partitions
- Faster probing
 - No inter-GPU traffic

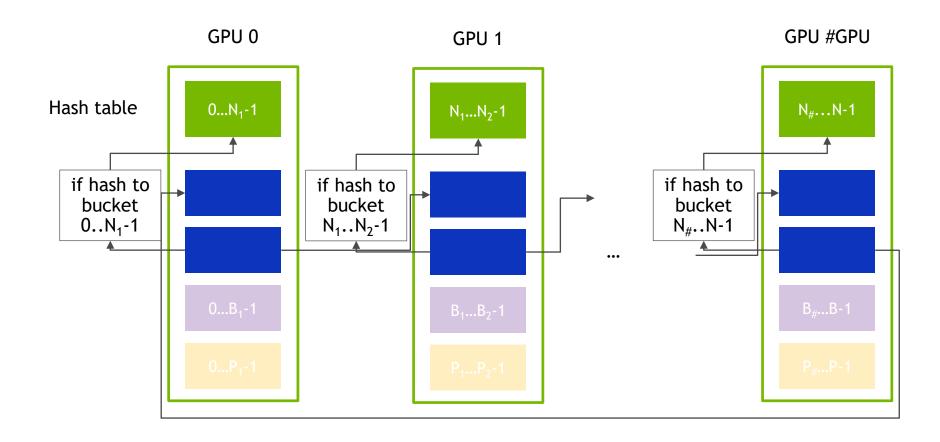
Partitioned:

- High capacity
- Faster building
 - No need to merge partitions
- Slower probing
 - Need to access remote partitions

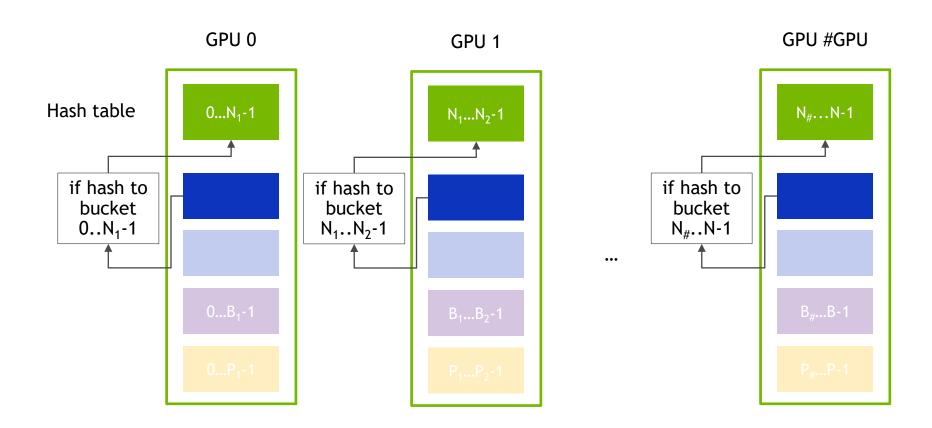
parallel build of a partitioned HT (step 0)



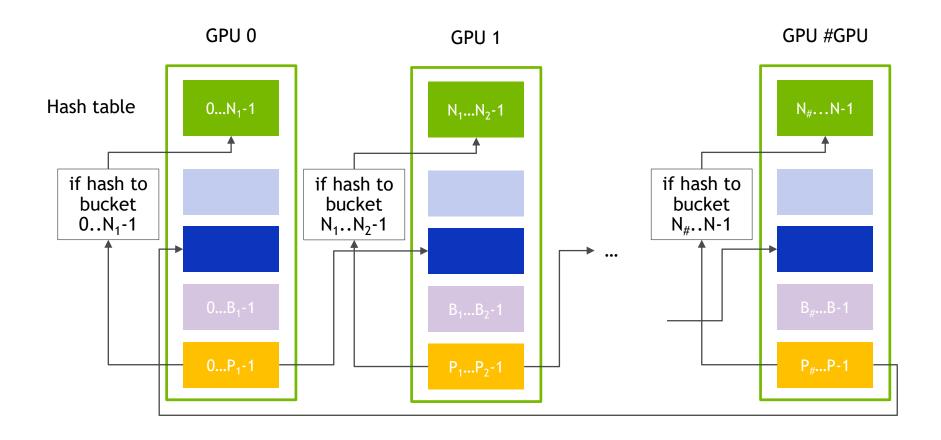
parallel build of a partitioned HT (step 1..#GPU-1)



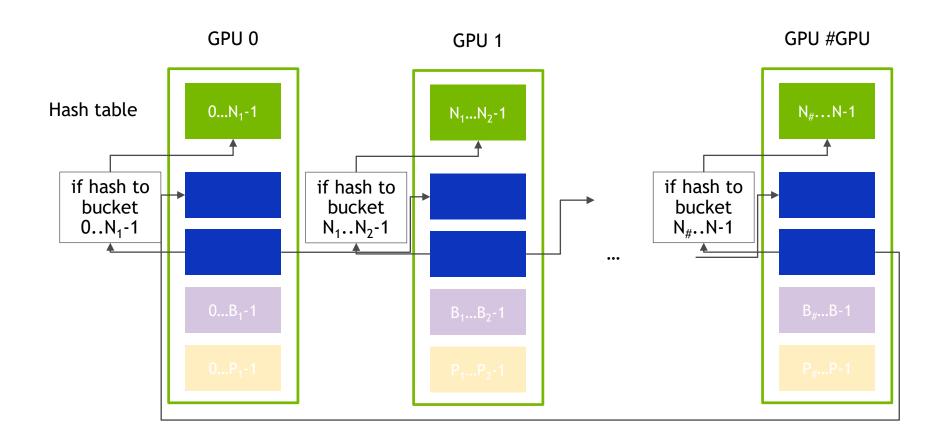
parallel build of a partitioned HT (ring exchange) (step #GPU)



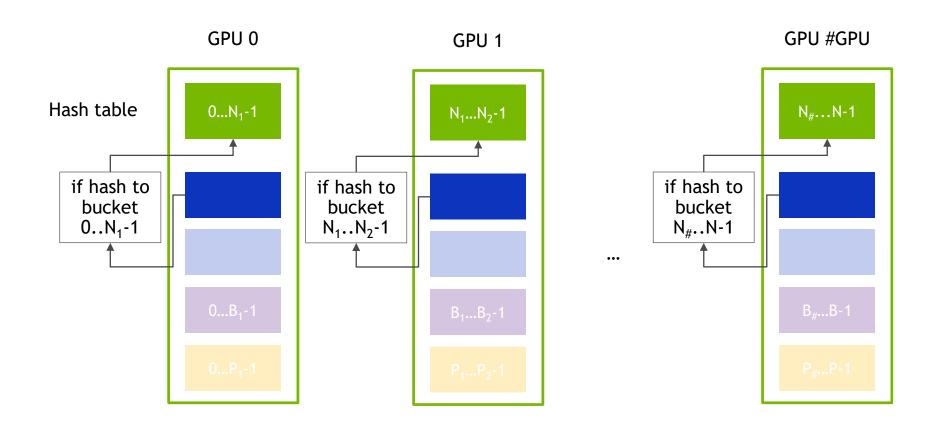
parallel probe of a partitioned HT (ring exchange) (step 0)



parallel probe of a partitioned HT (ring exchange) (step 1..#GPU-1)

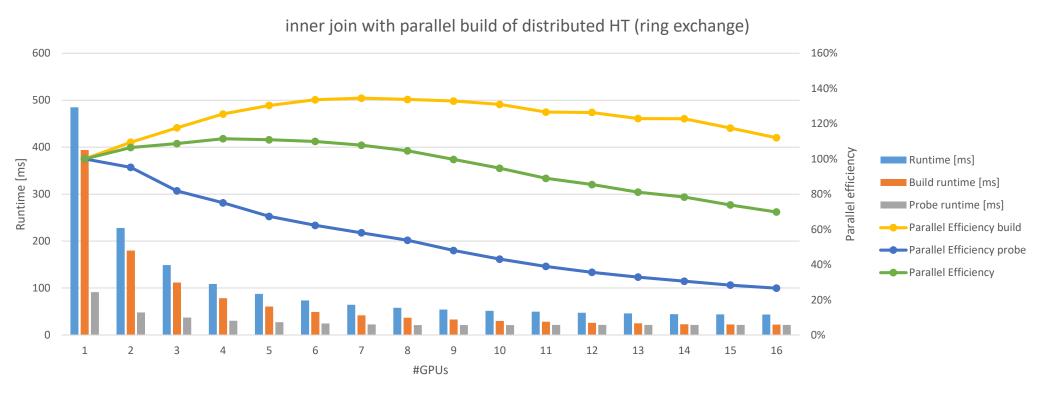


parallel probe of a partitioned HT (ring exchange) (step #GPU)



SCALING OF INNER JOIN ON DGX-2

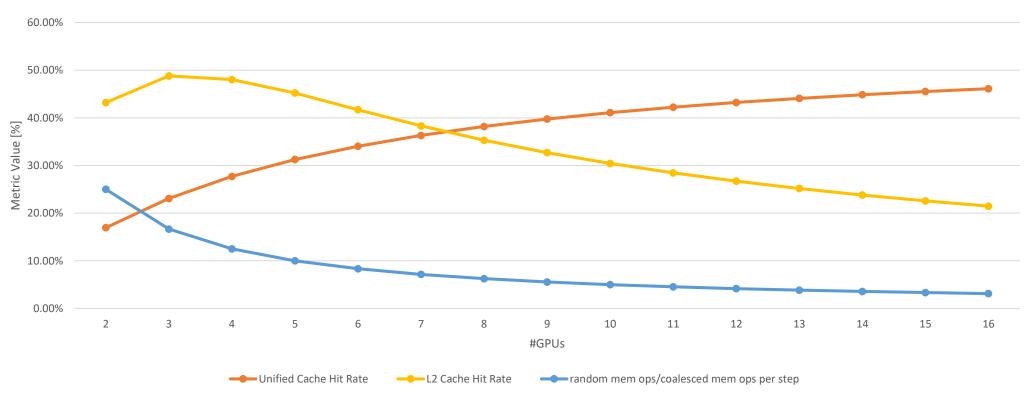
parallel build of partitioned HT and parallel probe (ring exchange)



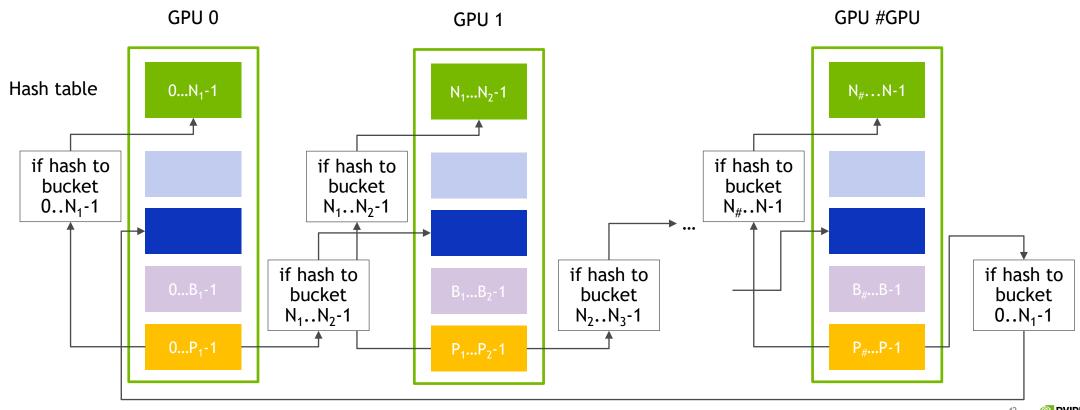


SCALING OF INNER JOIN ON DGX-2

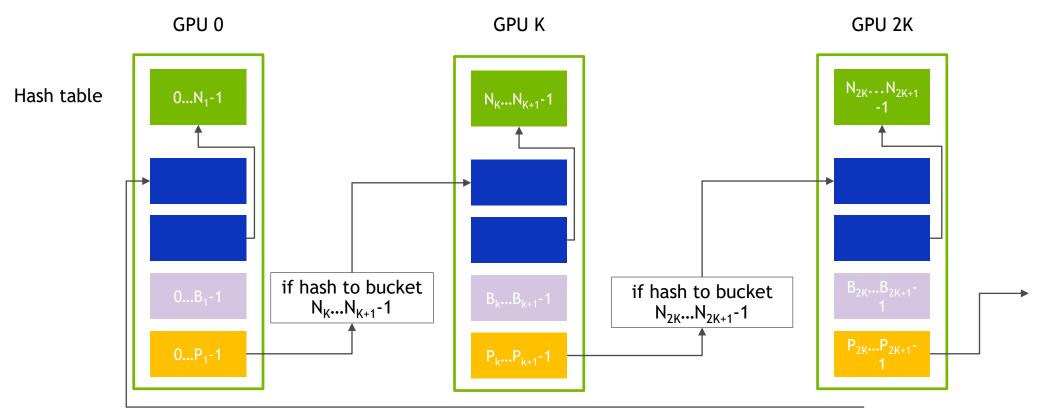
parallel build of partitioned HT - Memory Subsystem Metrics



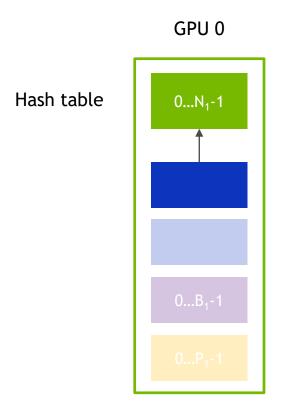
parallel probe of a partitioned HT (staged direct send) (round 0)

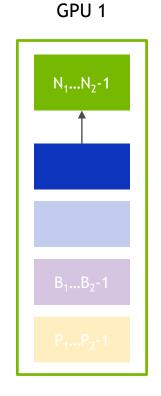


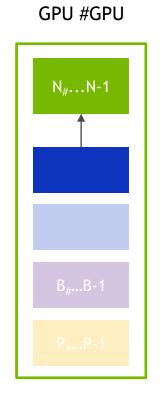
parallel probe of a partitioned HT (staged direct send) (round (k-1))



parallel probe of a partitioned HT (staged direct send) (round #GPU)

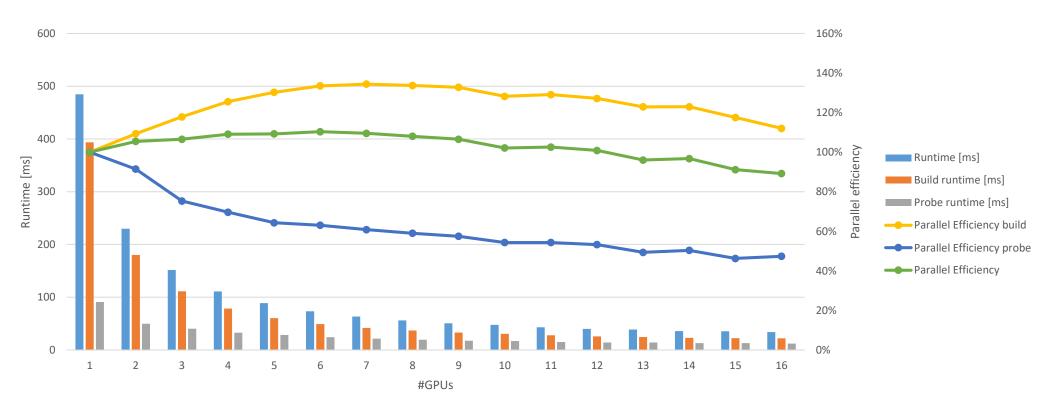






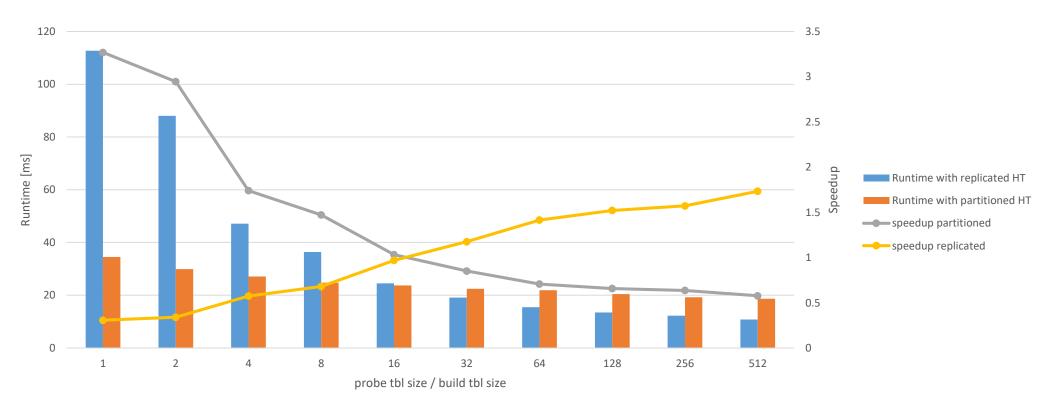
SCALING OF INNER JOIN ON DGX-2

parallel build of partitioned HT and parallel probe (staged direct send)



SCALING OF INNER JOIN ON DGX-2

replicated HT vs. partitioned HT (16 GPUs, total # rows = 671088640)



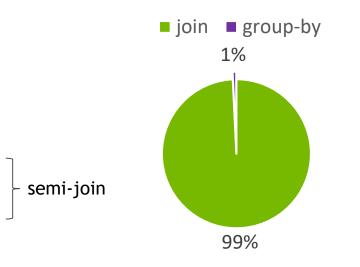


TPC-H BENCHMARK

SQL code for TPC-H Query 4:

```
select
 o orderpriority,
  count (o orderkey) as order count,
from
  orders
where
  o orderdate >= date '[DATE]' and
  o orderdate < date '[DATE]' + interval '3' month and
  exists (select * from lineitem
          where 1 orderkey = o orderkey and
                l commitdate < l receiptdate)</pre>
group by
  o orderpriority,
order by
  o orderpriority;
```

CPU execution breakdown



Q4: INPUT DATA

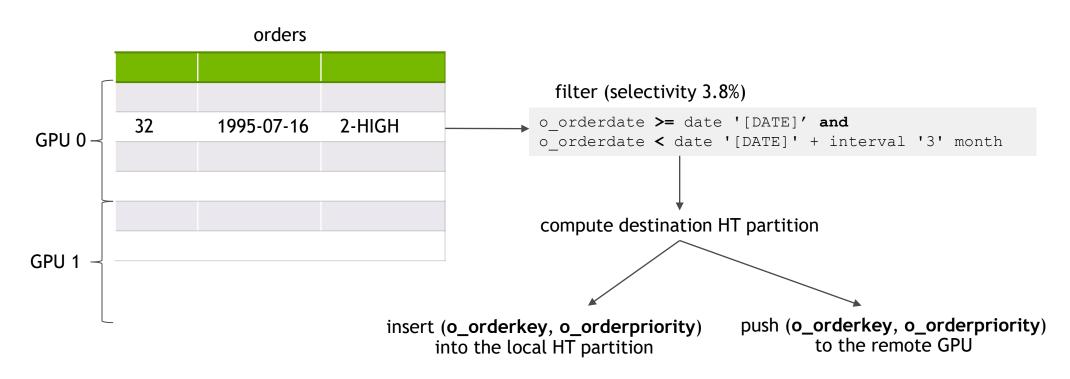
1.5M rows per SF

o_orderkey	o_orderdate	o_orderpriority
7	1996-01-10	2-HIGH
32	1995-07-16	2-HIGH
33	1993-10-27	3-MEDIUM
34	1998-07-21	3-MEDIUM

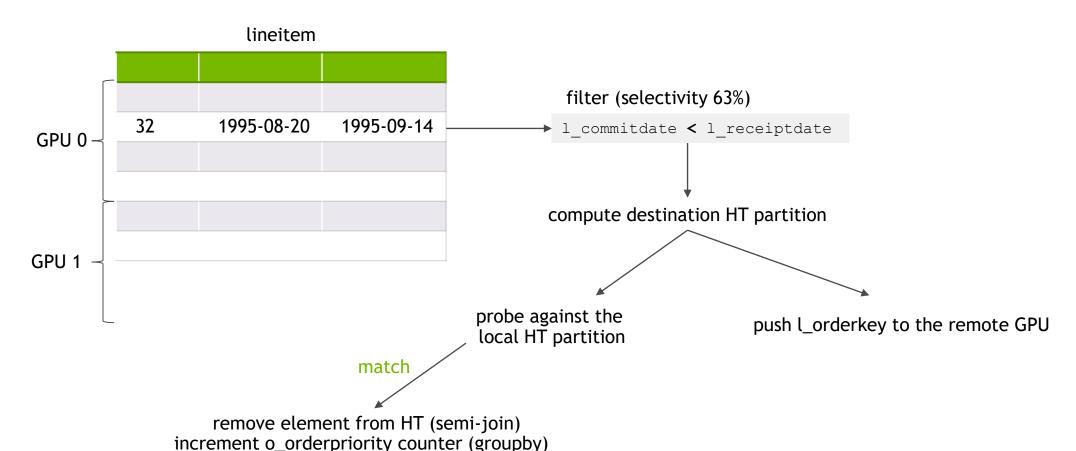
6M rows per SF

l_orderkey	l_commitdate	l_receiptdate
7		
7		
7		
32	1995-10-07	> 1995-08-27
32	1995-08-20	< 1995-09-14
32	1995-10-01	> 1995-09-03
34		
34		

Q4 JOIN: BUILD



Q4 JOIN: PROBE



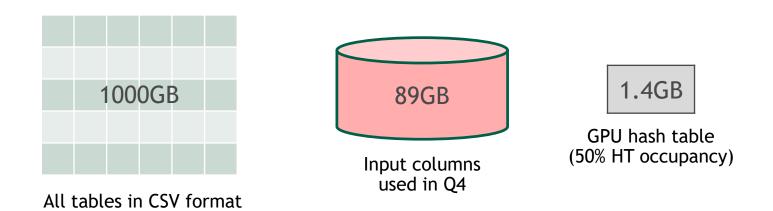
ON INVIDIA

TEST SETUP

TPC-H Q4 SF1000

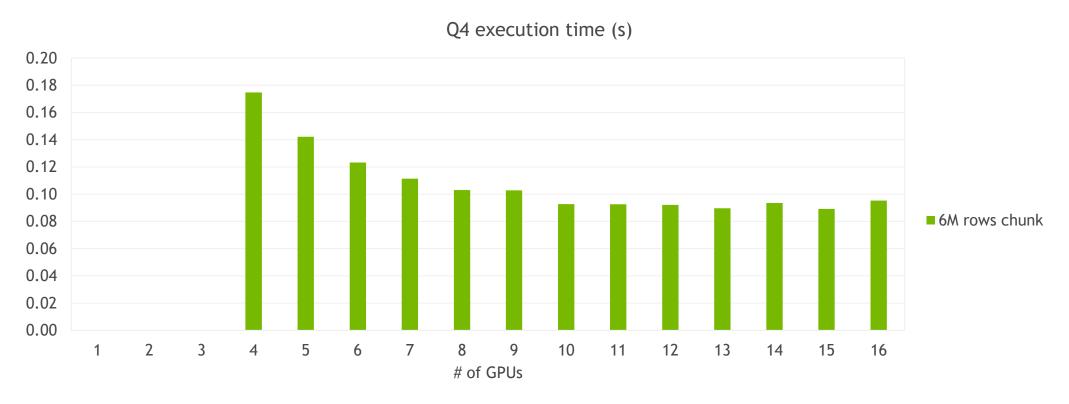
Performance metrics: time, parallel efficiency, throughput (input data size / time)

Use 8B keys, 2B encoded dates, 1B encoded priority string



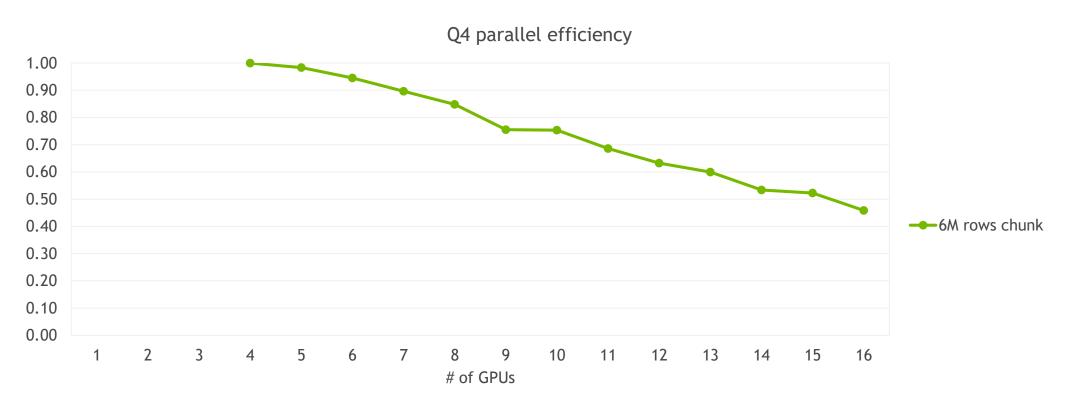
PERFORMANCE RESULTS ON DGX-2

Q4 SF1000, input distributed in GPU memory



PERFORMANCE RESULTS ON DGX-2

Q4 SF1000, input distributed in GPU memory

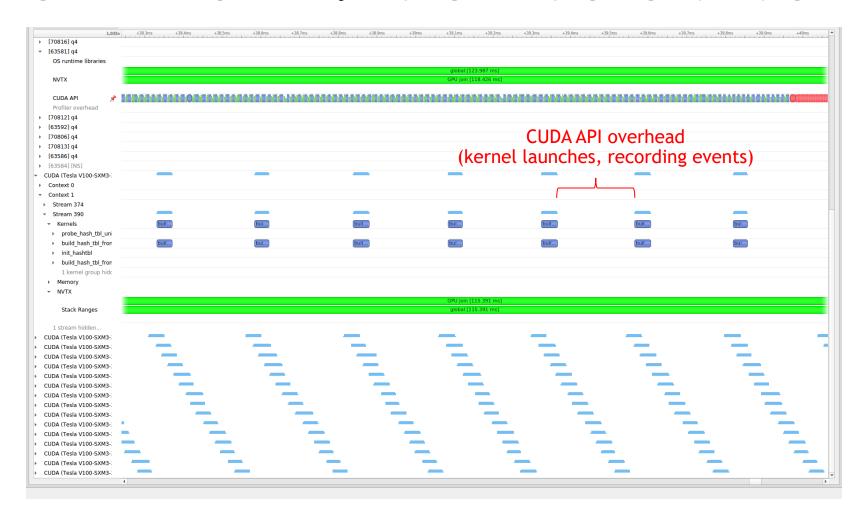


DGX-2 PROFILE: INPUT IN GPU MEMORY



the main bottleneck is HT build (74% of the overall query time)

DGX-2 PROFILE: INPUT IN GPU MEMORY



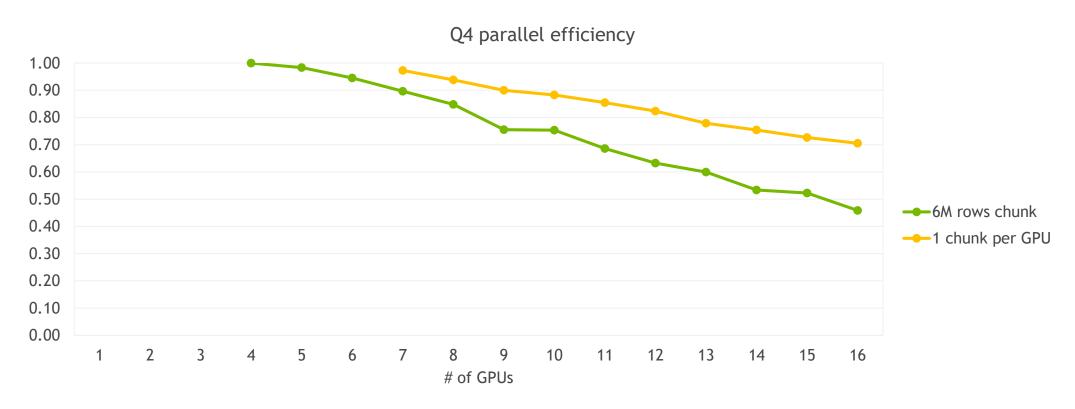
OPTIMIZED CHUNK SIZE ON DGX-2

Q4 SF1000, input distributed in GPU memory



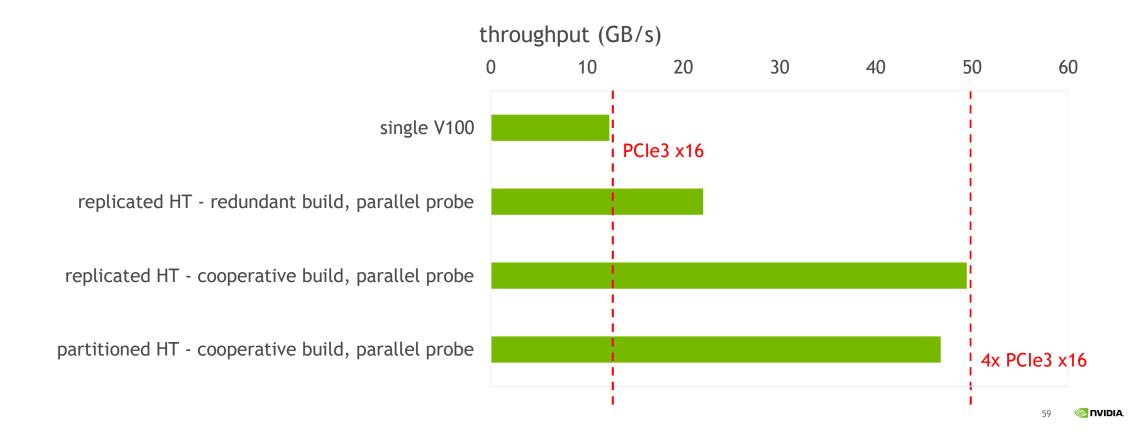
OPTIMIZED CHUNK SIZE ON DGX-2

Q4 SF1000, input distributed in GPU memory

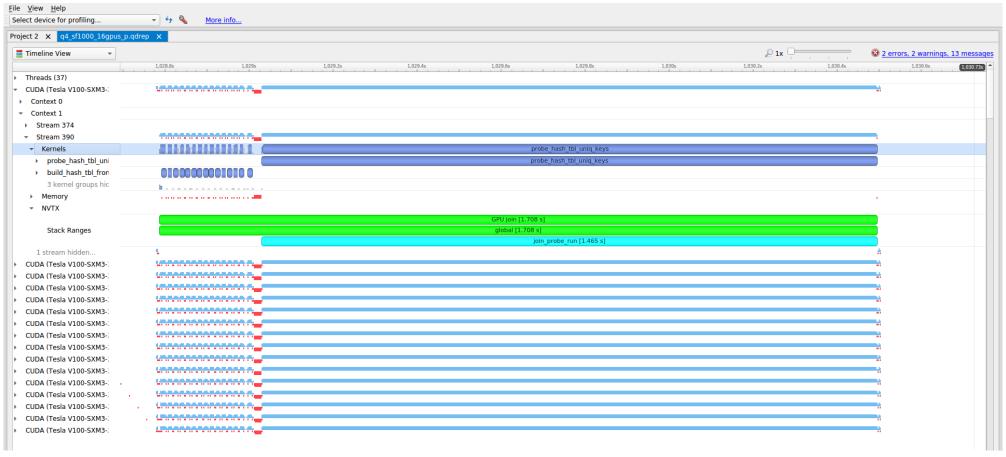


PERFORMANCE RESULTS ON DGX-2

Q4 SF1000, input in system memory



DGX-2 PROFILE: INPUT IN CPU MEMORY



2B	2B
l_commitdate	l_receiptdate
1995-10-07	1995-08-27
1995-08-20	1995-09-14
1995-10-01	1995-09-03
	1995-10-07 1995-08-20

l_orderkey	l_commitdate	l_receiptdate
7		
7		
7		
32	1995-10-07	1995-08-27
32	1995-08-20	1995-09-14
32	1995-10-01	1995-09-03
34		
34		

8B	2B	2B
l_orderkey	l_commitdate	l_receiptdate
7		
7		
7		
32	1995-10-07	1995-08-27
32	1995-08-20	1995-09-14
32	1995-10-01	1995-09-03
34		
34		

can be compressed to <8B per key

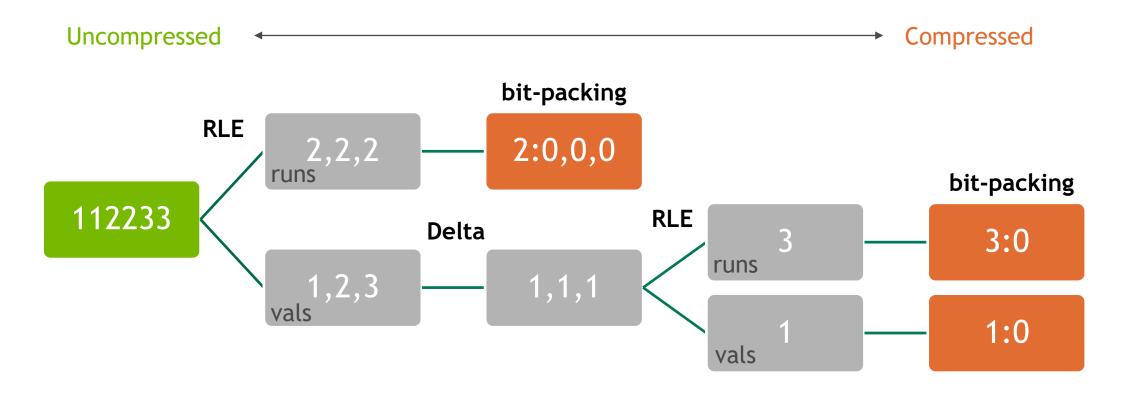
8B	2B	2B
l_orderkey	l_commitdate	l_receiptdate
7		
7		
7		
32	1995-10-07	1995-08-27
32	1995-08-20	1995-09-14
32	1995-10-01	1995-09-03
34		
34		

can be compressed to <2B per date

8B	2B	<u>2B</u>
l_orderkey	l_commitdate	l_receiptdate
7		
7		
7		
32	1995-10-07	1995-08-27
32	1995-08-20	1995-09-14
32	1995-10-01	1995-09-03
34		
34		

can be compressed to <2B per date

RLE-DELTA-RLE COMPRESSION



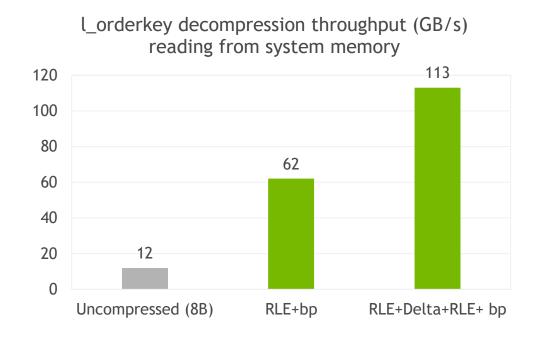
APPLYING COMPRESSION TO TPC-H Q4

Use RLE + Delta + RLE + bit-packing

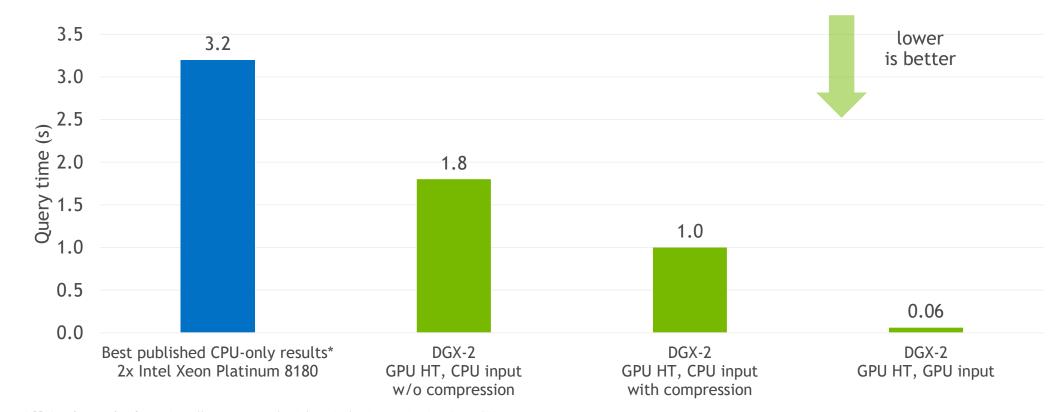
Compression rate for SF1K l_orderkey: 14x

Multiple streams per GPU

Pipeline decompress & probe kernels

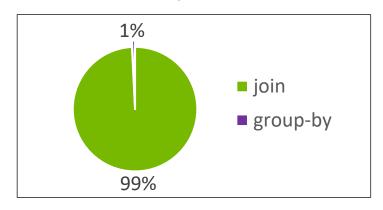


TPC-H SF1000 Q4 RESULTS

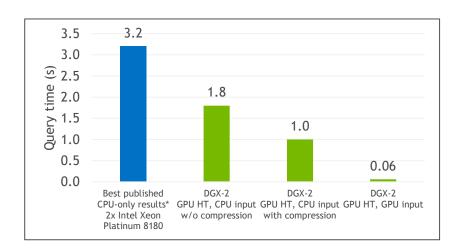


TAKEAWAY

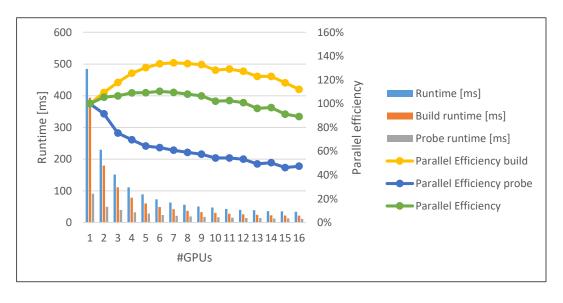
1. Joins is the key bottleneck in OLAP



3. Speed-ups on real analytical queries



2. Multi-GPU joins improve perf and enable larger workloads



DGX-2 can run TPC-H Q4 SF1K in 1 second! (input data in system memory)

If columns preloaded to GPU memory Q4 time goes down to just 60ms

