

5) ζ_{1korr} der ideale

$$V_0 = V_L - V_n \text{ '0'}$$

• $V_1 = 12V$ $V_L = 5V$

$V_0 = 5 - 12 = -7V$

• $V_1 = -3V$ $V_L = 5V$

$V_0 = 8V$

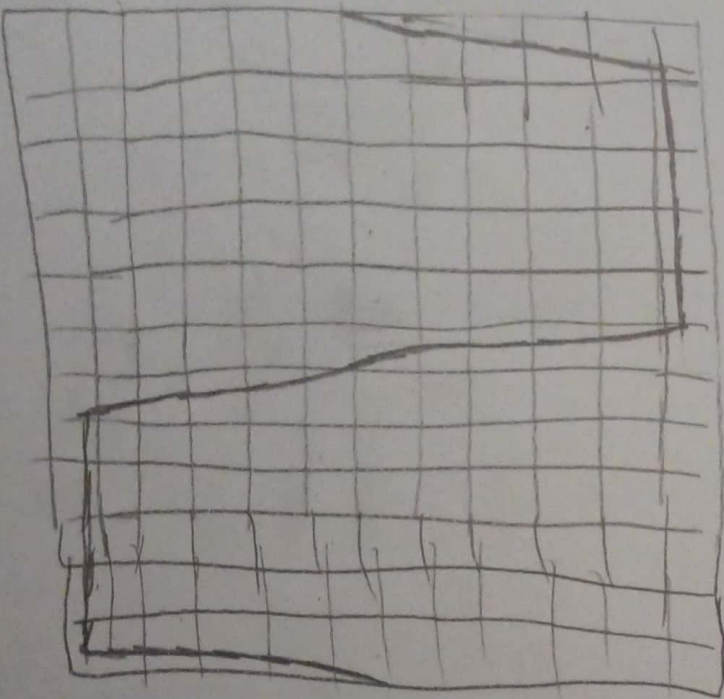
• $V_1 = 4V$ $V_L = -5V$

$V_0 = -9V$

6) ζ_{1korr} der ideale input block ablesbar

ζ_{1korr} der ideale output block ablesbar

ζ_{1korr} der ideale	$V_1(V)$	$V_L(V)$	$V_0(V)$
12	5	-7	
-3	8	5	
4	-5	-9	



Volt/div
3