RTL Micro Operations

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Instruction	Sequence of Operations						
ADD R1, R2, R3	R1 <= R2 + R3						
ADDI R3, #25	R3 <= R3 + 25						
LD R2, 10(R6)	R2 <= Mem[R6+10]						
ST R2, -2(R1)	Mem[R1-2] <= R2						
BMI R5, #-10	PC <= PC - 10 if (R5 < 0)						
MOVE R1, R5	R1 = R5						
CALL #1000	SP <= SP - 4 Mem[SP] <= PC + 4 PC <= PC + 1000						
RET	PC = Mem[PC] SP <= SP + 4						

	regDst	branch	memRead	memToReg	aluOp	memWrite	aluSrc	addrOp	writeDataOp	рсОр	aOp2	spOp	regWrite
ADD	1	0	0	0	0000	0	0	0	0	0	1	xxxx	1
ADDI	0	0	0	0	0000	0	1	0	0	0	1	xxxx	1
LD	0	0	1	1	0000	0	1	0	0	0	1	xxxx	1
ST	х	0	0	х	0000	1	1	0	0	0	1	xxxx	0
ВМІ	х	1	0	х	xxxx	0	0	0	0	0	1	xxxx	0
MOVE	0	0	×	0	0000	0	1	0	0	0	1	xxxx	1
CALL	1	0	×	х	xxxx	1	х	1	1	0	1	0001	0
RET	х	0	1	1	xxxx	0	х	0	0	1	0	0000	0