Assignment 3.1 - Basics of Logic Design 09/10/2024







Anonymous Grading: no

Unlimited Attempts Allowed

07/10/2024

∨ Details

Given the exercises below, Build the Truth Table and Draw the circuit representation.

- 1.X = A + BC
- 2.X = A(B+D)
- 3.X = W(V+Y)Z
- 4.X = PT(P+Z)
- 5.X = (A+B)(A+C)
- 6.X = (A + B)C
- 7.X = A + BC + D
- 8. X= AB + AC
- 9.X = (A + B)(C + D)C

Submission:

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