

Attempt 1

Review Feedback

Offline Score: 0/30

Add Comment

Anonymous Grading: no

Unlimited Attempts Allowed

Details

- To solve this assignment:
- Read chapter 4 from the references book, page 272-303. And also, watch this videos to help you understand a little more about pipelining:
 - [Introduction to Piplining in MIPS Datapath \(Adding IF, ID, EX, MEM, WB Stages\) \(16/21\)](https://www.youtube.com/watch?v=EKgpG85A_mw&index=17&list=PLPXsMt57rLthe1kihStAdRgGdj3IZ7WHe&t=0s) [. \(https://www.youtube.com/watch?v=EKgpG85A_mw&index=17&list=PLPXsMt57rLthe1kihStAdRgGdj3IZ7WHe&t=0s\)](https://www.youtube.com/watch?v=EKgpG85A_mw&index=17&list=PLPXsMt57rLthe1kihStAdRgGdj3IZ7WHe&t=0s)
 - Complete the following exercises
 - 4.1.1
 - 4.1.2
 - 4.1.3
 - 4.2.1
 - 4.2.2
 - 4.2.3
 - Record your answers in a PDF or DOC file and submit.

View Rubric

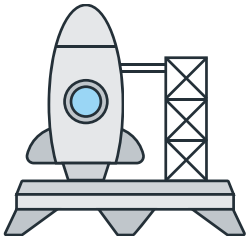
Assign 7 - Processor		
Criteria	Ratings	Pts
Q 4.1.1		/ 4 pts
Q 4.1.2		/ 2 pts
Q 4.1.3		/ 4 pts
Q 4.2.1		/ 2 pts
Q 4.2.2		/ 2 pts
Q 4.2.3		/ 2 pts
Q 4.9.1		/ 2 pts
Q 4.9.2		/ 8 pts
Q 4.9.3		/ 4 pts
		Total Points: 0

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Assignment
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