

PROJECT PROPOSAL FOR
STUDENT PROJECTS SCHEME
(2023-2024)

PROJECT TITLE

**DESIGN AND ANALYSIS OF MINIATURIZED
MICROSTRIP PATCH ANTENNA USING
SUBSTRATE INTEGRATED WAVEGUIDE**

Mr.R.HARISH (953620106027)

Mr.K.MANOJ PRABHAKARAN (953620106041)

PROJECT GUIDE

Mr. D.GOPINATH
Assistant Professor,
Department of Electronics and Communication Engineering,
Ramco Institute of Technology,
Rajapalayam.



RAMCO
INSTITUTE OF
TECHNOLOGY

Submitted To

The Member Secretary,
TAMIL NADU STATE COUNCIL FOR SCIENCE AND TECHNOLOGY,
DOTE Campus, Chennai- 600 025.

DESIGN AND ANALYSIS OF MINIATURIZED MICROSTRIP PATCH ANTENNA USING SUBSTRATE INTEGRATED WAVEGUIDE

I. INTRODUCTION:

A growing expansion of wireless applications operating at centimeter- and millimeter-wave (cm- and mm-wave) frequencies motivates the development of effective and affordably-priced technologies for manufacturing cm- and mm-wave components like antennas, filters, directional couplers, etc. A substrate integrated waveguide (SIW) is a promising candidate for implementing such devices. SIW exhibits advantages similar to conventional metallic rectangular waveguides (high quality factor, high power capacity, and self-consistent electrical shielding). The geometry of an SIW structure . The walls of the SIW are represented by two rows of metallized via holes with center-to-center distance w_{SIW} embedded into a dielectric substrate and by the top and the bottom metallization of the dielectric substrate. In recent years, SIW technology has gained considerable attention and the SIW structure was intensively analyzed and studied . The field distribution in an SIW is similar to that in a conventional metallic rectangular waveguide (RWG). However, only TE_{m0} modes can exist in the SIW structure because of the gaps in the narrow walls.

OBJECTIVES:

- To Determine the required gain for your antenna system. This may be influenced by factors like communication range or link budget..
- Decide on the required bandwidth for your antenna to support the signal bandwidth of interest.
- Determine the required gain for your antenna system. This may be influenced by factors like communication range or link budget.

II. METHODOLOGY:

Define the problem statement or research objectives that your microstrip patch antenna with SIW aims to address. Conduct an extensive review of existing literature on microstrip patch antennas and SIWs. Identify relevant theories, designs, and applications to build a solid foundation for your research.

Clearly define the design specifications, including frequency of operation, bandwidth, gain, and polarization requirements.

Choose an appropriate substrate material for the SIW and microstrip patch antenna, considering factors like dielectric constant, loss tangent, and fabrication constraints.

Design the microstrip patch antenna's geometry, including dimensions, shape, and feed location, based on your design specifications.

Integrate the SIW into the microstrip patch antenna design, ensuring proper alignment and coupling between the SIW and antenna elements.

Use electromagnetic simulation software (e.g., CST Microwave Studio, HFSS) to model and simulate the proposed design. Perform parametric studies to optimize the antenna's performance.

Detail the step-by-step fabrication process, including substrate preparation, metallization, and SIW integration. Mention any specialized equipment or techniques required.

Construct a physical prototype of the microstrip patch antenna with SIW based on the optimized design. Set up measurement equipment (e.g., vector network analyzer) to measure the antenna's performance. Record data on parameters such as return loss, radiation patterns, and gain.

Analyze the measured data and compare it with simulation results to validate the antenna's performance.

Identify any discrepancies and possible reasons. If the measured performance does not meet the design specifications, refine the design through iterative optimization.

III. WORK PLAN:

Time line	Work
Phase 1	Analyzing the Literature survey & Calculate the Antenna dimensions with the help of design equations
Phase 2	Design the Proposed antenna in the CST or HFSS Simulation tool and get the desired antenna parameters
Phase 3	Fabrication of the Proposed antenna and measure the antenna parameters using Network analyzer
Phase 4	Comparison of Simulated result and Real-time measured result
Phase 5	Report Preparation

IV. BUDGET DETAILS:

Sl.No.	List of Apparatus	Price
1.	Proposed Antenna Fabrication	Rs.5000
2.	Gain measurement using Anechoic chamber	Rs.2500
3.	Return Loss and VSWR measurement using Network Analyzer	Rs.1500
	Total	Rs.9000

REFERENCES

1. UCHIMURA, H., TAKENOSHITA, T., FUJII, M. Development of a “laminated waveguide”. IEEE Transactions on Microwave Theory and Techniques. 1998, vol. 46, no. 12, p. 2438–2443.
2. BOZZI, M., GEORGIADIS, A., WU, K. Review of substrate-integrated waveguide circuits and antennas. IET Microwaves, Antennas and Propagation. 2011, vol. 5, no. 8, p. 909–920.
3. CASSIVI, Y., PERREGRINI, L., ARCIONI, P., BRESSAN, M., Wu, K., CONCIAURO, G. Dispersion characteristics of substrate integrated rectangular waveguide. IEEE Microwave and Wireless Components Letters. 2002, vol. 12, no. 9, p. 333–335.
4. XU, F., WU, K. Guided-wave and leakage characteristics of substrate integrated waveguide. IEEE Transaction on Microwave Theory and Techniques. 2005, vol. 53, no. 1, p. 66–73.
5. YAN, L., HONG, W., WU, K., CUI, T.J. Investigations on the propagation

characteristics of the substrate integrated waveguide based on the method of lines. IEE Proceedings Microwaves, Antennas and Propagation. 2005, vol. 152, no. 1, p. 35–42.

6. DESLANDES, D., WU, K. Accurate modeling, wave mechanisms, and design considerations of a substrate integrated waveguide. IEEE Transactions on Microwave Theory and Techniques. 2006, vol. 54, no. 6, p. 2516–2526.
7. CHE, W., DENG, K., WANG, D., CHOW, Y.L. Analytical equivalence between substrateintegrated waveguide and rectangular waveguide. IET Microwaves, Antennas and Propagation. 2008, vol. 2, no. 1, p. 35–41.
8. BOZZI, M., PASIAN, M., PERREGRINI, L., WU, K. On the losses in substrate integrated waveguides. In Proceedings of the European Microwave Conference. 2007, p. 384–387.
9. [YAN, L., HONG, W., HUA, G., CHEN, J., WU, K., CUI, T.J. Simulation and experiment on SIW slot array antennas. IEEE Microwave and Wireless Components Letters. 2004, vol. 14, no. 9, p. 446–448.