

10/11/18 KANALIKA PODDAR

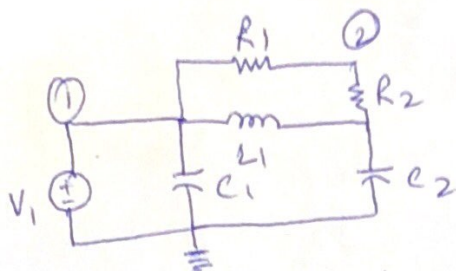
HW1

SID-862002289

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RR-213

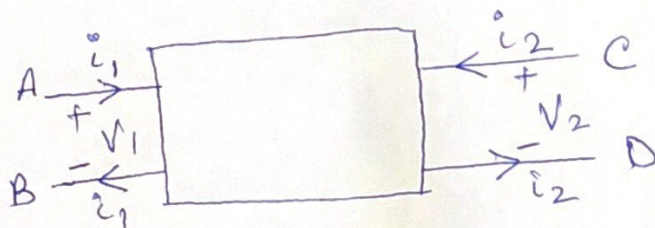
Question 1



	1	2	3	i_s	i_{L1}
1	$\frac{1}{R_1} + sC_1$	$-\frac{1}{R_1}$	0	1	1
2	$-\frac{1}{R_1}$	$\frac{1}{R_1} + \frac{1}{R_2}$	$-\frac{1}{R_2}$	0	0
3	0	$-\frac{1}{R_2}$	$\frac{1}{R_2} + sC_2$	0	-1
branch 1	1	0	0	0	0
branch 2	1	0	0	-1	0
				0	$-sL_1$

The above is the MNA equation for the RLC circuit.

Question 2



$$V_1 = e_A - e_B$$

$$V_2 = e_C - e_D$$

$$e_A - e_B = h_{11}i_1 + h_{12}(e_C - e_D)$$

$$i_2 = h_{21}i_1 + h_{22}(e_C - e_D)$$

P.T.O. →

2.a

MNA stamp for this element.

(2)

	e_A	e_B	e_C	e_D	i_1	i_2	RHS
e_A	0	0	0	0	1	0	0
e_B	0	0	0	0	-1	0	0
e_C	0	0	0	0	0	1	0
e_D	0	0	0	0	0	-1	0
branch 1	-1	1	h_{12}	$-h_{12}$	h_{11}	0	0
branch 2	0	0	h_{22}	$-h_{22}$	h_{21}	-1	0

2.b

$$V_1 = h_{11} i_1 + h_{12} V_2$$

$$h_{11} i_1 = V_1 - h_{12} V_2$$

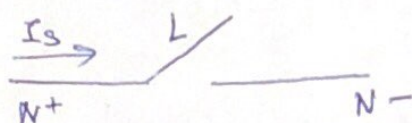
$$i_1 = \frac{V_1}{h_{11}} - \frac{h_{12}}{h_{11}} V_2 \quad \text{--- (i)}$$

$$i_2 = \frac{h_{21}}{h_{11}} V_1 + \left(h_{22} - \frac{h_{12} \cdot h_{21}}{h_{11}} \right) V_2$$

NA stamp	e_A	e_B	e_C	e_D	RHS
e_A	$\frac{1}{h_{11}}$	$-\frac{1}{h_{11}}$	$-\frac{h_{12}}{h_{11}}$	$\frac{h_{12}}{h_{11}}$	0
e_B	$-\frac{1}{h_{11}}$	$\frac{1}{h_{11}}$	$\frac{h_{12}}{h_{11}}$	$-\frac{h_{12}}{h_{11}}$	0
e_C	$\frac{h_{21}}{h_{11}}$	$-\frac{h_{21}}{h_{11}}$	$\left(h_{22} - \frac{h_{12} h_{21}}{h_{11}} \right)$	$-\left(h_{22} - \frac{h_{12} h_{21}}{h_{11}} \right)$	0
e_D	$-\frac{h_{21}}{h_{11}}$	$\frac{h_{21}}{h_{11}}$	$-\left(h_{22} - \frac{h_{12} h_{21}}{h_{11}} \right)$	$\left(h_{22} - \frac{h_{12} h_{21}}{h_{11}} \right)$	0

Question 3

③



MNA stamp for the switch

$$\begin{array}{c} N^+ \quad N^- \quad I \\ \begin{array}{c} N^+ \\ N^- \\ I \end{array} \begin{bmatrix} 0 & 0 & 1 \\ 0 & 0 & -1 \\ L & -L & -(1-L) \end{bmatrix} \end{array} \quad \begin{array}{c} \text{RHS} \\ \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix} \end{array}$$

where we have $L=1$ for switch on and $L=0$ for off.

Question 5

$$\begin{bmatrix} G+Sc & A_e^T \\ -A_e & S_L \end{bmatrix} \begin{bmatrix} V_n \\ i_L \end{bmatrix} = \begin{bmatrix} i_n(s) \\ 0 \end{bmatrix}$$

Schur Decomposition

$$\begin{bmatrix} G+Sc & A_e^T \\ 0 & S_L + \frac{1}{G+Sc} \end{bmatrix} \begin{bmatrix} V_n \\ i_L \end{bmatrix} = \begin{bmatrix} i_n(s) \\ \frac{A_e \cdot i_n(s)}{G+Sc} \end{bmatrix}$$

$$R_2 \leftarrow R_2 + \frac{A_e}{G+Sc} R_1$$

$$R_2 \leftarrow S_L + \frac{A_e A_e^T}{G+Sc}$$

Nodal Analysis

(4)

$$5(a) \left(s_L + \frac{1}{G+Sc} \right) i_L = \frac{A_L}{G+Sc} i_n(s)$$

$$(G+Sc) s_L i_L + i_L = A_L i_n(s)$$

Above is the nodal Analysis formulation of the RLCM circuit based on the node reduction formula.

5.(b) The NA formulation is not equivalent to the MNA formulation when the RLCM circuit has $[G+Sc=0]$. Therefore the capacitive and resistive components can't be equal to zero and the inductive branches (A_L) cannot be zero.

Question 4

4.a The constitutive relation for any circuit is

$$k_i i + k_u u = s \quad \text{where } k_i \text{ \& } k_u \text{ is}$$

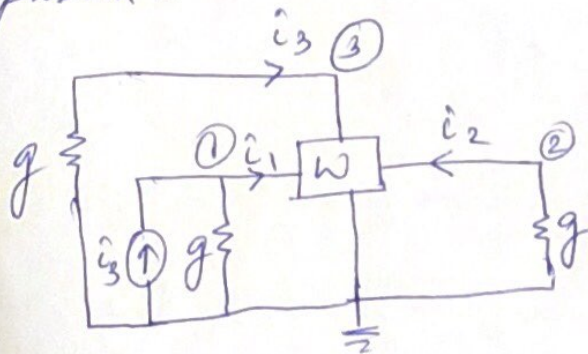
dependent on the nature of the element and s is a vector of source terms.

Normally for the equation $Gv=i$, can be solved by solving i on one side and the k_i matrix can be inverted. In this situation nodal analysis can be applied.

P.T.O. \rightarrow

4.a For the element W, the k_i matrix is singular i.e., first column equals the sum of the second plus third. Therefore we cannot solve the equation for i , and thus cannot use NA for the circuit which features W-element. (5)

4.b We are going to use modified Nodal Analysis for this problem.



∴ We have

$$g v_1 = i_3 - i_1$$

$$g v_2 = -i_2$$

$$g v_3 = -i_3$$

∴ we have Consecutive relation of W element.

$$v_1 + i_1 + i_2 = 0$$

$$v_1 + v_2 + i_2 - i_3 = 0$$

$$v_1 + v_2 + v_3 + i_1 + i_3 = 0$$

P.T.O. →

⑥

4.16 therefore we have MNA as.

$$\begin{bmatrix} g & 0 & 0 & 1 & 0 & 0 \\ 0 & g & 0 & 0 & 1 & 0 \\ 0 & 0 & g & 0 & 0 & 1 \\ 1 & 0 & 0 & 1 & 1 & 0 \\ 1 & 1 & 0 & 0 & 1 & -1 \\ 1 & 1 & 1 & 1 & 0 & 1 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ i_1 \\ i_2 \\ i_3 \end{bmatrix} = \begin{bmatrix} i_s \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix}$$