

NPTEL ONLINE CERTIFICATION COURSES

DIGITAL CONTROL IN SMPCs AND FPGA-BASED PROTOTYPING

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Module 02: Fixed and Variable Frequency Digital Control Architectures

Lecture 17: Constant On/Off- Time Mixed-Signal Current Mode Control Architectures

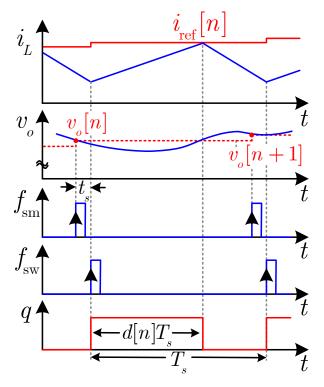


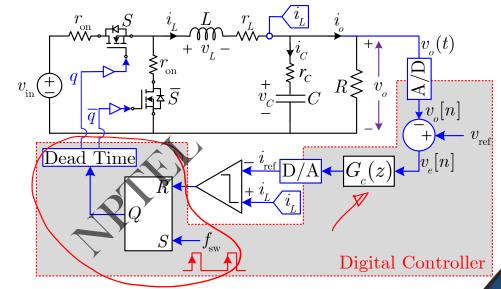


CONCEPTS COVERED

- Steady-state operation under mixed-signal constant off-time CMC
- Complete architecture of mixed-signal constant off-time peak CMC
- Complete architecture of mixed-signal constant on-time valley CMC
- Applications of mixed-signal constant on/off-time CMC

$Mixed ext{-}Signal\ Peak\ CMC\ Architecture: Recall$





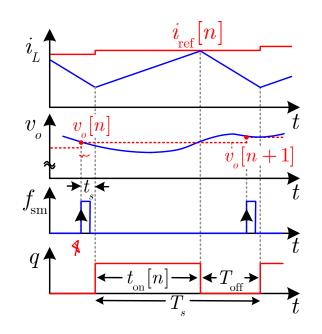
Structurally different sub-harmonic instability with duty ratio saturations

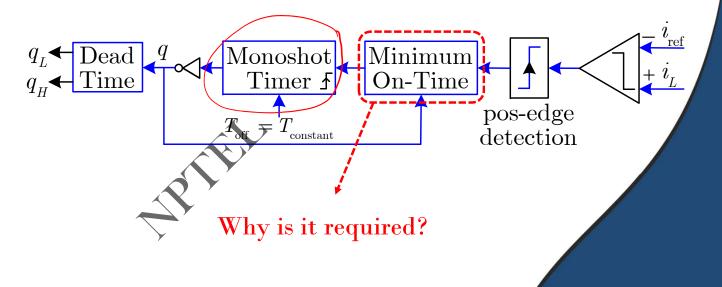
S. Kapat, "Fixed and Variable Frequency Digital Current Mode Control: Structural Stability ...", IEEE APEC, 2021





Mixed-Signal Constant Off-time CMC: Basics (contd...)



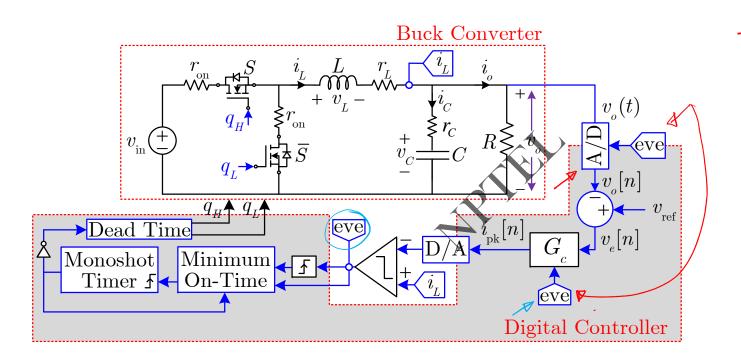


[For details, refer to Lecture~20, NPTEL "Control and Tuning Methods ..." course (Link)



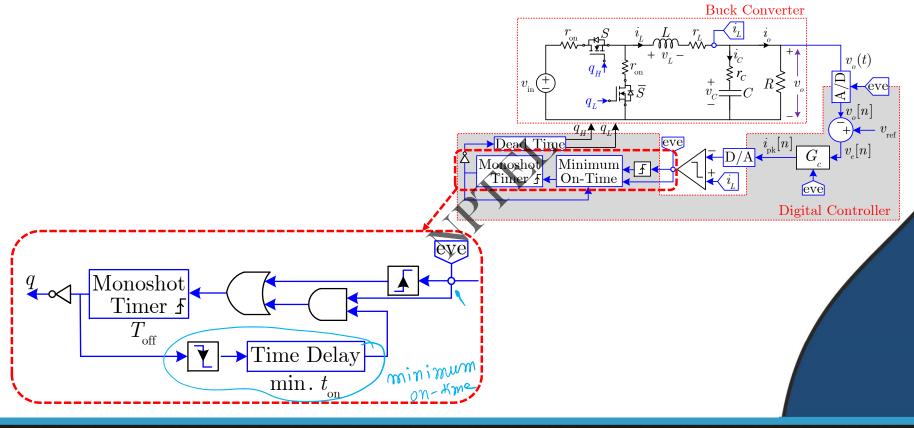


Mixed-Signal Constant Off-time CMC | Event based sampling 7



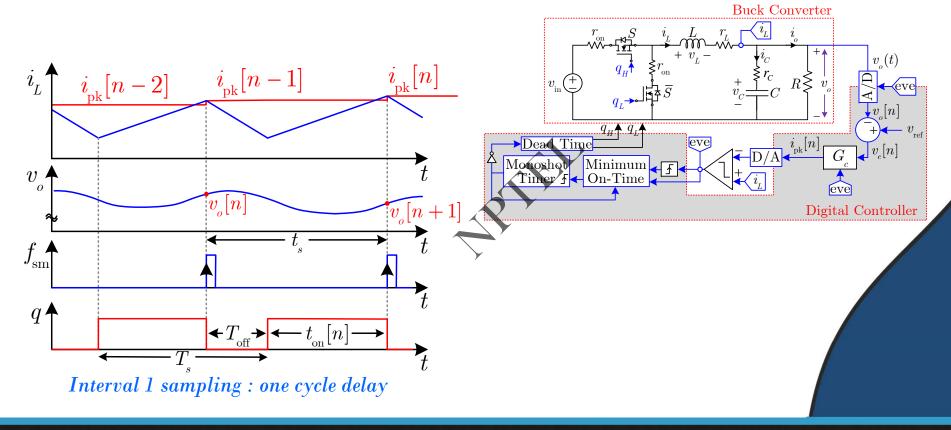


Mixed-Signal Constant Off-time CMC | Event based sampling (contd...)





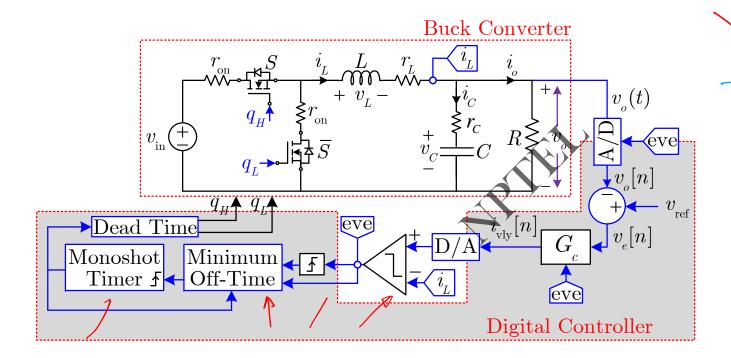
Mixed-Signal Constant Off-time CMC | Event based sampling (contd...)





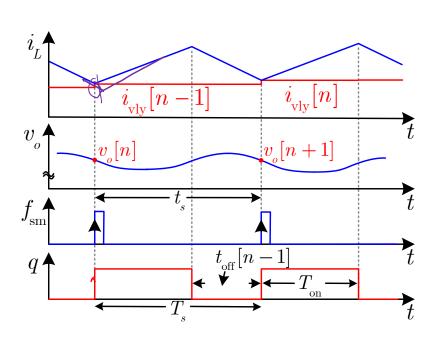


Mixed-Signal Constant On-time CMC | Event based sampling

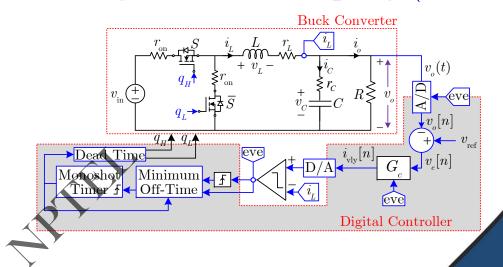




Mixed-Signal Constant On-time CMC | Event based sampling (contd...)

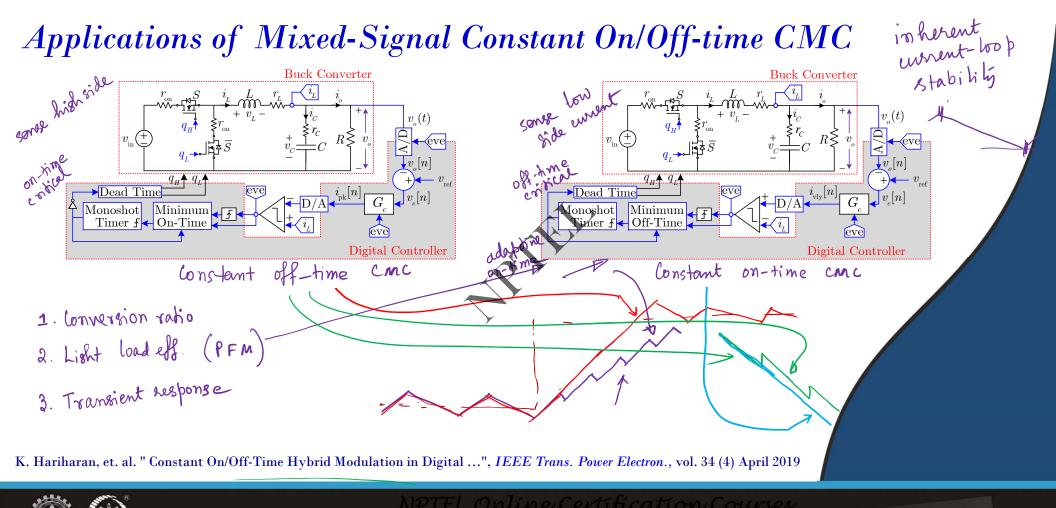












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CONCLUSION

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