



**NPTEL ONLINE CERTIFICATION COURSES**

# **DIGITAL CONTROL IN SMPCs AND FPGA-BASED PROTOTYPING**

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**Module 04: Modeling Techniques and Model Validation using MATLAB**

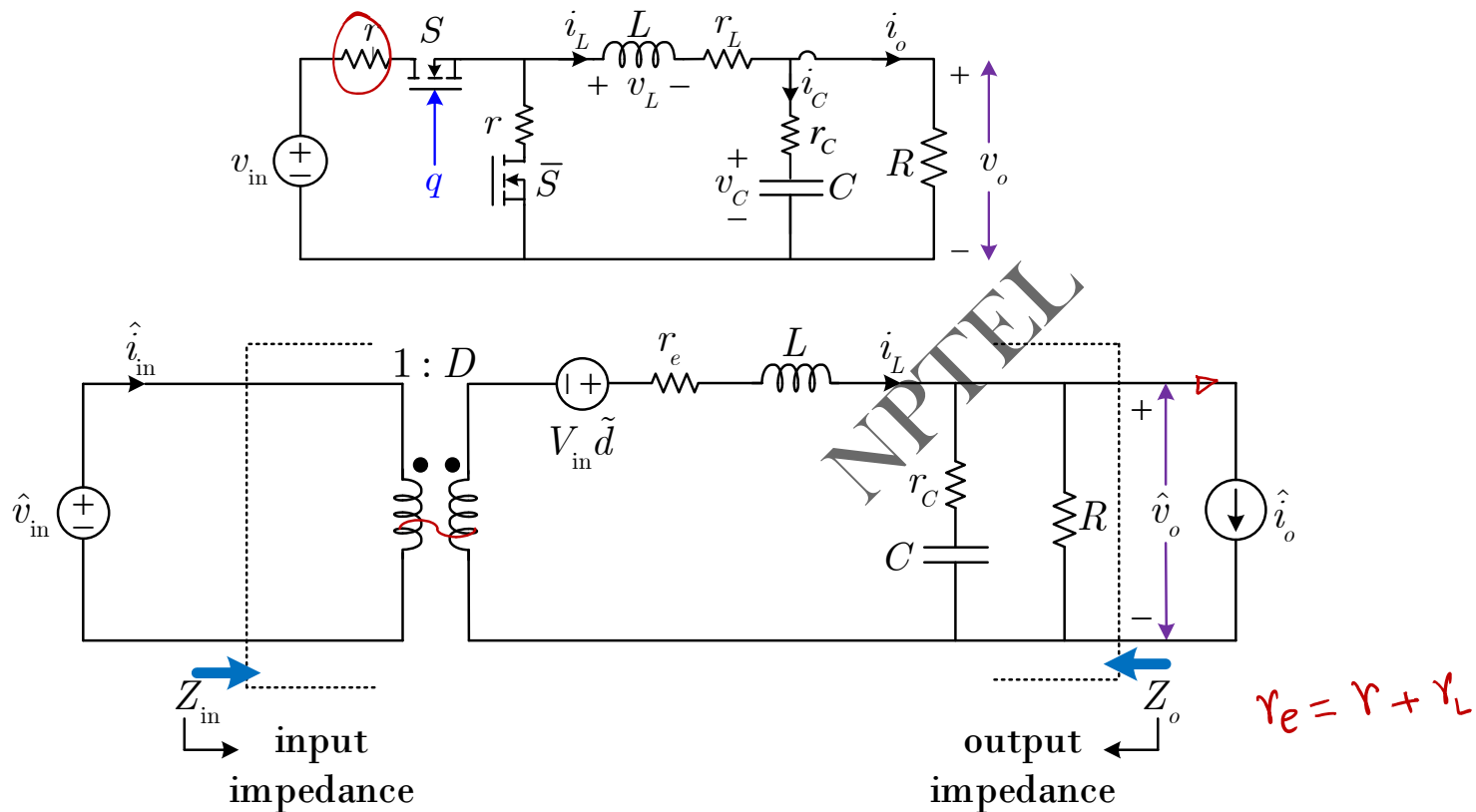
**Lecture 31: Continuous-Time Small-Signal Modeling under Digital Control**



## CONCEPTS COVERED

- Recap of continuous-time small-signal modeling
- Modeling of sampled data-system
- Continuous-time small-signal modeling under digital control

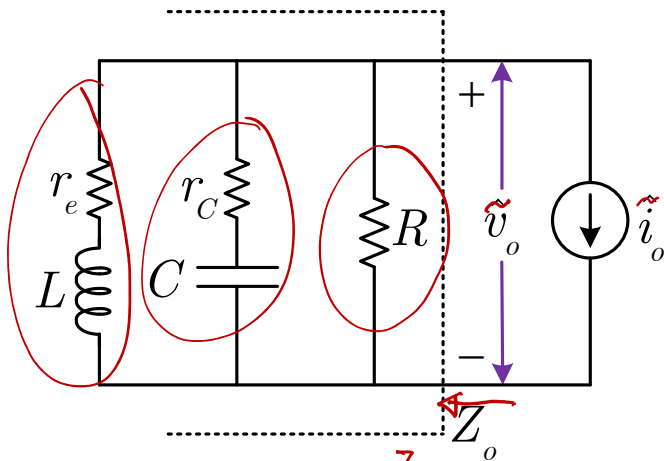
## AC Equivalent Circuit of a Practical Synchronous Buck Converter



## Output Impedance

### Assumptions:

- a) Converter in open-loop with  $\hat{d} = 0$
- b) No input voltage perturbation  $\hat{v}_{in} = 0$



$$Z_o(s) = (r_e + sL) \parallel \left( r_C + \frac{1}{Cs} \right) \parallel R$$

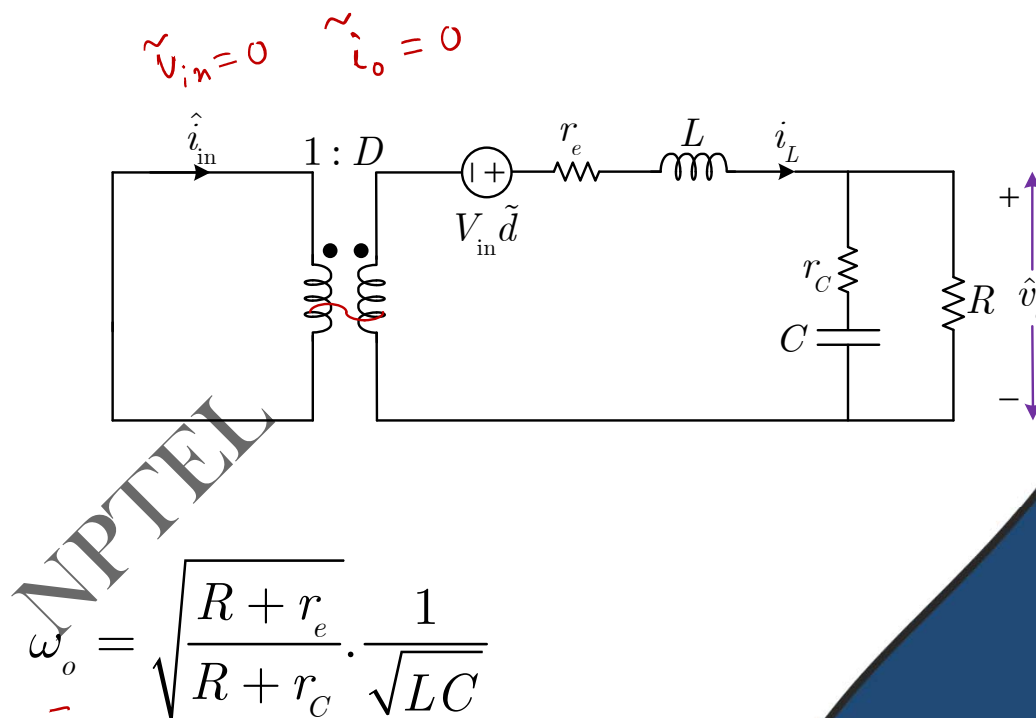
$$\tilde{v}_o = -Z_o \tilde{i}_o$$

[ For details, refer to  <sup>$Z_o$</sup> Lecture~33, NPTEL “Control and Tuning Methods ...” course ([Link](#))

## Control-to-Output TF

$$\underline{G_{vd}(s)} = \frac{V_{IN}}{\frac{R+r_e}{R}} \frac{(1+r_cCs)}{\left(1 + \frac{s}{Q\omega_o} + \frac{s^2}{\omega_o^2}\right)}$$

$$\underline{Q} = \frac{R+r_e}{R} \left[ \frac{r_c+r_e}{\sqrt{\frac{L}{C}}} + \frac{\sqrt{\frac{L}{C}}}{R} \right]^{-1}$$



[ For details, refer to [Lecture~33, NPTEL “Control and Tuning Methods ...” course \(Link\)](#)

## Complete Small-Signal Block Diagram

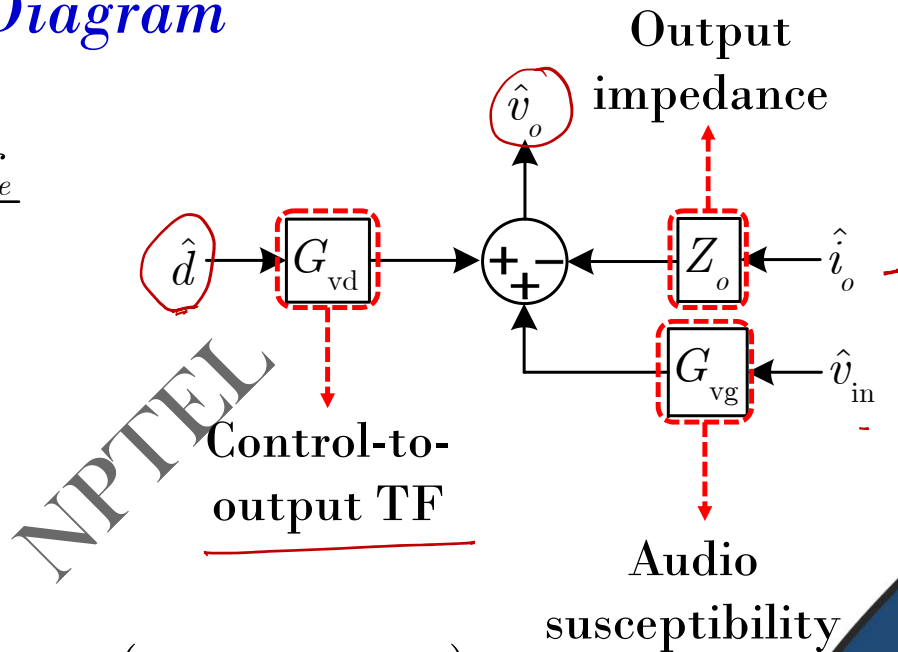
$$\underline{G_{vd}} = \frac{V_{IN}}{\alpha} \frac{\left(1 + \frac{s}{w_{ESR}}\right)}{\Delta(s)}$$

$$\alpha = \frac{R + r_e}{R}$$

$$\underline{G_{vg}} = \frac{D}{\alpha} \frac{\left(1 + \frac{s}{w_{ESR}}\right)}{\Delta(s)}$$

$$\underline{Z_o(s)} = \frac{r_e}{\alpha} \frac{\left(1 + \frac{s}{w_L}\right) \left(1 + \frac{s}{w_{ESR}}\right)}{\Delta(s)}$$

$$\Delta(s) = \left(1 + \frac{s}{Q\omega_o} + \frac{s^2}{\omega_o^2}\right)$$



[ For details, refer to [Lecture~33, NPTEL “Control and Tuning Methods ...” course](#) ([Link](#))

## Modulator Gain – Pulse Width Modulator

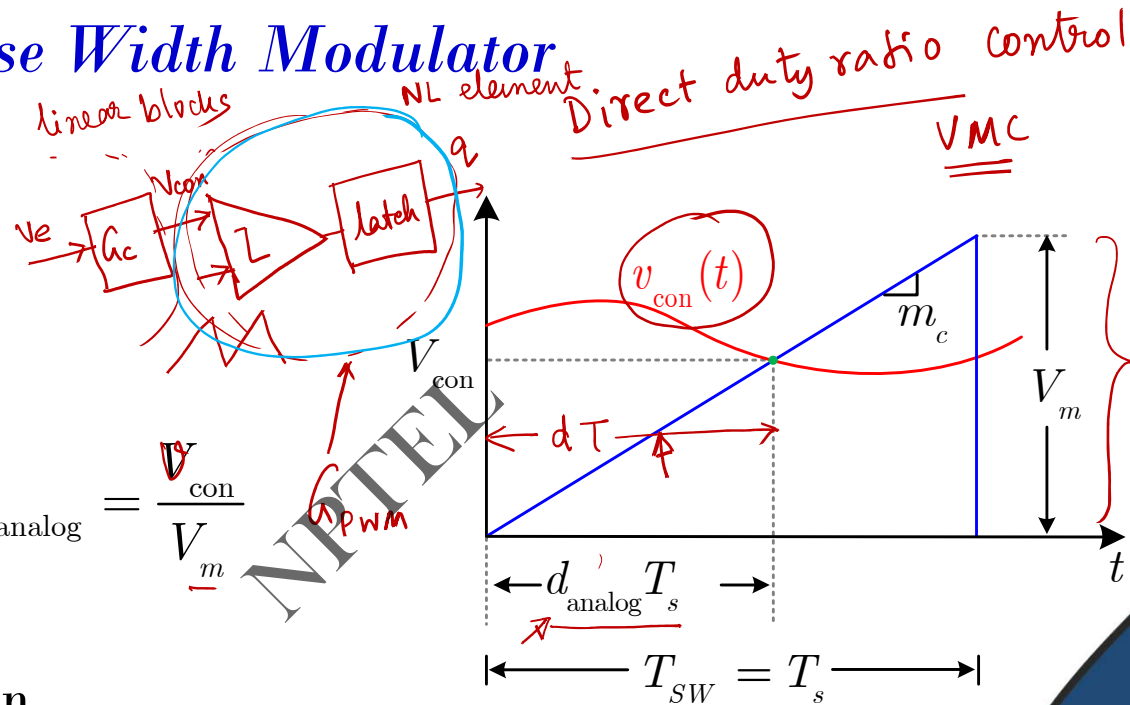
- In case of analog control

$$v_{\text{con}}(t) \Big|_{t=d_{\text{analog}} T_s} = V_{\text{con}}$$

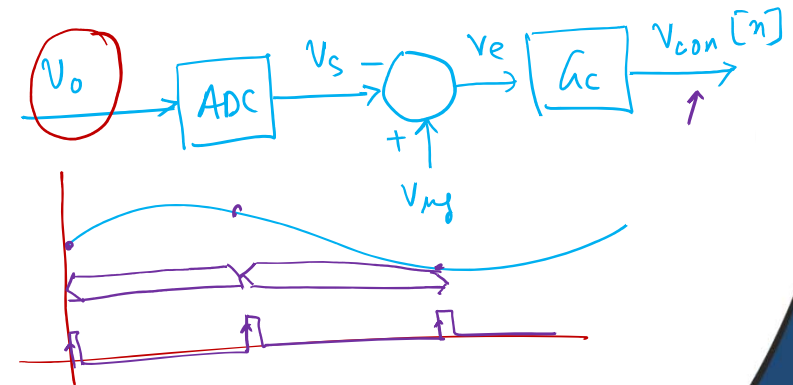
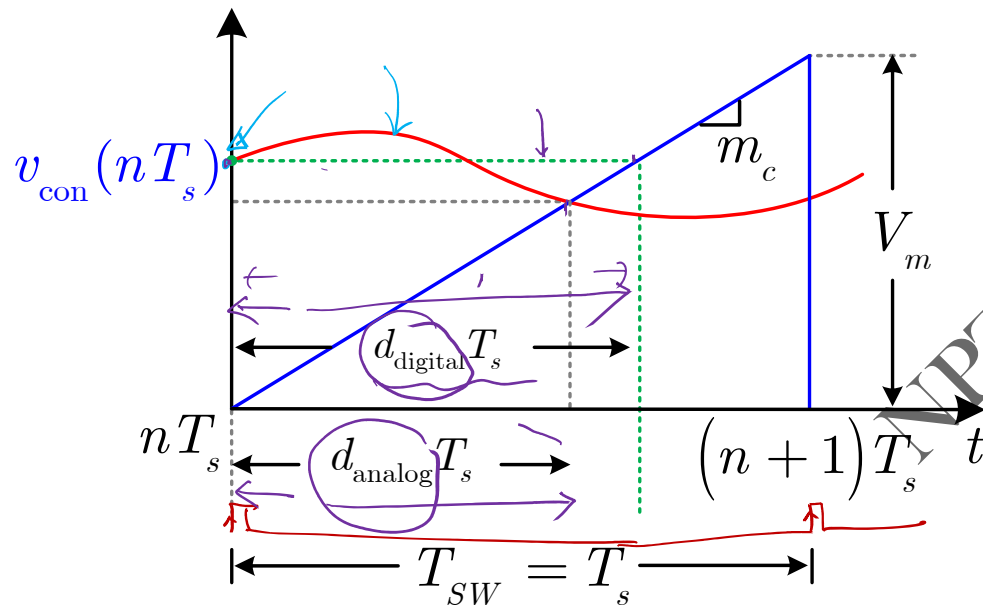
$$\frac{v_{\text{con}}}{V_m} = \frac{d_{\text{analog}} T_s}{T_s} \Rightarrow d_{\text{analog}} = \frac{v_{\text{con}}}{V_m}$$

- Modular transfer Function

$$G_{\text{PWM}}(s) = \frac{1}{V_m} \triangleq F_m$$

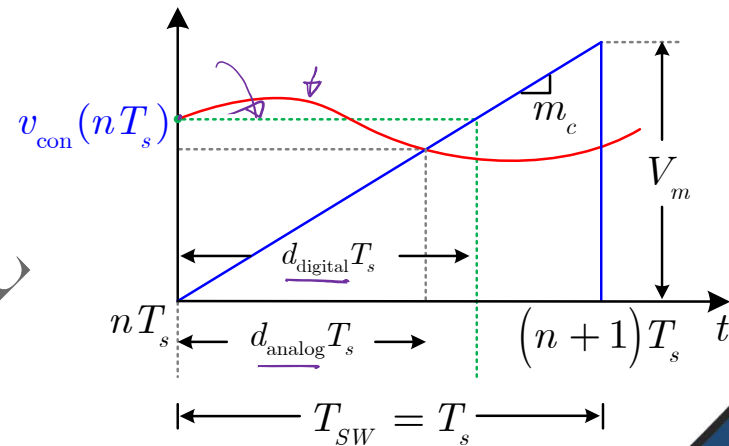
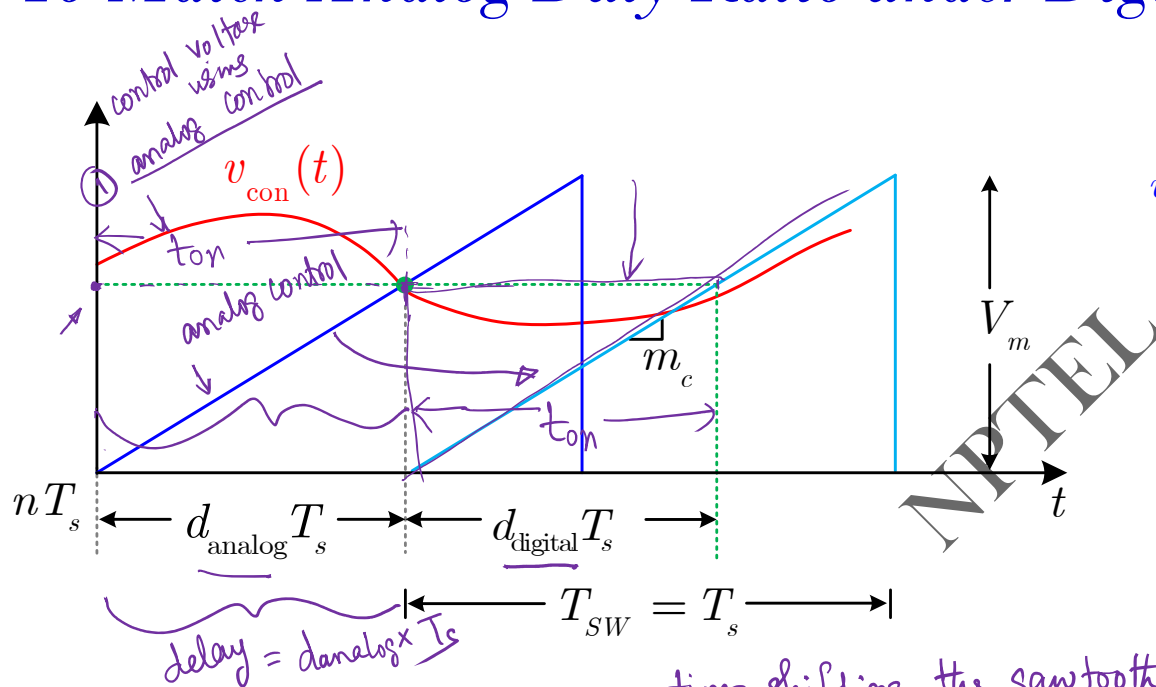


## Modulator Gain – Pulse Width Modulator





## To Match Analog Duty Ratio under Digital Control



$d_{digital} = d_{analog}$  with a time delay of  $d_{analog} T_s$ !!

## Modulator Gain under Digital Control

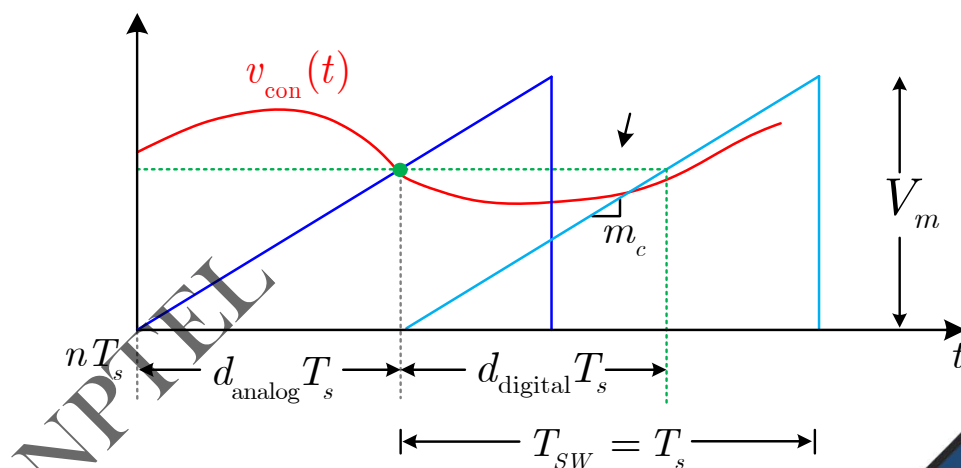
- Modular transfer function (analog)

$$G_{\text{analog}}(s) = \frac{1}{V_m}$$

- Modular transfer function (digital)

$$G_{\text{digital}}(s) = e^{-sDT_s} G_{\text{analog}}(s)$$

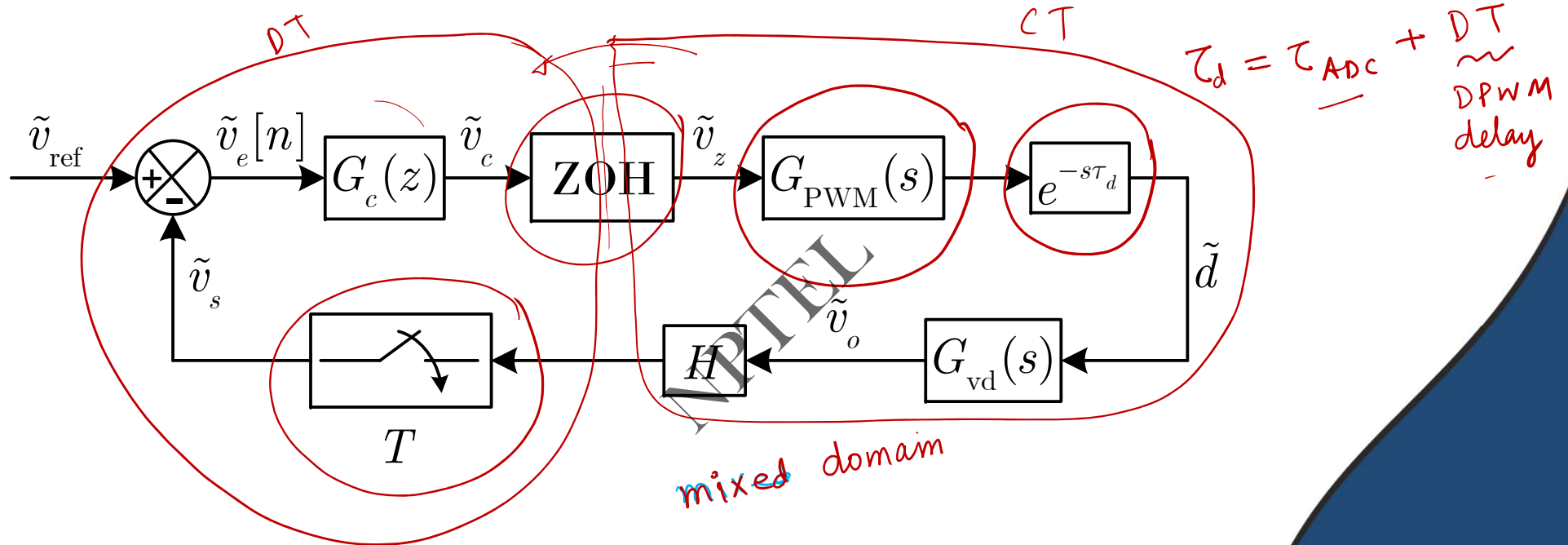
$$G_{\text{digital}}(s) = \frac{1}{V_m} e^{-sDT_s}$$



Digital PWM  $\rightarrow$  introduces a delay  $= DT_s$

**Modular delay  $t_d = DT_s$  !!**

## Small-Signal Block Diagram under Digital Control



## Modeling of ZOH Block

ZOH transfer function:

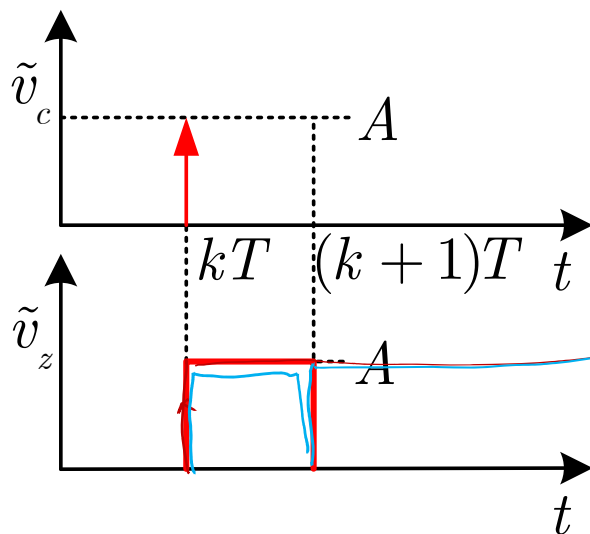
$$\tilde{v}_c = A\delta(t - kT)$$

$$\tilde{v}_z = A \left[ \underbrace{u(t - kT)}_{\text{red}} - \underbrace{u\{t - (k + 1)T\}}_{\text{red}} \right]$$

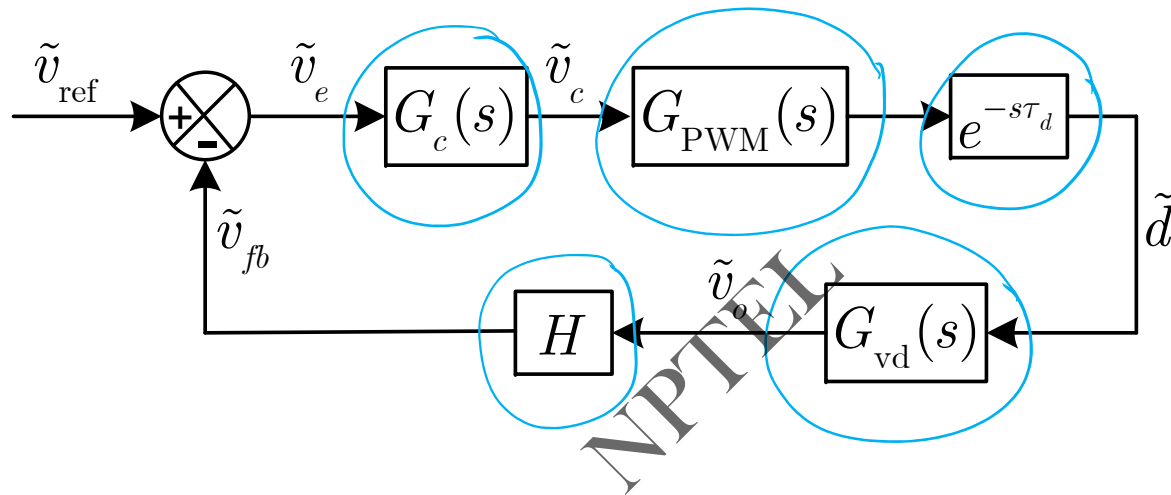
$$\tilde{v}_c(s) = A$$

$$\tilde{v}_z(s) = \frac{A}{s} [1 - e^{-sT}]$$

$$G_{\text{ZOH}} = \frac{1 - e^{-sT}}{s}$$



## Approximate CT Small-Signal Model under Digital Control



$$\underline{K_{\text{loop, digital}}(s)} = \underline{K_{\text{loop, analog}}(s)} \times e^{-s\tau_d}$$

$$\tau_d = \underline{t_{\text{adc}}} + \underline{t_{\text{DPWM}}}$$

[R. Erickson and D. Maksimovic, “*Fundamentals of power electronics*”, 3<sup>rd</sup> Ed., Springer, 2020]

## Control-to-Output TF with Delay

$$G_{vd}(s) = \frac{V_{IN}}{\left(\frac{R+r_e}{R}\right)} \frac{(1+r_CCs)}{\left(1 + \frac{s}{Q\omega_o} + \frac{s^2}{\omega_o^2}\right)}$$

$$G_{vd\_delay}(s) = e^{-s\tau_d} \times G_{vd}(s)$$

$$G_{vd\_delay}(s) = \frac{V_{IN}}{\left(\frac{R+r_e}{R}\right)} \frac{(1+r_CCs)e^{-s\tau_d}}{\left(1 + \frac{s}{Q\omega_o} + \frac{s^2}{\omega_o^2}\right)}$$

Total loop delay  $\tau_d = t_{adc} + t_{DPWM}$

NPTEL

$$\tau_d = \tau_{adc} + DT$$

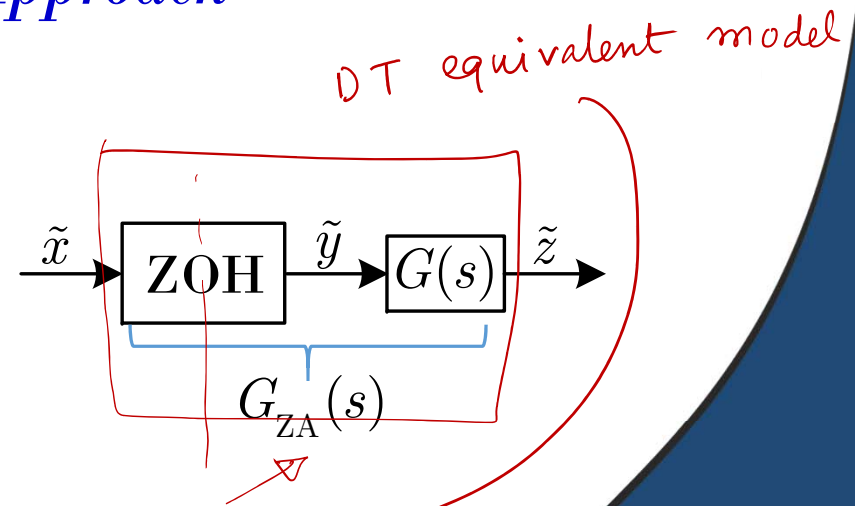
TE PWM

## ZOH Equivalent Modeling – Alternative Approach

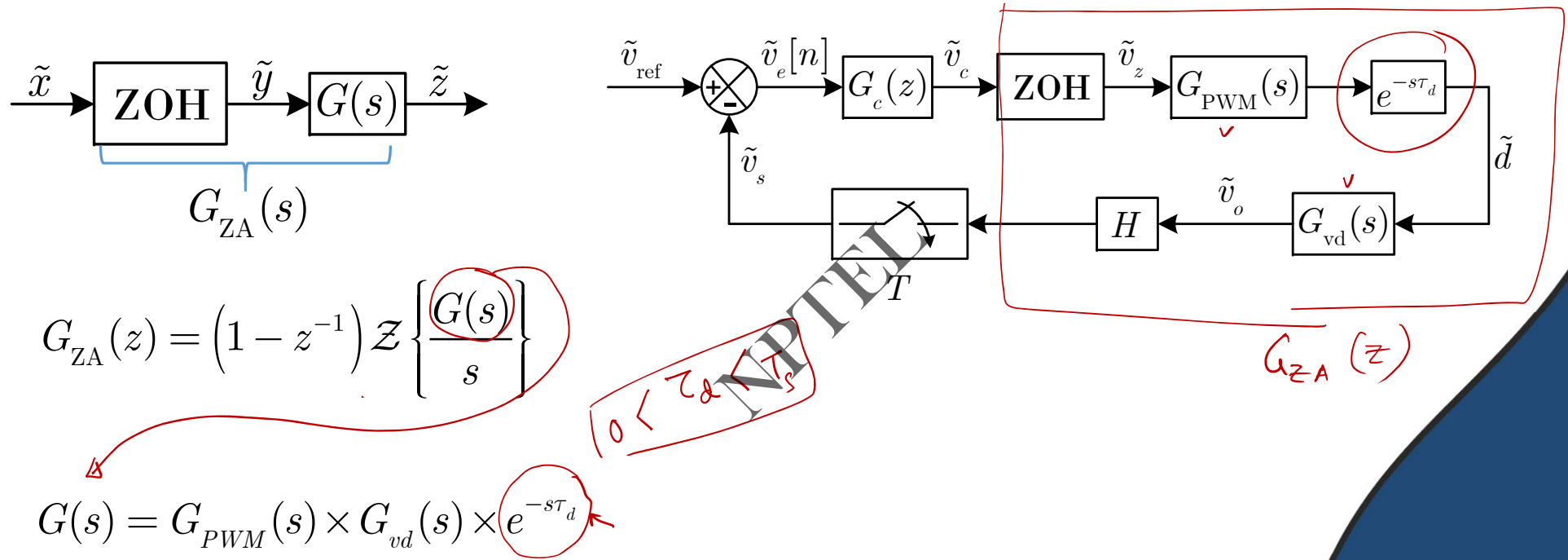
Consider a cascaded block

$$G_{ZA}(s) = \underline{G(s)} \times \underline{G_{ZOH}(s)} = (1 - e^{-sT}) \left( \frac{G(s)}{s} \right)$$

$$\underline{G_{ZA}(z)} = (1 - z^{-1}) \mathcal{Z} \left\{ \frac{\underline{G(s)}}{s} \right\}$$



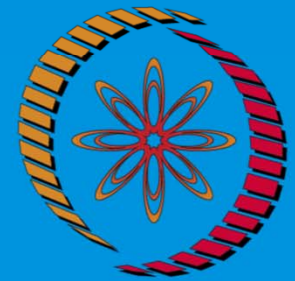
## ZOH Equivalent Modeling of SMPC – Alternative Approach





# CONCLUSION

- Recap of continuous-time small-signal modeling
- Modeling of sampled data-system
- Continuous-time small-signal modeling under digital control



**THANK  
YOU !**