



NPTEL ONLINE CERTIFICATION COURSES

DIGITAL CONTROL IN SMPCs AND FPGA-BASED PROTOTYPING

Dr. Santanu Kapat

Electrical Engineering Department, IIT KHARAGPUR

Module 04: Modeling Techniques and Mode Validation using MATLAB

Lecture 37: Derivation of Discrete-Time Small-Signal Models - I



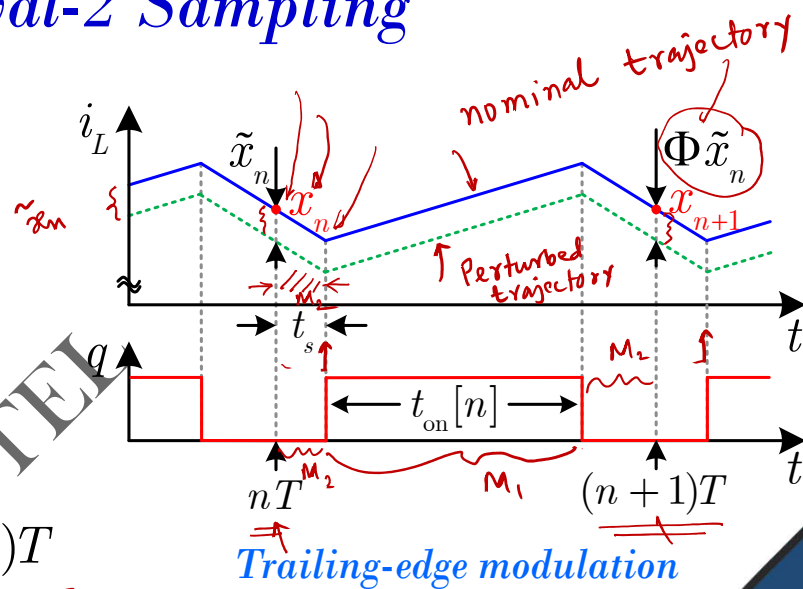
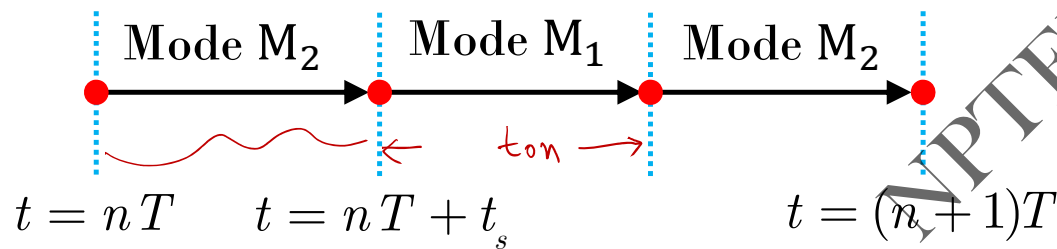
CONCEPTS COVERED

- Effect of perturbations in discrete-time modeling
- Zero-input and zero-state response
- Discrete-time small-signal modeling

Discrete-Time Modeling with Interval-2 Sampling

$$x(t)\big|_{t=nT} = x_n \quad x(t)\big|_{t=(n+1)T} = x_{n+1}$$

$$t = nT + t_s + t_{\text{on}}$$



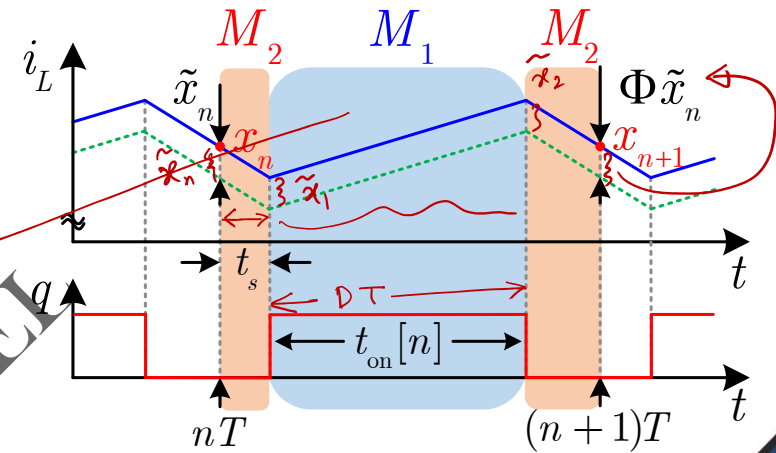
Mode M ₁	Mode M ₂
A_1, B_1, C_1	A_2, B_2, C_2

Discrete-Time Modeling : Initial State Perturbation

Considering the effect of perturbation of states \tilde{x}_n and neglecting the effect of perturbation in duty cycle $\tilde{d} = 0$

$$\Rightarrow \tilde{x}_{n+1} = e^{A_2(T-t_{\text{on}}-t_s)} e^{A_1 t_{\text{on}}} e^{A_2 t_s} \tilde{x}_n$$

$$\Rightarrow \tilde{x}_{n+1} = \underbrace{e^{A_2((1-D)T-t_s)} e^{A_1 D T} e^{A_2 t_s}}_{\Phi} \tilde{x}_n$$

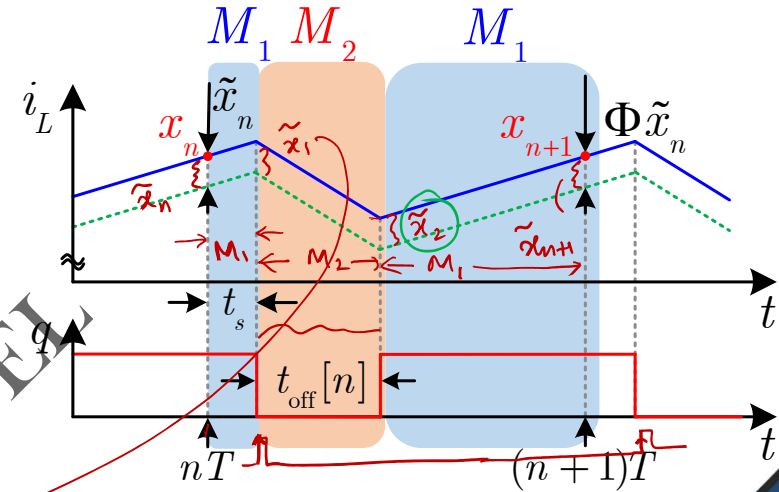
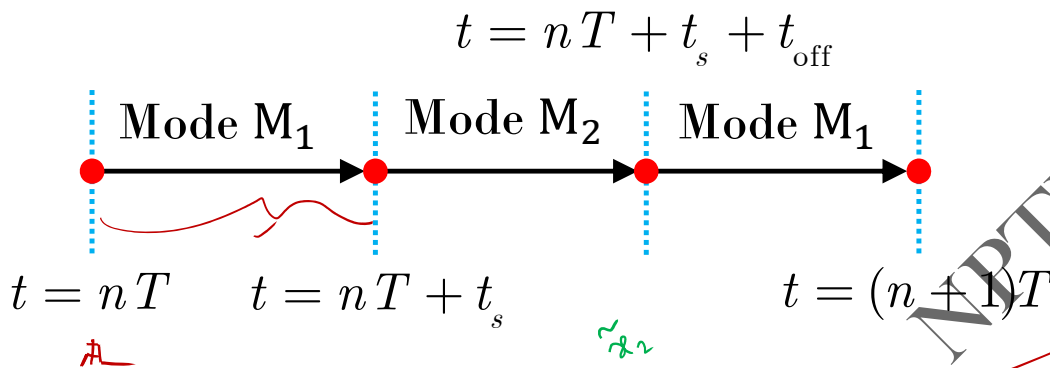


[D. Maksimovic and R. Zane, "Small-Signal Discrete-Time Modeling.....", *IEEE TPEL*, 2007]

Discrete-Time Modeling : Initial State Perturbation

$$\tilde{x}_{n+1} = \Phi \tilde{x}_n$$

Interval-1 sampling

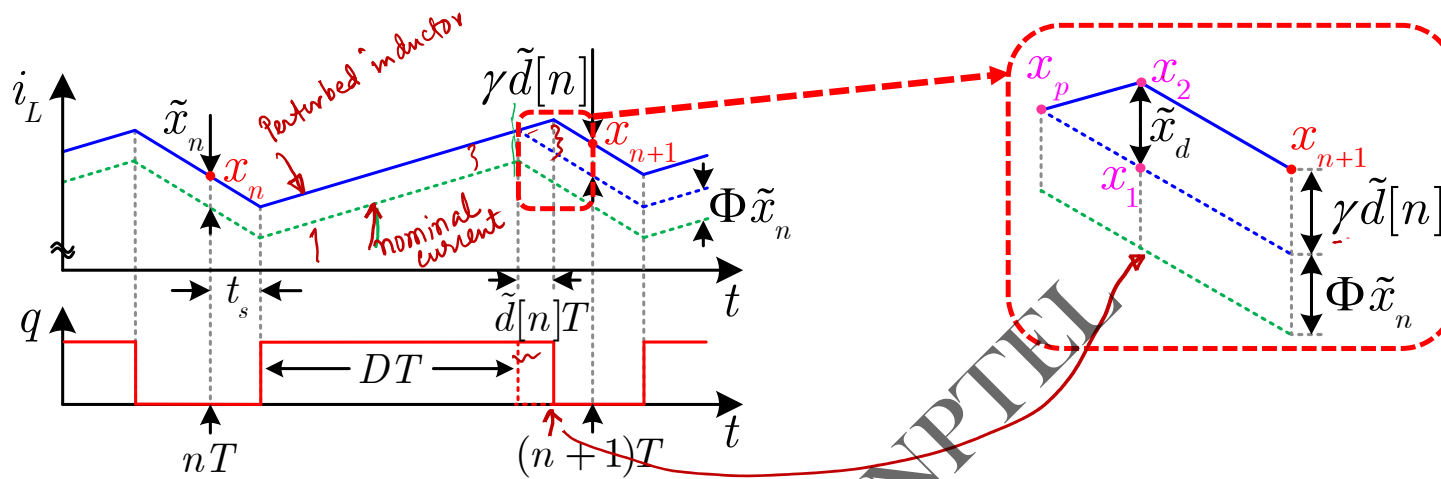


$$\Rightarrow \tilde{x}_{n+1} = e^{A_1(T-t_{\text{off}}-t_s)} e^{A_2 t_{\text{off}}} e^{A_1 t_s} \tilde{x}_n \Rightarrow \tilde{x}_{n+1} = \underbrace{e^{A_1(DT-t_s)} e^{A_2(1-D)T}}_{\Phi} e^{A_1 t_s} \tilde{x}_n$$

$\tilde{x}_{n+1} = \Phi \tilde{x}_n$

[D. Maksimovic and R. Zane, "Small-Signal Discrete-Time Modeling.....", *IEEE TPEL*, 2007]

Discrete-Time Modeling : Initial State and Duty Ratio Perturbations



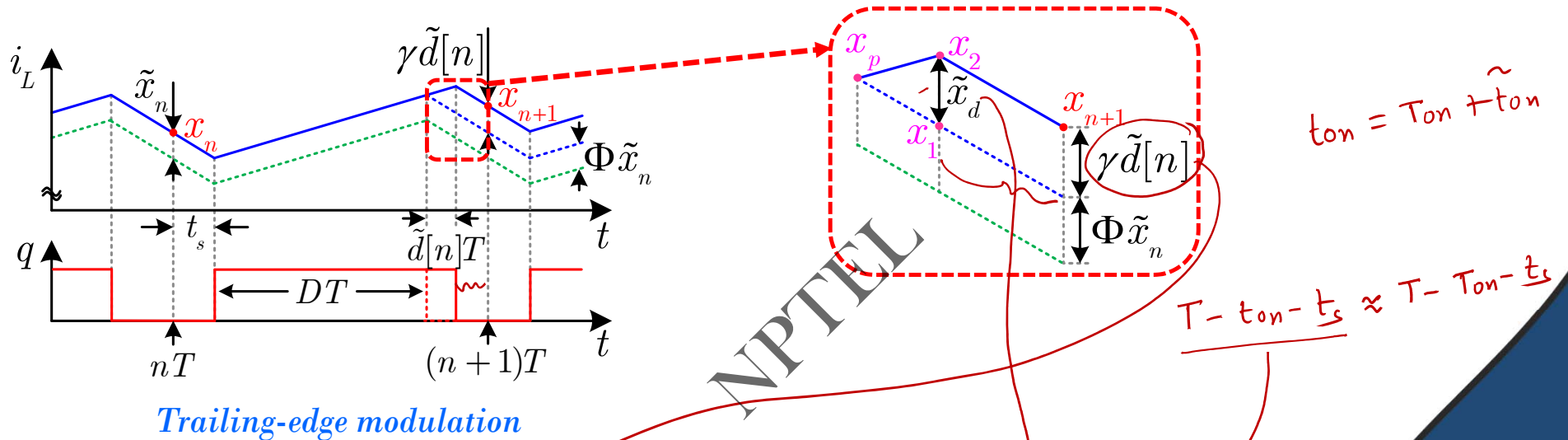
Trailing-edge modulation

Considering only the effect of perturbation in duty cycle $\tilde{d}[n]$

$$\tilde{x}_d = x_2 - x_1 = \left[(A_1 - A_2)x_p + (B_1 - B_2)V_{in} \right] \tilde{d}[n]T = \text{constant} \times \tilde{d}[n]$$

[D. Maksimovic and R. Zane, "Small-Signal Discrete-Time Modeling.....", *IEEE TPEL*, 2007]

Discrete-Time Modeling : Initial State and Duty Ratio Perturbations



Considering $\underline{\alpha} = (A_1 - A_2)x_p + (B_1 - B_2)V_{in} \Rightarrow \tilde{x}_d = \underline{\alpha}T\tilde{d}[n]$

$$\Rightarrow \tilde{x}_{n+1} = e^{A_2(T-t_{on}-t_s)} \tilde{x}_d = e^{A_2(T-t_{on}-t_s)} \underline{\alpha}T\tilde{d}[n]$$

only due to duty ratio perturbation

Discrete-Time Modeling : Initial State and Duty Ratio Perturbations

$$\tilde{x}_{n+1} = \Phi \tilde{x}_n + \gamma \tilde{d}[n]$$

ccm buck conv. with $A_1 = A_2 = A$
 $\Phi = e^{AT}$

$$\Phi = e^{A_2((1-D)T - t_s)} e^{A_1 D T} e^{A_2 t_s}$$

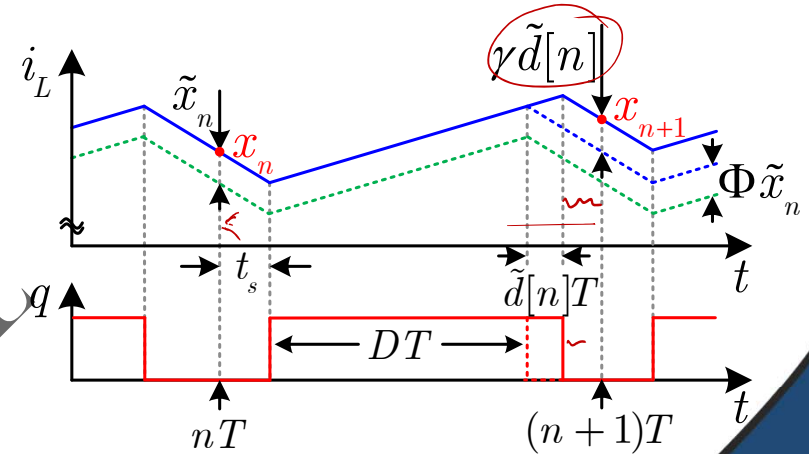
$$\gamma = e^{A_2(T - t_{on} - t_s)} \alpha T = e^{A_2((1-D)T - t_s)} \alpha T$$

Output state-space equation $\tilde{y}_n = C_{eq} \tilde{x}_n$

where $C_{eq} = [\alpha r_c \quad \alpha]$

Using the approximation $e^{AT} \approx 1 + AT$ and Z-transform

$$G_{vd}(z) = \frac{\tilde{v}_{out}(z)}{\tilde{d}(z)}$$

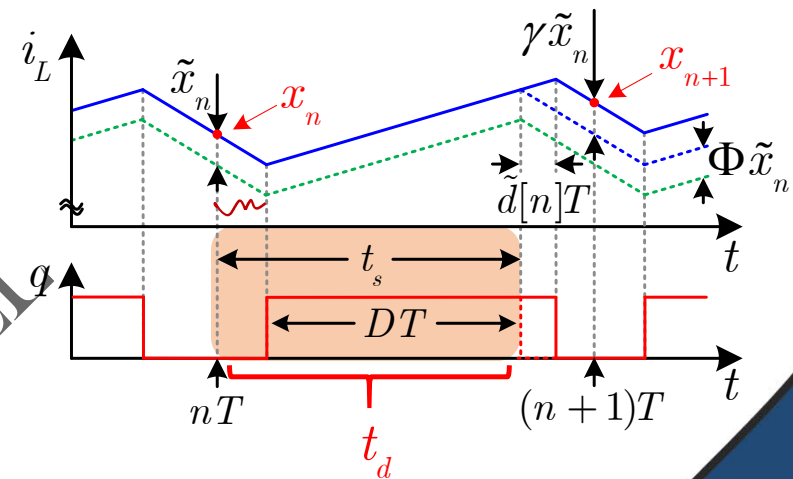


Trailing-edge modulation

Discrete-Time Small-Signal Model Parameters : Trailing-edge PWM

Considering Total delay

$$t_d = DT + t_s$$



Sampling	Duration	Φ	γ
Interval-1	$0 \leq t_s < DT$	$e^{A_1(DT-t_d)} e^{A_2 DT} e^{A_1 t_d}$	$e^{A_1(DT-t_d)} e^{A_2 DT} \alpha T$
Interval-2	$DT \leq t_s < T$	$e^{A_2(T-t_d)} e^{A_1 DT} e^{A_2(t_d-DT)}$	$e^{A_2(T-t_d)} \alpha T$

[For details, refer to “Digital Control of High-frequency Switched ...”, Wiley-IEEE Press, 2015]

Discrete-Time Small Signal Model : Synchronous Buck Converter

For synchronous buck converter $A_1 = A_2 = A$, $B_1 = \begin{bmatrix} \frac{1}{L} & 0 \end{bmatrix}^T$ and $B_2 = \begin{bmatrix} 0 & 0 \end{bmatrix}^T$

$$\tilde{x}_{n+1} = \Phi \tilde{x}_n + \gamma \tilde{d}[n] = e^{A_2(T-t_d)} e^{A_1 D T} e^{A_2(t_d-DT)} \tilde{x}_n + e^{A_2(T-t_d)} \alpha T \tilde{d}[n]$$

where $\alpha = \underbrace{(A_1 - A_2)}_{=0} x_p + \underbrace{(B_1 - B_2)}_{=0} V_{in} = \underline{B_1} V_{in}$

$$\Rightarrow \tilde{x}_{n+1} = e^{AT} \tilde{x}_n + e^{A(T-t_d)} B_1 V_{in} T \tilde{d}[n]$$

Output Voltage $v_o[n] = C_{eq} x_n$ where $C_{eq} = \begin{bmatrix} \alpha r_C & \alpha \end{bmatrix}$

$$\Rightarrow \tilde{v}_o[n] = C_{eq} \tilde{x}_n$$

$e^{A_1 t_1} \cdot e^{A_2 t_2} \neq e^{A_1 t_1 + A_2 t_2}$

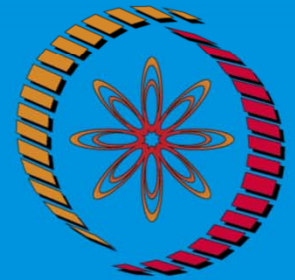
If this holds
 $A_1 A_2 = A_2 A_1$

then
 $e^{A_1 t_1} \cdot e^{A_2 t_2} = e^{(A_1 t_1 + A_2 t_2)}$

$A_1 A_2 = A^2$

CONCLUSION

- Effect of perturbations in discrete-time modeling
- Zero-input and zero-state response
- Discrete-time small-signal modeling



**THANK
YOU !**