

NPTEL ONLINE CERTIFICATION COURSES

DIGITAL CONTROL IN SMPCs AND FPGA-BASED PROTOTYPING

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Module 04: Modeling Techniques and Mode Validation using MATLAB

Lecture 38: Derivation of Discrete-Time Small-Signal Models - II





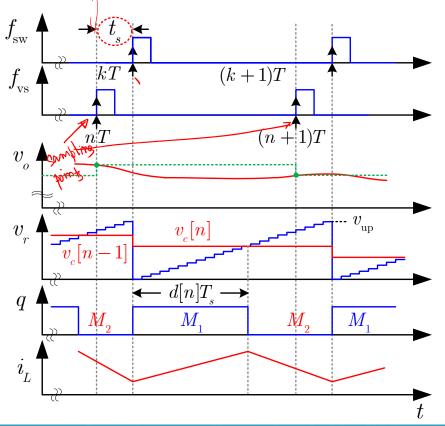
CONCEPTS COVERED

■ Discrete-time large-signal modeling

Formulation of Jacobian matrix

Discrete-time small-signal modeling

Discrete-Time Large Signal Model under Interval-2 Sampling



$$x(t)\Big|_{t=nT} = x_n$$
 $x(t)\Big|_{t=(n+1)T} = x_{n+1}$

Large-Signal Discrete-Time Model

$$= e^{AT}x_n + e^{A(T-t_{\rm on}-t_s)}(e^{At_{\rm on}}-I)A^{-1}B_1v_{\rm in}$$
 ccm buck converter with $A_1 = A_2 = A$ under resisting load



Discrete-Time Small Signal Model

Consider perturbations in $x_n = x_{ss} + \tilde{x}_n$ and $t_{on} = T_{on} + \tilde{t}_{on}$ Applying Taylor series approximation $\tilde{x}_n = \frac{\lambda_n}{\lambda_n} = \frac{\lambda_n}{$

where
$$x_{n+1} = e^{AT}x_n + e^{A(T-t_{on}-t_s)}(e^{At_{on}} - I)A^{-1}B_1v_{in} \triangleq f = \begin{bmatrix} \xi_1 \\ \xi_2 \end{bmatrix}$$

[S. Kapat, "An Analytical Approach of Discrete-Time ..." IEEE APEC, 2021]



ton -> control input

$$x_{n+1} = \underbrace{e^{AT}}_{n} x_{n} + \underbrace{e^{A(T-t_{\text{on}}-t_{s})}(e^{At_{\text{on}}} - I)A^{-1}B_{1}v_{\text{in}}}_{\text{f}} \triangleq f$$

$$x_{n+1} = f_{zi} + f_{zs}$$

where

where
$$f_{zi} = e^{AT} x_n, \quad f_{zs} = e^{A(T - t_{
m on} - t_s)} (e^{At_{
m on}} - I) A^{-} B_1 v_{
m in}$$

$$\left. \frac{\partial f}{\partial x_n} \right|_{\mathrm{ss}} = \frac{\partial f_{zi}}{\partial x_n} \right|_{\mathrm{ss}} = e^{AT}$$



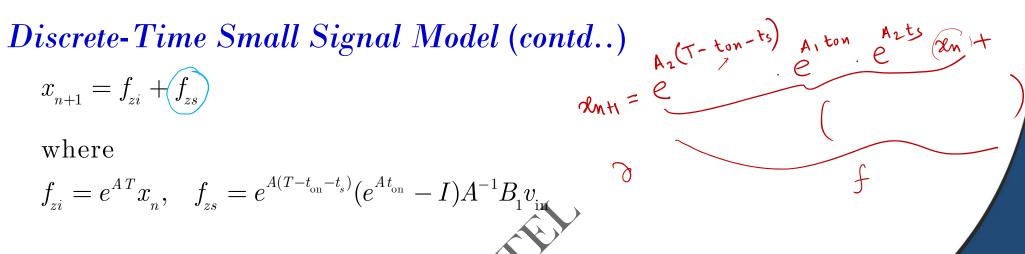
$$x_{n+1} = f_{zi} + f_{zs}$$

where

$$f_{zi} = e^{AT} x_n, \quad f_{zs} = e^{A(T - t_{\text{on}} - t_s)} (e^{At_{\text{on}}} - I) A^{-1} B_1 v_{\text{in}}$$

$$\frac{\partial f}{\partial t_{\rm on}} = \frac{\partial f_{zs}}{\partial t_{\rm on}} = \frac{\partial}{\partial t_{\rm on}} \left(e^{A(T - t_{\rm on} - t_s)} (e^{At_{\rm on}} - 1) A^{-1} B_1 v_{\rm in} \right)$$

$$\frac{\partial f}{\partial t_{\text{on}}} = \frac{\partial}{\partial t_{\text{on}}} \left((e^{\stackrel{A(T-t_s)}{-}} - (e^{\stackrel{A(T-t_{\text{on}}-t_s)}{-}})A^{-1}B_1 v_{\text{in}} \right)$$





Zero state response:

$$\begin{split} f_{\rm zs} &= (e^{A(T-t_{\rm s})} - e^{A(T-t_{\rm on}-t_{\rm s})})A^{-1}B_1v_{\rm in} \\ \Rightarrow \frac{\partial f_{\rm zs}}{\partial t_{\rm on}} &= e^{A(T-t_{\rm on}-t_{\rm s})}B_1v_{\rm in} \\ \Rightarrow \frac{\partial f_{\rm zs}}{\partial t_{\rm on}} \bigg|_{\rm ss} &= e^{A(T-T_{\rm on}-t_{\rm s})}B_1V_{\rm in} \end{split}$$

[S. Kapat, "An Analytical Approach of Discrete-Time ..." IEEE APEC, 2021]



$$\begin{split} \tilde{x}_{n+1} = & \frac{\partial f}{\partial x_n} \Big|_{\text{ss}} \tilde{x}_n + \frac{\partial f}{\partial t_{\text{on}}} \Big|_{\text{ss}} \tilde{t}_{\text{on}} \\ \Rightarrow \tilde{x}_{n+1} = e^{AT} \tilde{x}_n + e^{A(T-T_{\text{on}}-t_s)} B_1 V_{\text{in}} \tilde{t}_{\text{on}} \\ & \text{Again On-time } \ t_{\text{on}} = dT \ \text{ and Total delay } \ t_{\underline{d}} = DT + t_s \\ & \Rightarrow \tilde{t}_{\text{on}} = T\tilde{d} \\ & \Rightarrow \tilde{x}_{n+1} = e^{AT} \tilde{x}_n + e^{A(T-t_d)} B_1 V_{\text{in}} T\tilde{d} \end{split}$$



$$\tilde{x}_{n+1} = A_{\text{eq}} \tilde{x}_n + B_{\text{eq}} \tilde{d}$$

where
$$\left[A_{_{\mathrm{eq}}}=e^{^{A}T}
ight]$$
 and $\left[B_{_{\mathrm{eq}}}=e^{^{A(T-t_{_{d}})}}B_{_{1}}V_{_{\mathrm{in}}}T
ight]$

Output Voltage
$$v_o[n] = C_{eq} x_n$$

where
$$C_{\text{eq}} = \begin{bmatrix} \alpha r_{\!\scriptscriptstyle C} & \alpha \end{bmatrix}$$

$$\Rightarrow \tilde{v}_{_{o}}[n] = C_{_{\mathrm{eq}}}\tilde{x}_{_{n}}$$



Discrete-Time Small Signal Analysis

$$\tilde{x}_{\scriptscriptstyle n+1} = A_{\scriptscriptstyle \rm eq} \tilde{x}_{\scriptscriptstyle n} + B_{\scriptscriptstyle \rm eq} \tilde{d} \ \ {\rm and} \ \ \tilde{v}_{\scriptscriptstyle o}[n] = C_{\scriptscriptstyle \rm eq} \tilde{x}_{\scriptscriptstyle n}$$

Applying Z-transformation

$$\Rightarrow \tilde{x}(z) \times zI = A_{\rm eq} \tilde{x}(z) + B_{\rm eq} \tilde{d}(z) \ \ {\rm and} \ \ \tilde{v}_o(z) = C_{\rm eq} \tilde{x}(z)$$

$$\Rightarrow \tilde{x}(z) = \left(zI - A_{\rm eq}\right)^{-1} B_{\rm eq} \tilde{d}(z)$$

$$\Rightarrow \boxed{\frac{\tilde{v}_o(z)}{\tilde{d}(z)} = C_{\rm eq} \left(zI - A_{\rm eq}\right)^{-1} B_{\rm eq}} \quad \text{Control-to-output transfer function}$$



Comparative Analysis: DT SSM under Interval-2 Sampling

Approach-1

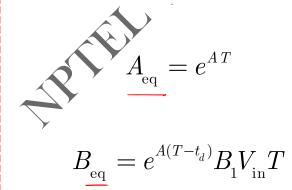
$$\tilde{x}_{\scriptscriptstyle n+1} = \Phi \tilde{x}_{\scriptscriptstyle n} + \gamma \tilde{d}$$

$$\Phi = e^{AT}$$

$$\underline{\gamma} = e^{A\left(T-t_d\right)} \alpha T = e^{A\left(T-t_d\right)} B_1 V_{
m in} T$$

Approach-2

$$\tilde{x}_{\scriptscriptstyle n+1} = A_{\scriptscriptstyle \rm eq} \tilde{x}_{\scriptscriptstyle n} + B_{\scriptscriptstyle \rm eq} \tilde{d}$$



$$B_{\underline{eq}} = e^{A(T - t_d)} B_1 V_{in} T$$



Discrete-Time Small-Signal Model Parameters: Buck Converter

$$\tilde{x}_{n+1} = A_{\rm eq} \tilde{x}_n + B_{\rm eq} \tilde{d}$$

Considering sampling delay t_s

Modulation	$\left(A_{eq}\right)$	B_{eq}
Trailing-edge	e^{AT}	$e^{A((1-D)T-t_S)}BV_{\rm in}T$
Leading-edge	e^{AT}	$-e^{A(DT-t_S)}BV_{\rm in}T$

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CONCLUSION

■ Discrete-time large-signal modeling

• Formulation of Jacobian matrix

Discrete-time small-signal modeling

