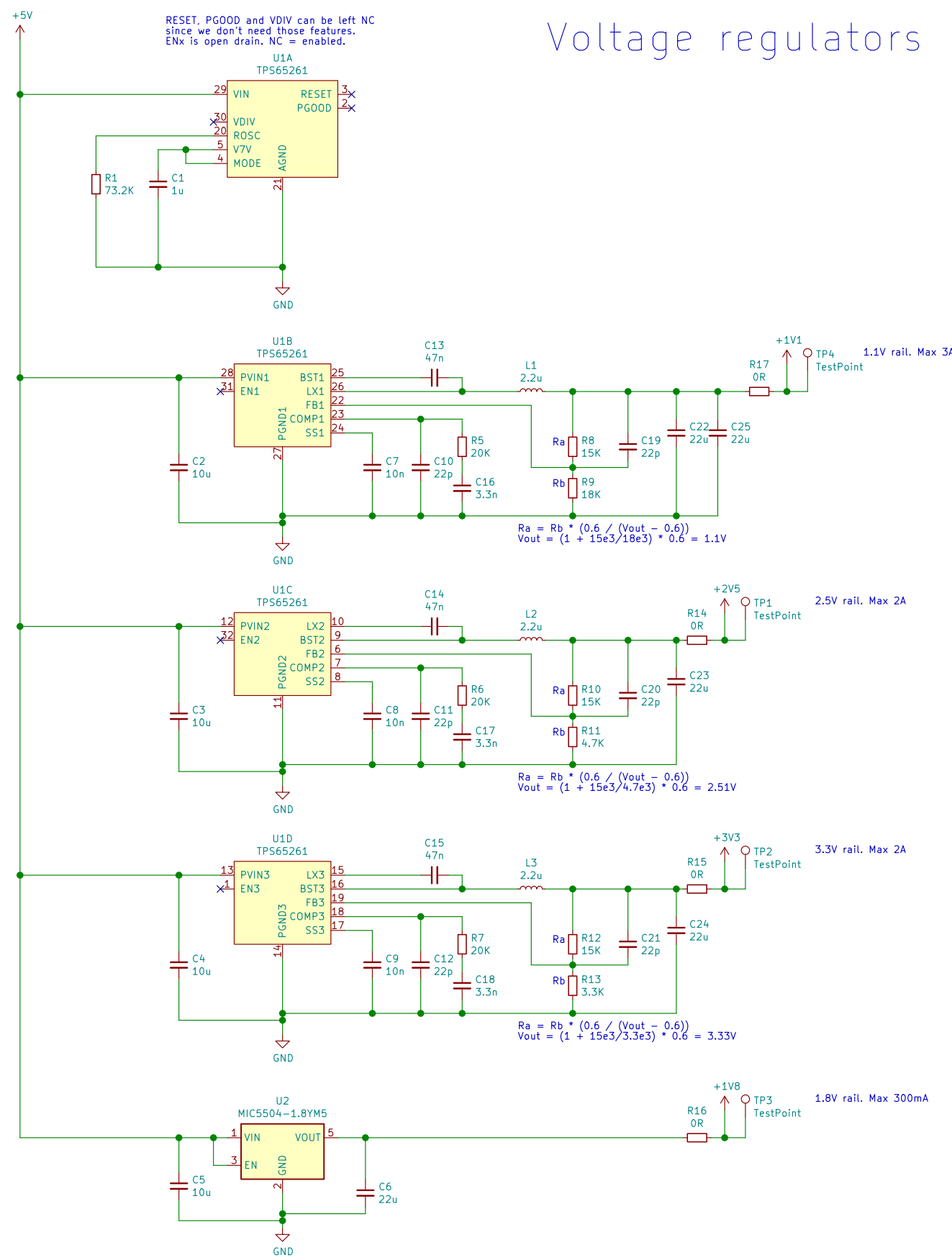
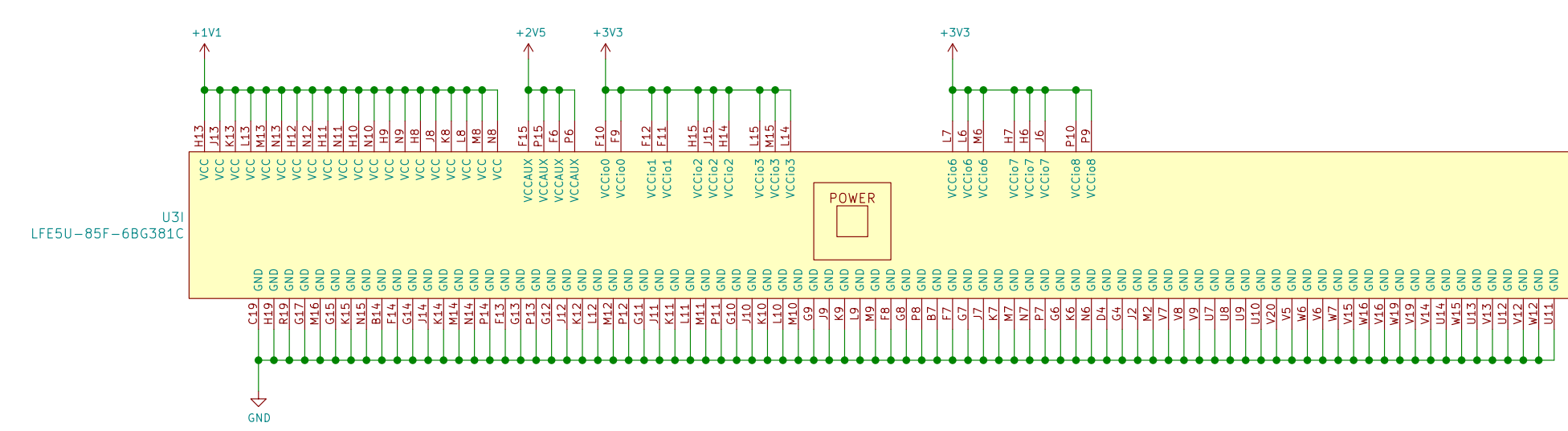


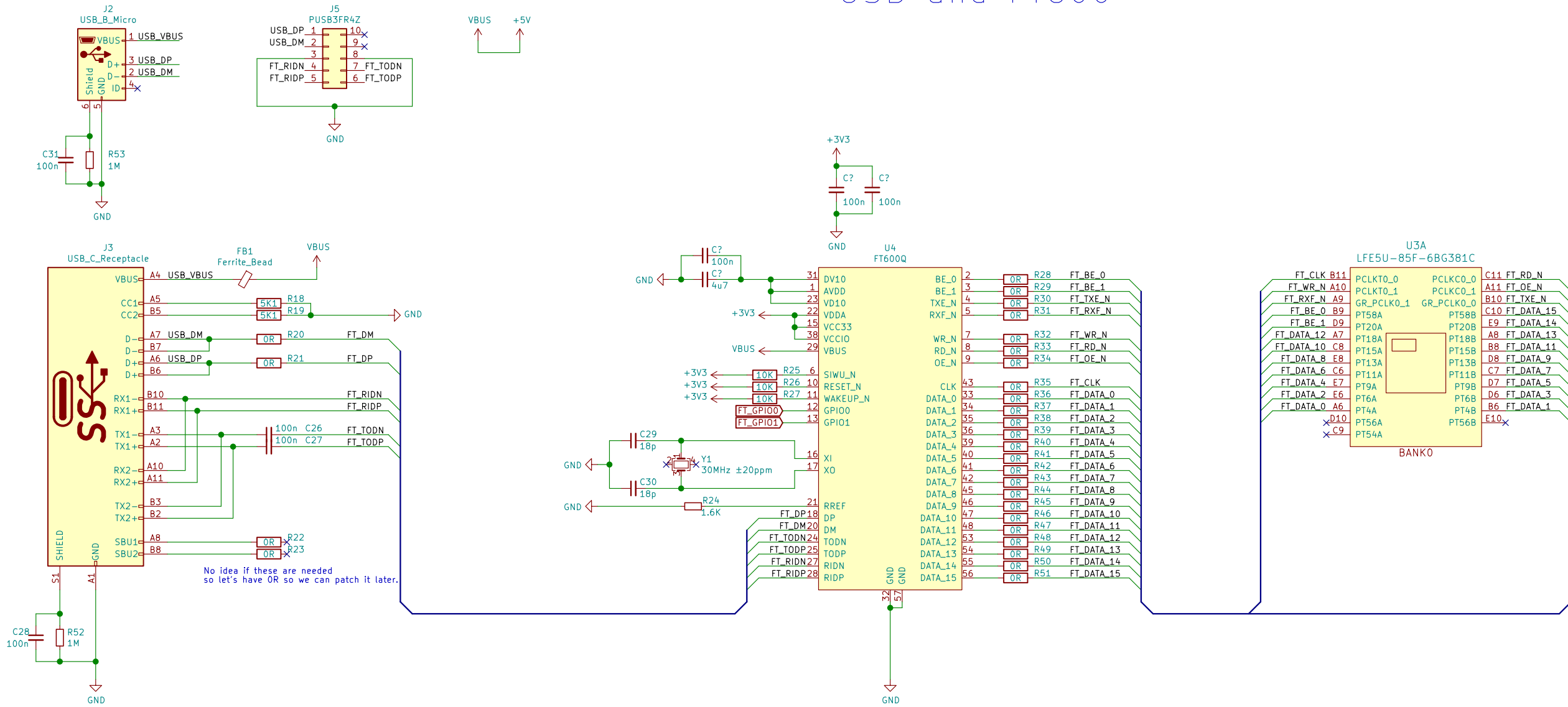
## Voltage regulators



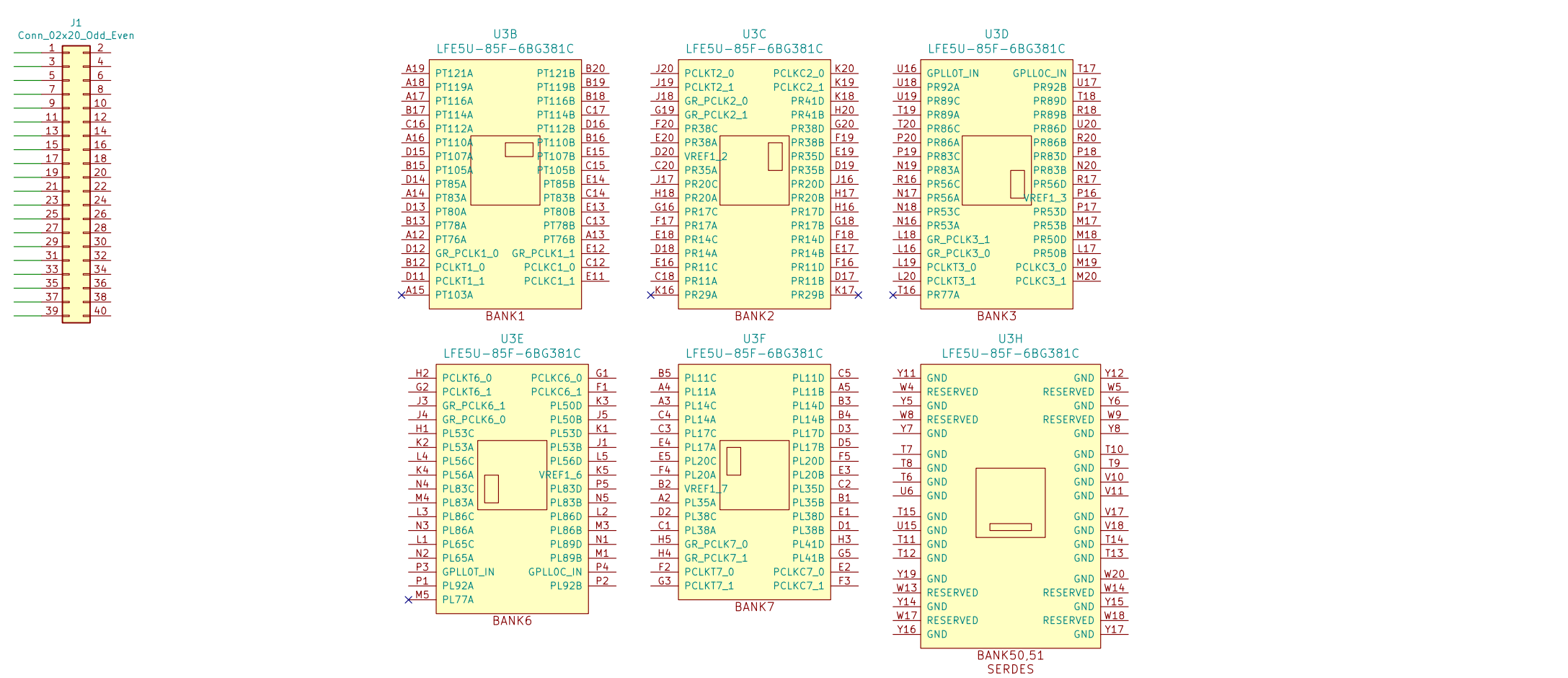
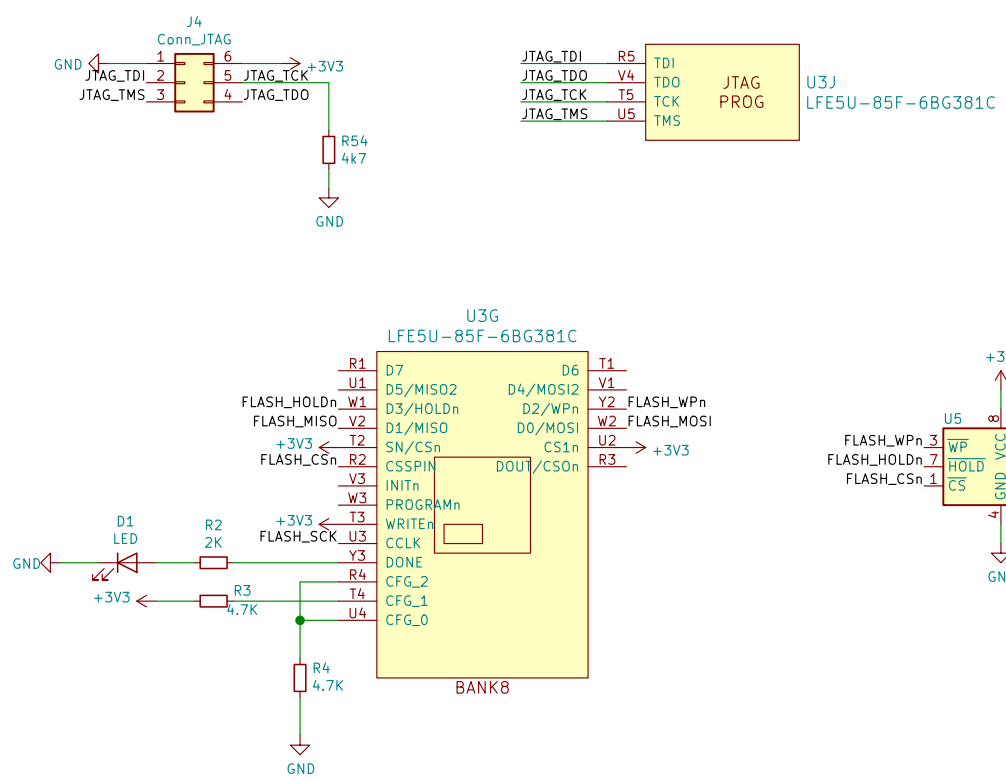
## FPGA Power



## USB and FT600



## JTAG and Flash



- Design goals:
  - High speed transfers with the FT600. Should reach 190MB/s theoretically with 16 bit @ 100MHz.
  - Should work with any variant of LFUSE, i.e. 12F, 25F, 45F, 85F.
  - Low cost BOM. Ideally <\$25 for everything.
  - Loads bitstream from flash
  - TAG should work
  - Bootloader on flash that allows programming the rest of the flash over the FT600, is 128MB/s enough? This should allow people to use the board without any extra hardware, as long as the bootloader is flashed.
  - Even lower cost alternatives:
    - FPGA should work even in the FT600 is unpopulated
    - Cyclone II based or Xilinx on the FT600
    - Micro-UWB. Will only be used for power then is guesst. Cheap and everyone has a Cyclone II even work the FT600.