

Computer Architecture, Spring 2021

Homework 2

B07207063, 心理三, 廖政華

May 8, 2021

Register Name	Type	Width	Bus	MB	AR	AS	SR	SS	ST
alu_in_reg	Flip-flop	32	Y	N	N	N	N	N	N
shreg_reg	Flip-flop	64	Y	N	N	N	N	N	N
state_reg	Flip-flop	2	Y	N	Y	N	N	N	N
counter_reg	Flip-flop	5	Y	N	N	N	N	N	N