Nodal Analysis

Laboratory I

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1 Aim of exercise

The aim of our exercise was to experimentally verify the nodal analysis in RLC circuits. We have achieved it by measuring the voltages on different nodes of the chosen circuits using a dedicated evaluation board and vector voltmeter. The obtained measurement results are compared with analytical calculations.

Apart from the values of potentials in individual nodes of the circuits being measured, we calculated the currents flowing through pointed elements.

2 Nodal analysis - method

Method which we are going to use to solve this circuit is know as "Nodal Analysis by Inspection". In this method we need to construct 3 matrices: $\mathbf{i}$ - current vector, $\mathbf{u}$ - voltage vector(unknown), $\mathbf{G}$ - conductance matrix with sizes respectively $N \times 1$, $N \times 1$, $N-1 \times N-1$

$$\mathbf{Gu} = \mathbf{i}$$

$$\begin{bmatrix} G_{11} & -G_{12} & -G_{13} \\ -G_{21} & G_{22} & -G_{23} \\ -G_{31} & -G_{32} & G_{33} \end{bmatrix} \begin{bmatrix} U_1 \\ U_2 \\ U_3 \end{bmatrix} = \begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix}$$

Where G_{11}, G_{22}, G_{33} are sums of conductance of each branch connected to the node
$G_{12} = G_{21}$, $G_{13} = G_{31}$, $G_{32} = G_{23}$ are sums of conductance of branches between nodes
I_1, I_2, I_3 are sums of current sources entering or exiting node and U_1, U_2, U_3 are unknown voltages that we are trying to find

With simple matrix operation we obtain equation

$$\mathbf{u} = \mathbf{G}^{-1}\mathbf{i}$$

which can be easily calculated

3 theoretical calculations

We are using Python with library NumPy for all calculation

3.1 Circuit A

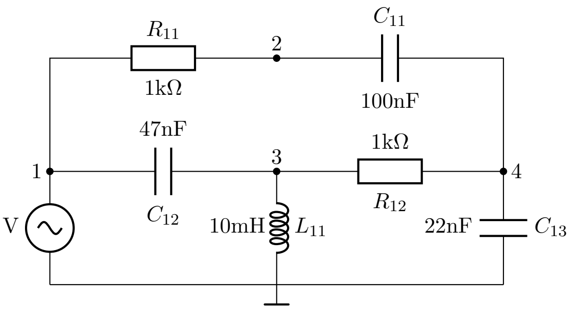

Figure 1: theoretical circuit A

$$\begin{bmatrix} \frac{1}{Z_{C_1 2}} + \frac{1}{Z_{C_1 1}+Z_{R_{11}}} & \frac{-1}{Z_{C_{11}}} & \frac{-1}{Z_{R_{11}}+Z_{C_{11}}} \\ \frac{-1}{Z_{C_{11}}} & \frac{1}{Z_{R_{12}}} + \frac{1}{Z_{C_{12}}} + \frac{1}{Z_{L_{11}}} & \frac{-1}{Z_{R_{12}}} \\ \frac{-1}{Z_{R_{11}}+Z_{C_{11}}} & \frac{-1}{Z_{R_{12}}} & \frac{1}{Z_{C_1 3}} + \frac{1}{Z_{C_1 1}+Z_{R_{11}}} + \frac{1}{Z_{R_{12}}} \end{bmatrix} \begin{bmatrix} U_1 \\ U_3 \\ U_4 \end{bmatrix} = \begin{bmatrix} \frac{-V}{Z_{R_{11}}} - \frac{1}{Z_{C_1 1}+Z_{R_{11}}} \\ \frac{V}{Z_{C_1 2}} \\ \frac{V}{Z_{R_{12}}+Z_{C_{11}}} \end{bmatrix}$$

3.2 Circuit B

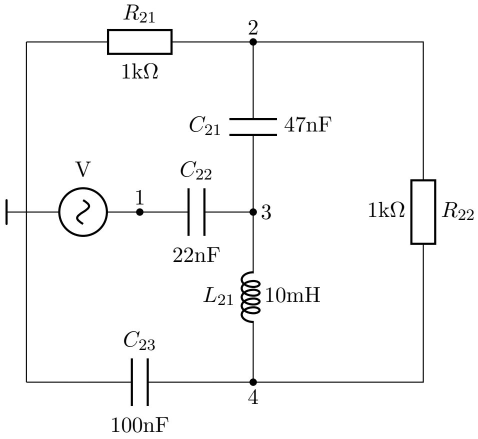

Figure 2: theoretical circuit B

$$\begin{bmatrix} \frac{2}{Z_{R_{21}}} + \frac{1}{Z_{L_{21}}} & \frac{-1}{Z_{C_{21}}} & \frac{-1}{Z_{R_{22}}} \\ \frac{-1}{Z_{C_{21}}} & \frac{1}{Z_{C_{21}}} + \frac{1}{Z_{C_{22}}} + \frac{1}{Z_{L_{21}}} & \frac{-1}{Z_{L_{21}}} \\ \frac{-1}{Z_{R_{22}}} & \frac{-1}{Z_{L_{21}}} & \frac{1}{Z_{C_{23}}} + \frac{1}{Z_{L_{21}}} + \frac{1}{Z_{R_{22}}} \end{bmatrix} \begin{bmatrix} U_2 \\ U_3 \\ U_4 \end{bmatrix} = \begin{bmatrix} 0 \\ \frac{V}{Z_{C_{22}}} \\ 0 \end{bmatrix}$$

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4 real measurements

4.1 Circuit A

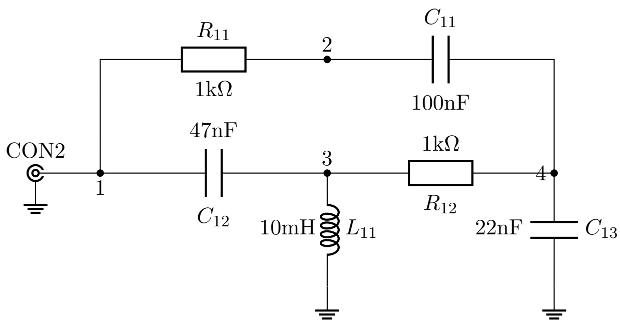

Figure 3: circuit A

Circuit A:			
Freq [kHz]:	Channel 1 [V]:	Channel 2 [V]:	Angle[°]:
Node 1:			
1kHz	1.117	1.115	
5kHz	1.122	1.119	
9kHz	1.121	1.119	
Node 2:			
1kHz	1.117	0.830	-19.5
5kHz	1.122	0.338	14.0
9kHz	1.121	1.342	-11.7
Node 3:			
1kHz	1.117	0.043	140,1
5kHz	1.122	0.952	135.0
9kHz	1.121	1.864	28.6
Node 4:			
1kHz	1.117	0.422	37.3
5kHz	1.122	0.493	43.9
9kHz	1.121	1.302	13.6

Table 1: evaluation board measurements for Circuit A

4.2 Circuit B

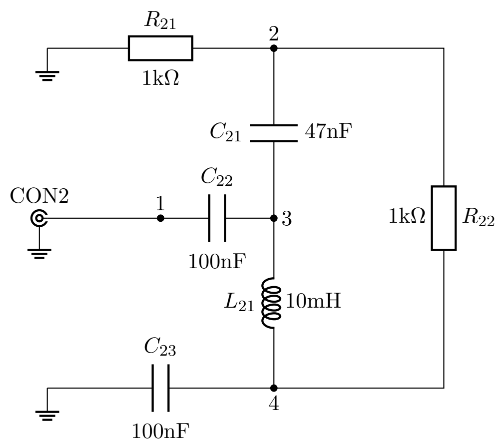

Figure 4: circuit B

Circuit B:			
Freq [kHz]:	Channel 1 [V]:	Channel 2 [V]:	Angle[°]:
Node 1:			
1kHz			
5kHz			
9kHz			
Node 2:			
1kHz	1.117	0.250	28.1
5kHz	1.122	0.245	-42.9
9kHz	1.121	0.921	68.0
Node 3:			
1kHz	1.117	0.486	22.6
5kHz	1.122	0.332	69.0
9kHz	1.121	1.279	24.0
Node 4:			
1kHz	1.117	0.502	20.6
5kHz	1.122	1.077	-13.5
9kHz	1.121	0.503	-114.5

Table 2: evaluation board measurements for Circuit B

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