

Figure 1: circuit A

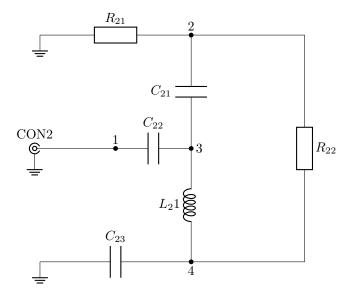


Figure 2: circuit B