

# Stylophone

555 timer

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# 1 Introduction

## 1.1 Stylophone

Stylophone is pocket synthesizer which uses voltage-controlled oscillator controlled with stylus which closes circuit in different position on metal keyboard on printed circuit board.



Figure 1: Stylophone 2007

## 1.2 Goal

Voltage controlled oscillator compatible with octave per volt standard, generation of saw tooth and square wave, vibrato effect and supplied with single pole supply.

## 1.3 History

Stylophone was invented by Brian Jarvis of Dubreq Studios in 1967 and initially was produced from 1968 to 1975, but in 2007 production of stylophone was relunched. Version of stylophone from 2007 used digital electronics unlike original but in 2020 Dubreq released new analog version of stylophone which uses 555 timer integrated circuit as its heart.

Number of widely known artists used stylophone in the past like The White Stripes, David Bowie or Jarvis Cocker. These days stylophone can be heard in various genres or in full stylophone covers of popular songs.

## 1.4 555 integrated circuit

555 integrated circuit, designed in 1971 by Hans Camenzind, is said to be the most popular integrated circuit ever made. 555 can be used in many configuration like: oscillators, schmitt triggers, SR latches and many more.

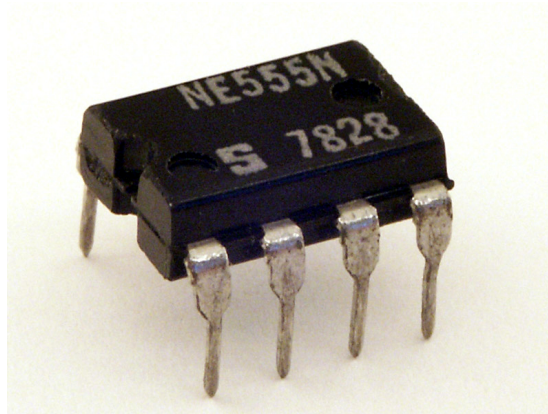


Figure 2: Signetics NE555N

Output of 555 IC can be either high or low it is dependent on state of flip-flop which is controller with 2 comparators, when voltage on pin 2(trigger) is smaller than  $\frac{1}{3}$  of supply voltage comparator sets flip-flop and output is high and stays that way until flip-flop is reset by pin 4(reset) or when voltage on pin 6(threshold) is greater than  $\frac{2}{3}$  of supply voltage.

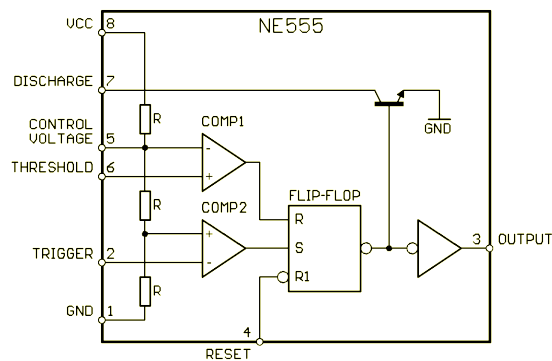


Figure 3: Signetics NE555 internal block diagram

## 2 Design

### 2.1 VCO

In our circuit we are going to utilize capacitor as integrator and integrate current flowing in and out capacitor.

This way we can generate many different waveforms like sawtooth or triangle depending on speed of charging and discharging of the capacitor.

In example circuit from documentation resistance was used to control charging and discharging of capacitor which doesn't satisfies us, we want to use voltage therefor instead of resistors we are going to used bipolar junction transistors.

### 2.1.1 Capacitor

$$i = C \frac{dV_c}{dt} \quad (1)$$