Title: **Q-TAG**

Function: UWB DW3210

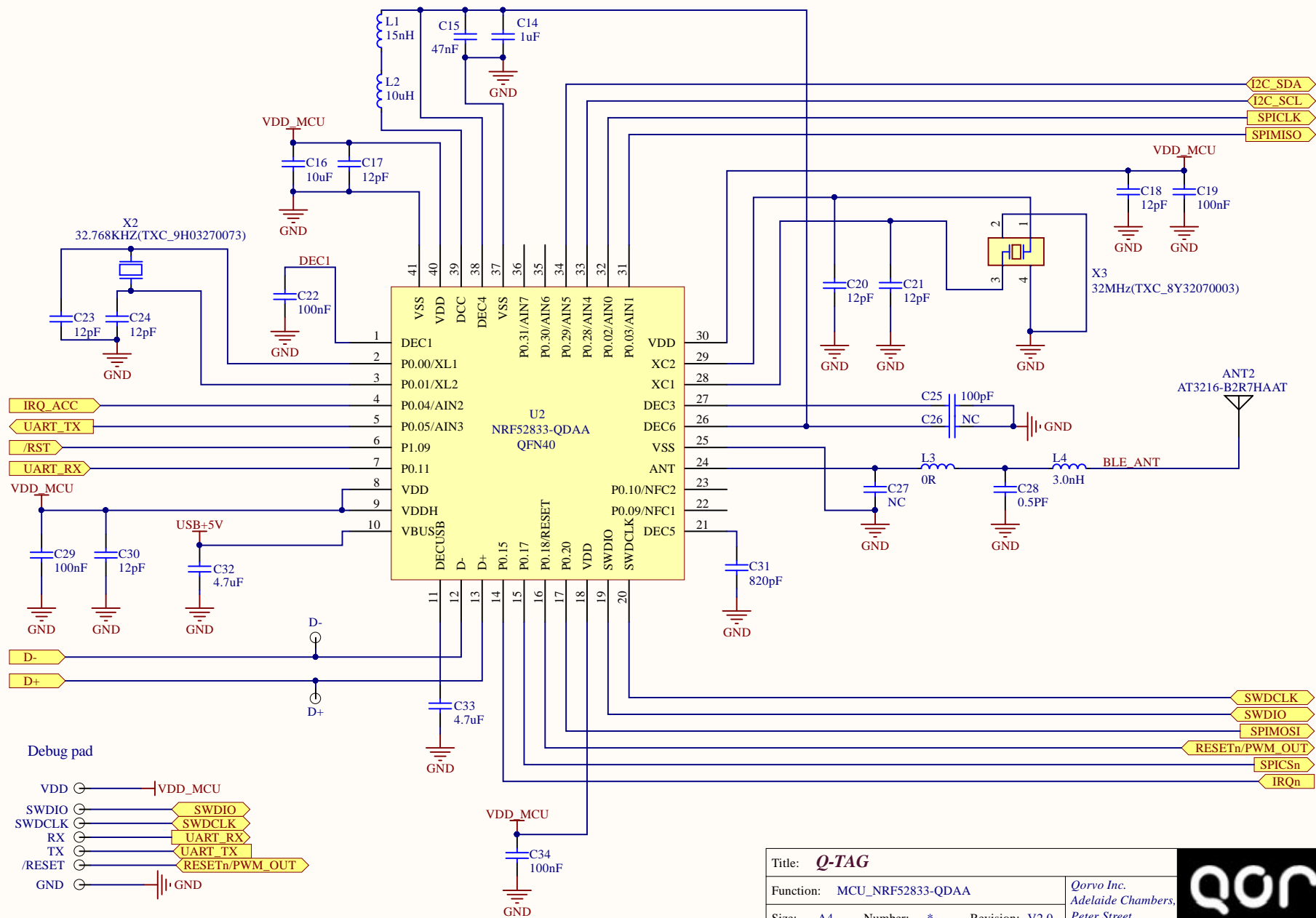
Size: A4 Number: * Revision: V2.0


Date: 2021/9/23 Time: 18:15:47 Sheet: 1 of 6

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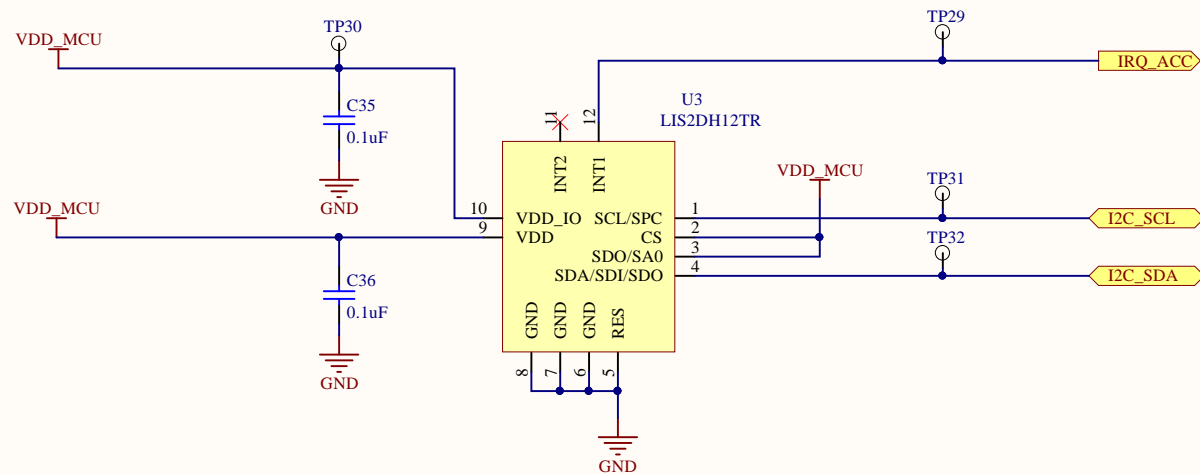
qorvo
all around you




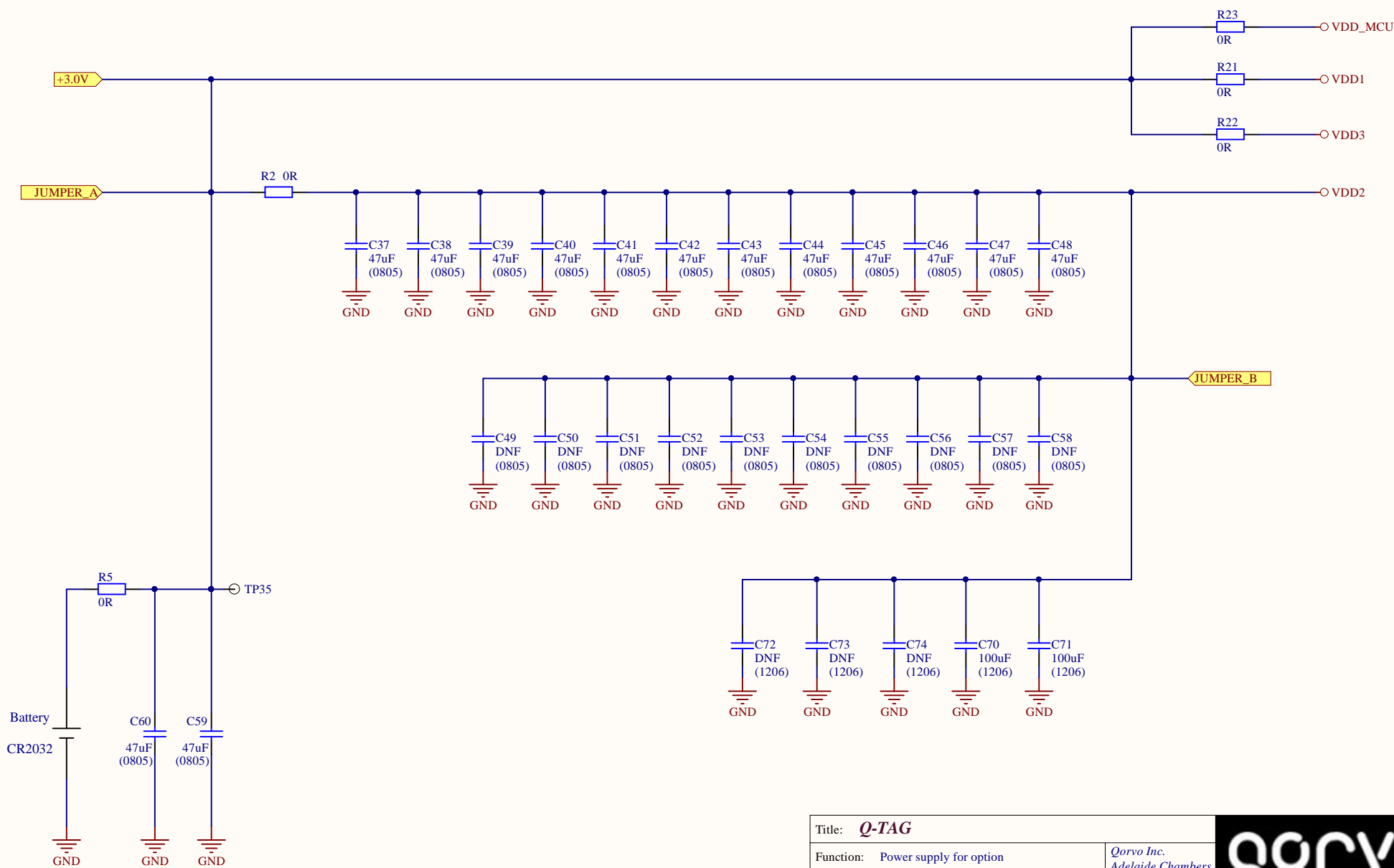
Title: Q-TAG			
Function: MCU_NRF52833-QDAA		Qorvo Inc. Adelaide Chambers, Peter Street, Dublin,D08 T6YA Ireland.	
Size: A4	Number: *	Revision: V2.0	
Date: 2021/9/23	Time: 18:15:47	Sheet: 2 of 6	
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3-Axes Accelerometer

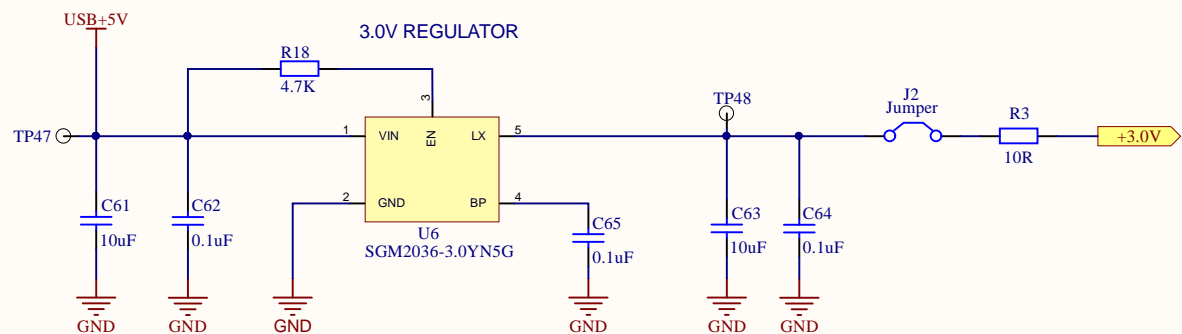
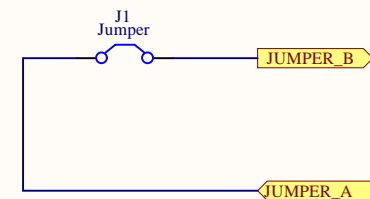
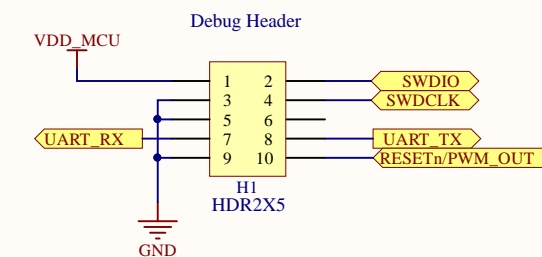
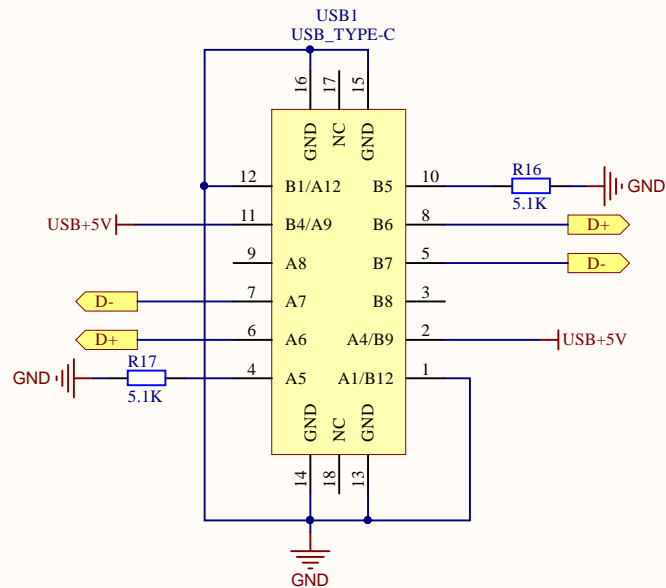


Title: Q-TAG				
Function: 3-axis Accelerometer			Qorvo Inc. Adelaide Chambers, Peter Street, Dublin,D08 T6YA Ireland.	
Size: A4	Number: *	Revision: V2.0		
Date: 2021/9/23	Time: 18:15:47	Sheet: 3 of 6		
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Title: <i>Q-TAG</i>				
Function: Power supply for option			<i>Qorvo Inc. Adelaide Chambers, Peter Street, Dublin,D08 T6YA Ireland.</i>	
Size: A4	Number: *	Revision: V2.0		
Date: 2021/9/23	Time: 18:15:47	Sheet: 4 of 6		
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Hole
Hole
Hole
Hole
Hole

Title: Q-TAG				
Function: Debug interface			<i>Qorvo Inc. Adelaide Chambers, Peter Street, Dublin,D08 T6YA Ireland.</i>	
Size: A4	Number: *	Revision: V2.0		
Date: 2021/9/23	Time: 18:15:47	Sheet: 5 of 6		
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V1.0

- Initial version
- Q-TAG OPTION-1,OPTION-2 and OPTION-3 are using same Q-TAG PCB V1.0, The PSU can be a selectable.

V1.1

- Removed vias from sensitive region beneath DW3210.
- Changed C37-C48(47uF) in common parts.
- Added R21&R22 0 OHM for connect VDD1&VDD3 to battery directly.
- Added Tantalum Capacitor C70&C71 100uF(TLKNK107M010#2500) for VDD2 on bottom side.

V1.2


- Used 0 OHM instead of 100 OHM for R2.
- Used 0 OHM instead of RB520S-30 for D1.
- Removed RB520S-30 for D2.

V1.3

- Used 2.0 OHM instead of 0 OHM for R2,prevent the LDO going into protection mode during power up via USB.

V2.0

- Removed OPTION-2 and OPTION-3 both in the schematic and PCB layout.
- Re-matching BLE antenna. Used 3nH instead of 2.2nH for L4,0.5pF instead of 1.2pF for C28,0R instead of 4.7nH for L3,Removed C27.
- Added more 10pcs of the 47uF(C49---C58) for option,Added more 3pcs of the 100uF(C72---C74) for option.
- Used a LDO SGM2036-3.0YN5G instead of XC9263A75CMR-G for power supply from Debug PCB.

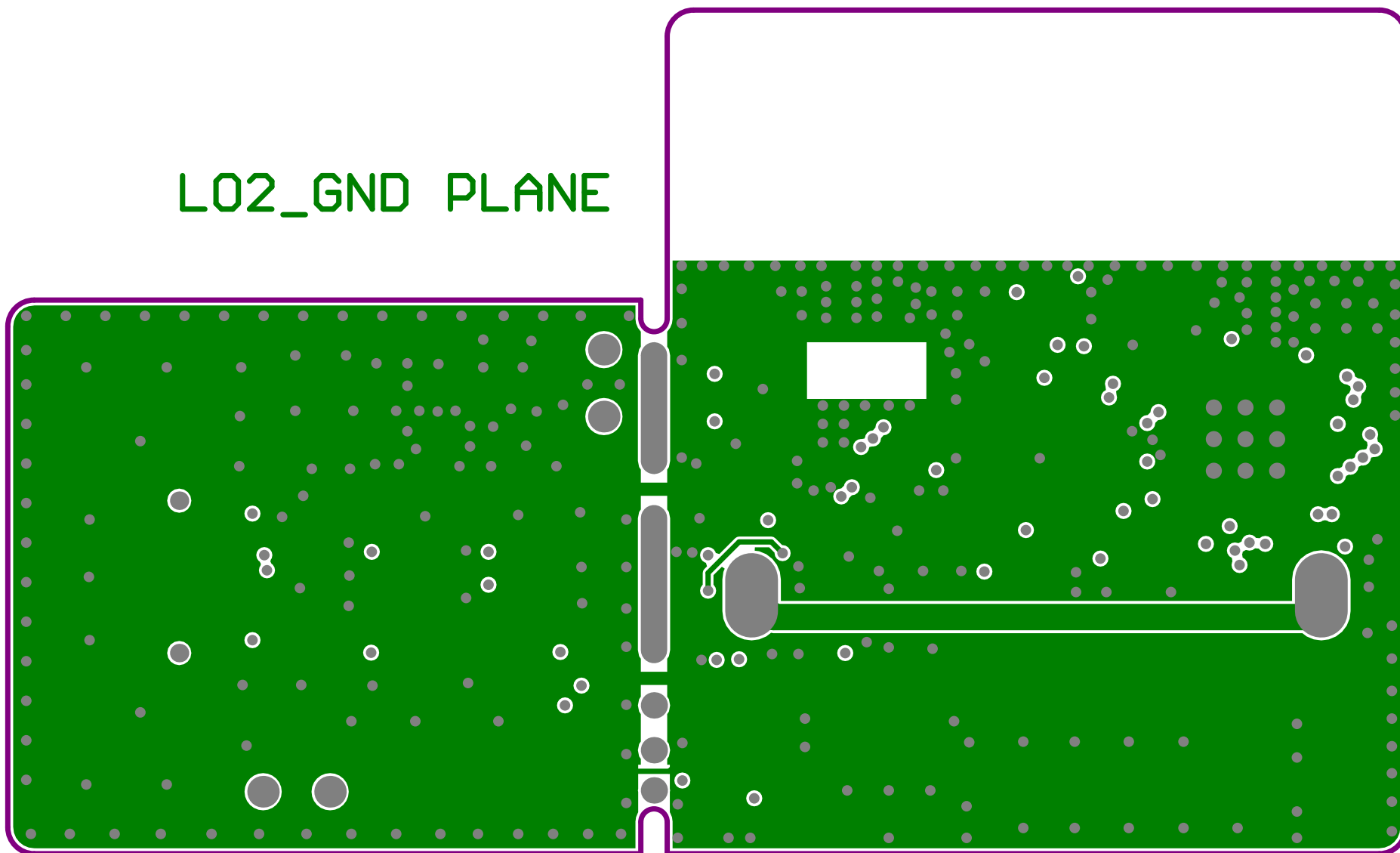
Title: <i>Q-TAG</i>				
Function: History			<i>Qorvo Inc. Adelaide Chambers, Peter Street, Dublin,D08 T6YA Ireland.</i>	
Size: A4	Number: *	Revision: V2.0		
Date: 2021/9/23	Time: 18:15:47	Sheet: 6 of 6		
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The image shows a detailed PCB layout for the L01_TOP. The board is red with white traces. A large red cutout is located at the top center. Various components are labeled, including resistors (R16, R17, R18), capacitors (C61, C62, C63, C64, C65, C66, C67, C68, C69, C70, C71, C72, C73, C74, C75, C76, C77, C78, C79, C80, C81, C82, C83, C84, C85, C86, C87, C88, C89, C90, C91, C92, C93, C94, C95, C96, C97, C98, C99, C100), and a USB1 connector. A large red component is labeled L01. The layout includes a USB1 connector, a USB2 connector, and a USB3 connector. The board is labeled L01_TOP in the top left corner. The Qorvo logo and Q-TAG V2.0 are in the top right corner.

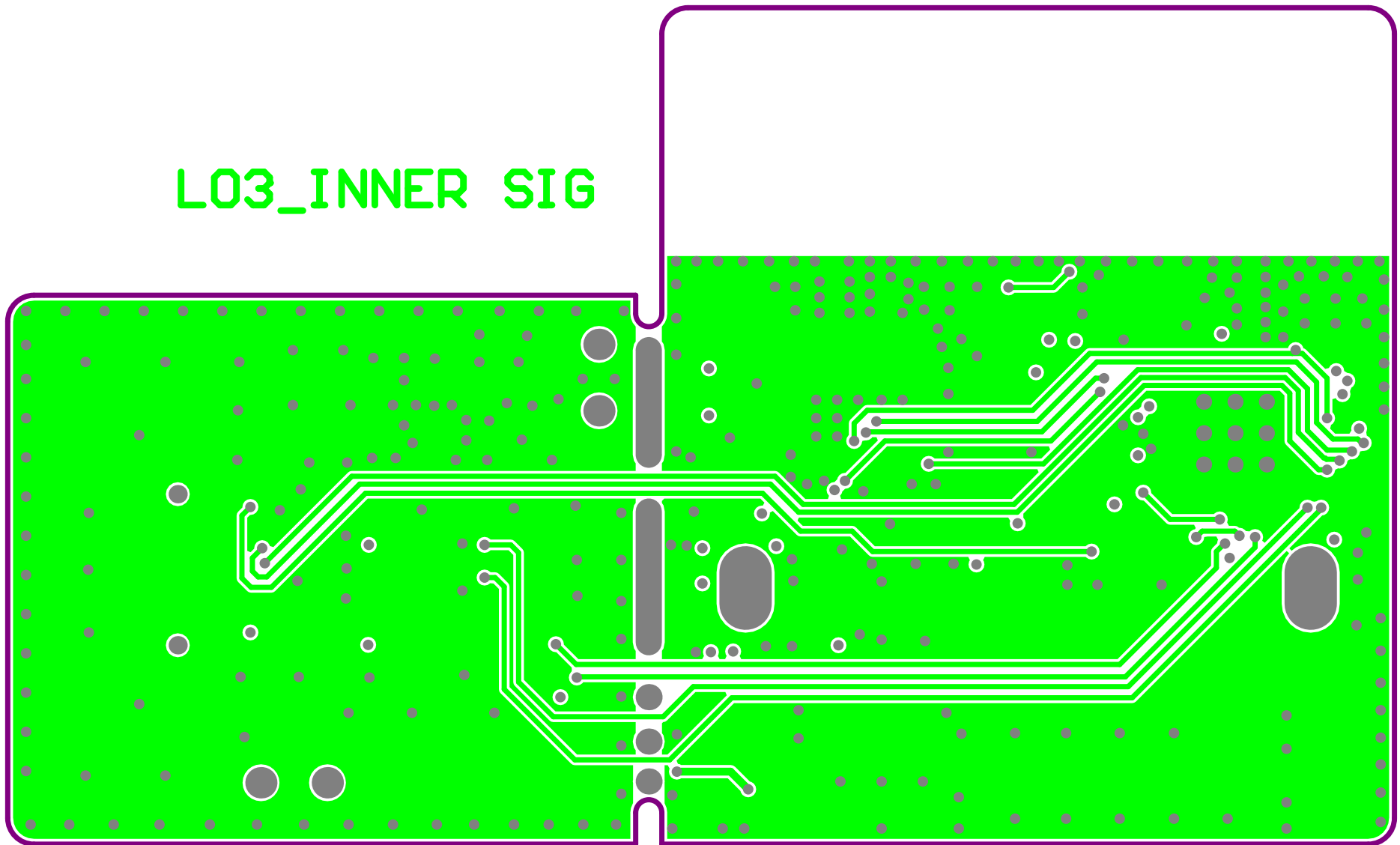
Qorvo
Q-TAG V2.0

The graphic features a large, stylized red 'Q' shape in the upper left corner. Below it, a red circuit board layout is shown with various components and traces. A green rectangular box highlights a specific area on the board, likely indicating the location of the Q-TAG V2.0 component. The overall design is clean and modern, with a focus on the red and green color scheme.

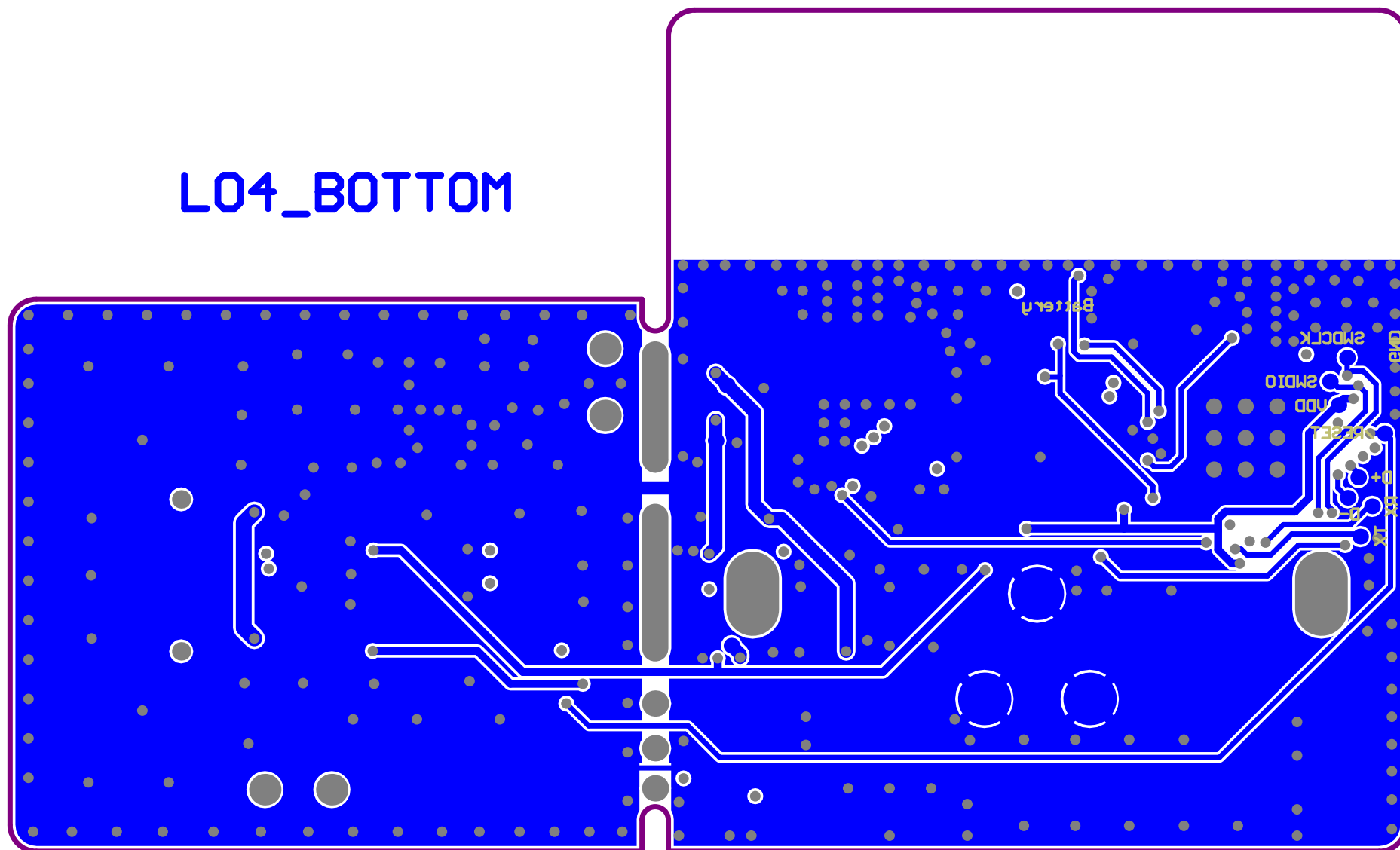
L02_GND PLANE



L03_INNER SIG



L04_BOTTOM



Assembly Top

Qorvo
Q-TAG V2.0

ANT2

USB1

J1

H1

SWDIO
SWCLK
NC
TX
VREF

TP1 TP2 TP3 TP4 TP5 TP6 TP7 TP8 TP9 TP10 TP11 TP12 TP13 TP14 TP15 TP16 TP17 TP18 TP19 TP20 TP21 TP22 TP23 TP24 TP25 TP26 TP27 TP28 TP29 TP30 TP31 TP32

U1 U2 U3 U6

R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13 R14 R15 R16 R17 R18 R19 R20 R21 R22

C1 C2 C3 C4 C5 C6 C7 C8 C9 C10 C11 C12 C13 C14 C15 C16 C17 C18 C19 C20 C21 C22 C23 C24 C25 C26 C27 C28 C29 C30 C31 C32 C33 C34 C35 C36 C37 C38 C39 C40 C41 C42 C43 C44 C45 C46 C47 C48 C49 C50 C51 C52 C53 C54 C55 C56 C57 C58 C59 C60 C61 C62



QORVO
Q-TAG V2.0

ANT2

VDD
GND
GND
RX
GND

SWDIO
SWCLK
NC
TX
/RST

53.00

- 28.00

32.00

Assembly Bottom

