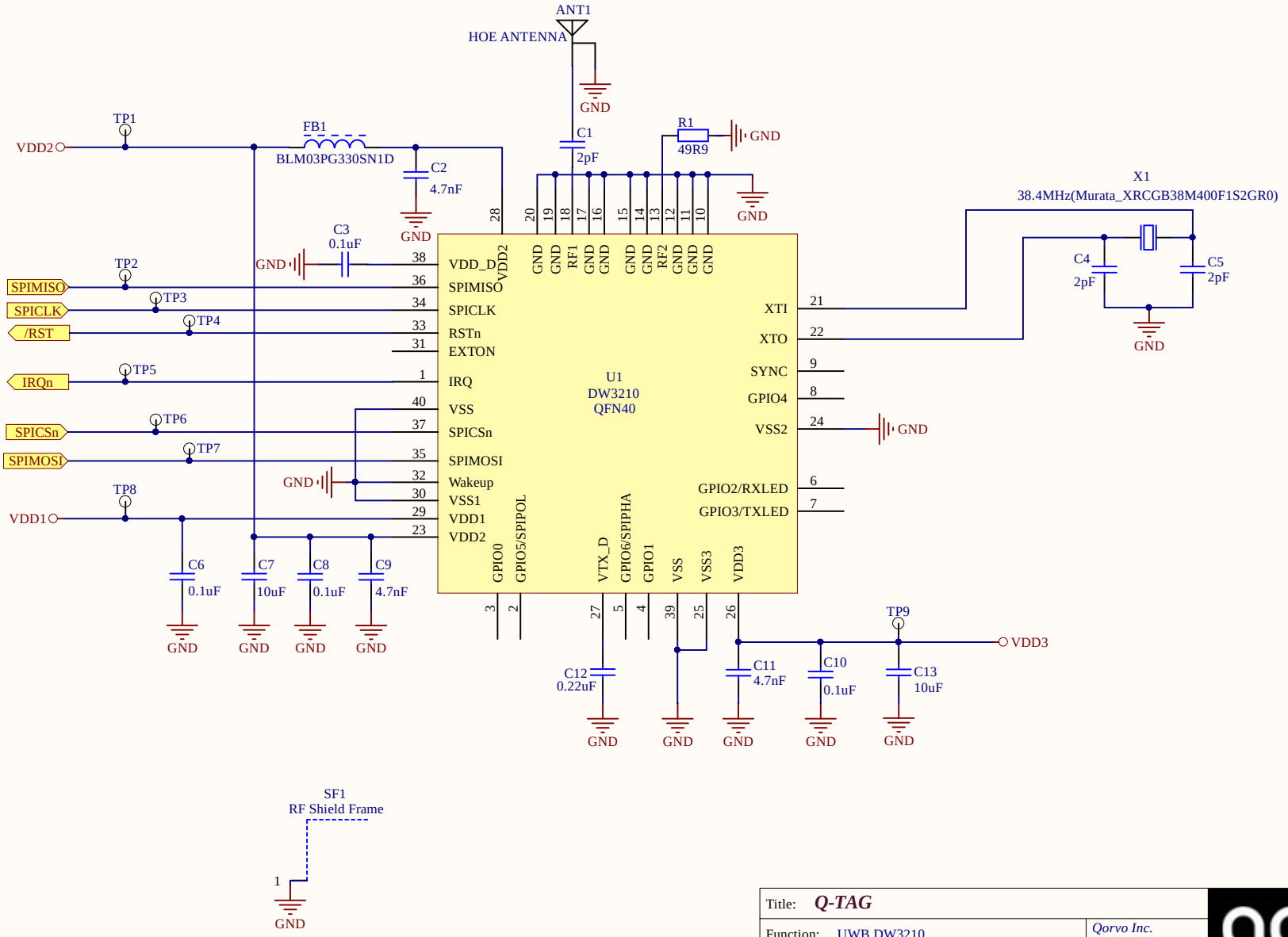
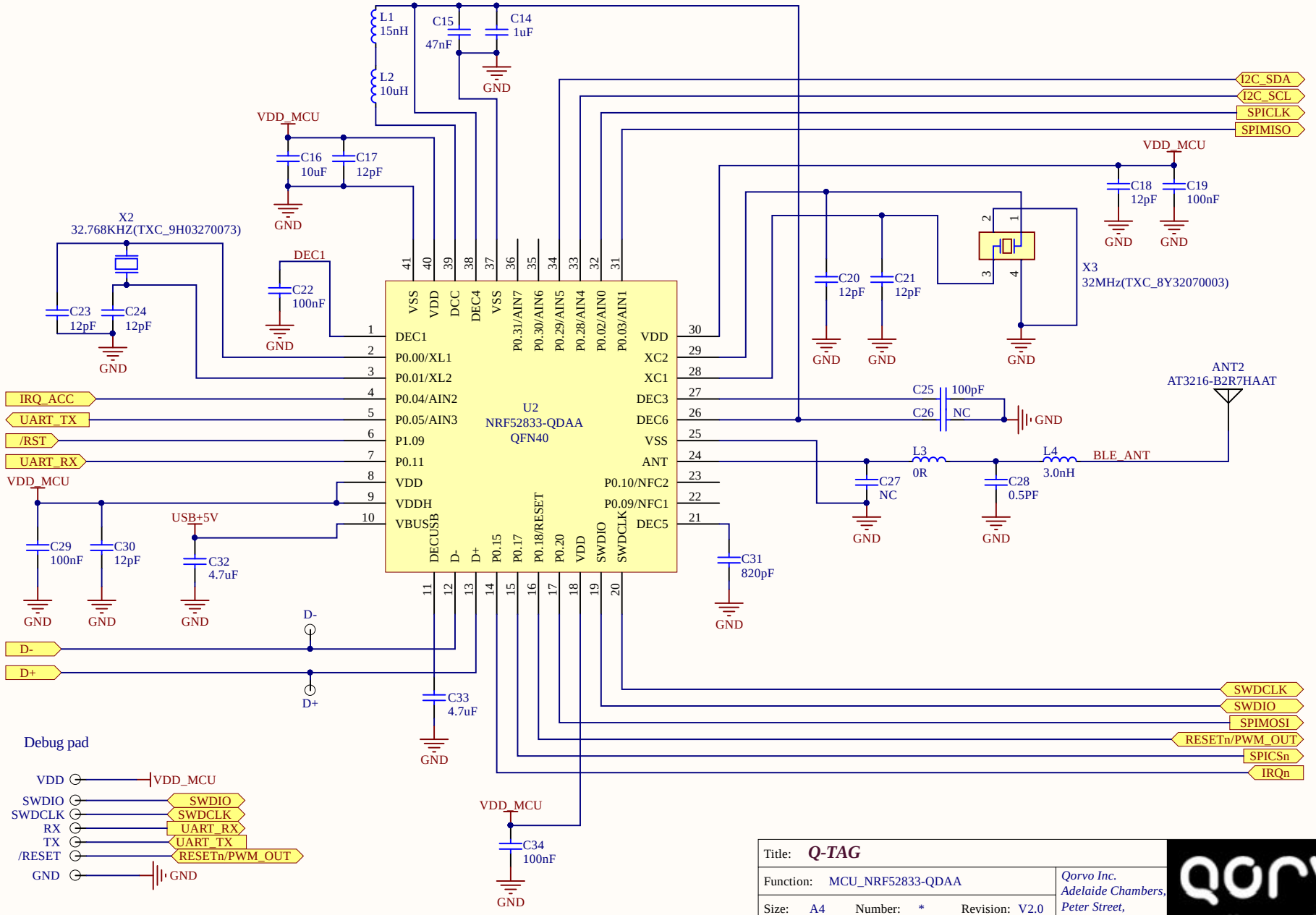


Title:	*Q-TAG*		
Function:	UWB DW3210		
Size:	A4	Number: *	Revision: V2.0
Date:	2021/9/23	Time: 18:15:47	Sheet: 1 of 6

Qorvo Inc.
Adelaide Chambers,
Peter Street,
Dublin,D08 T6YA
Ireland.



Title:	*Q-TAG*		
Function:	MCU_NRF52833-QDAA		
Size:	A4	Number: *	Revision: V2.0
Date:	2021/9/23	Time: 18:15:47	Sheet: 2 of 6

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Dublin,D08 T6YA
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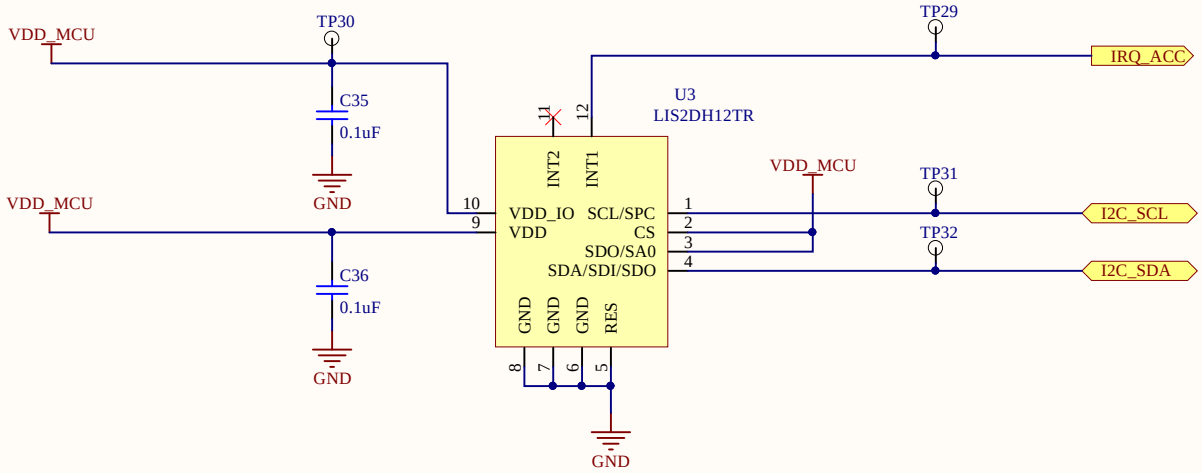
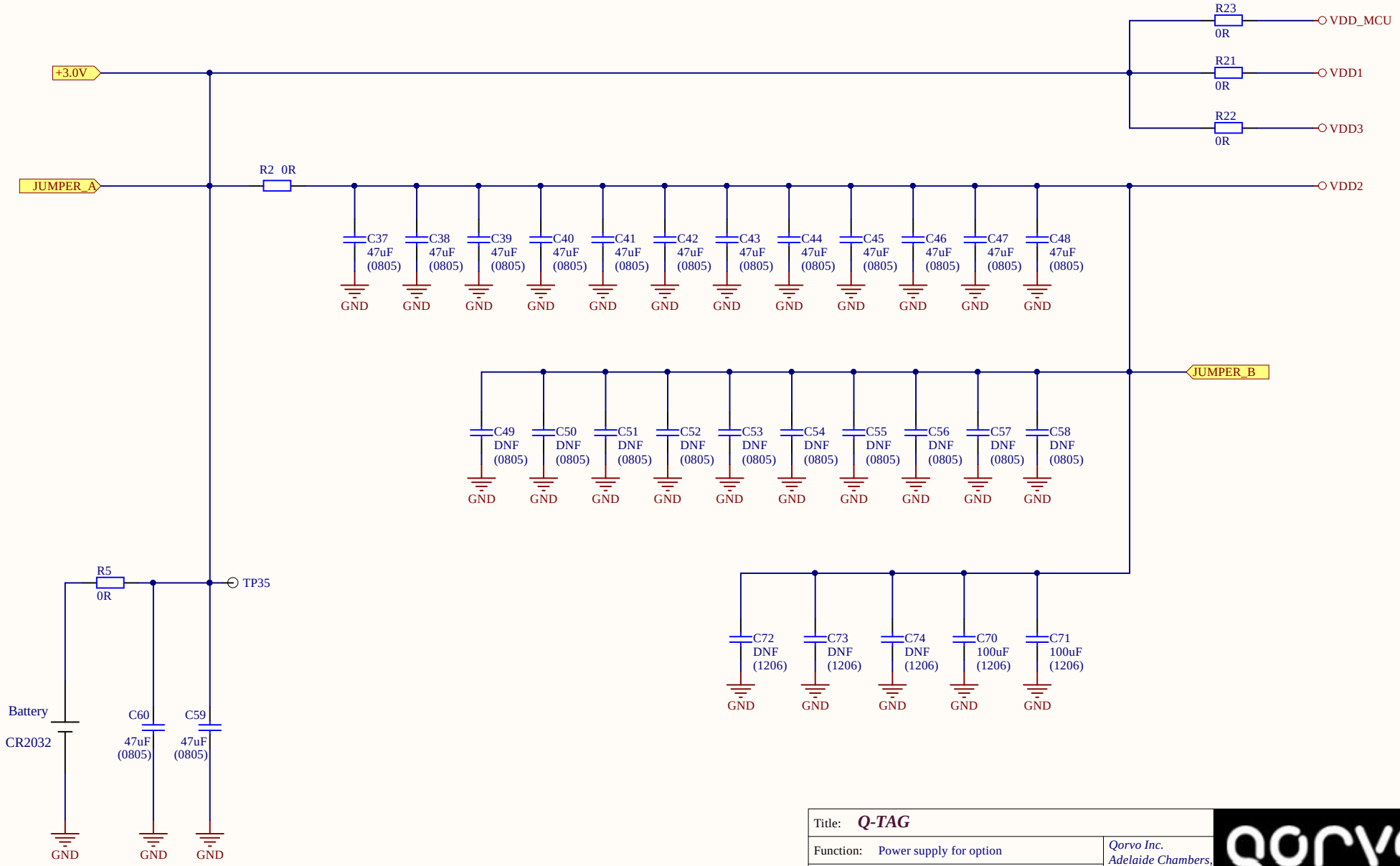
all around you

3-Axes Accelerometer



Title:	*Q-TAG*		
Function:	3-axis Accelerometer		*Qorvo Inc.* *Adelaide Chambers,* *Peter Street,* *Dublin,D08 T6YA* *Ireland.*
Size: A4	Number: *	Revision: V2.0	
Date: 2021/9/23	Time: 18:15:47	Sheet: 3 of 6	



Title:	*Q-TAG*		
Function:	Power supply for option		
Size:	A4	Number: *	Revision: V2.0
Date:	2021/9/23	Time: 18:15:47	Sheet: 4 of 6

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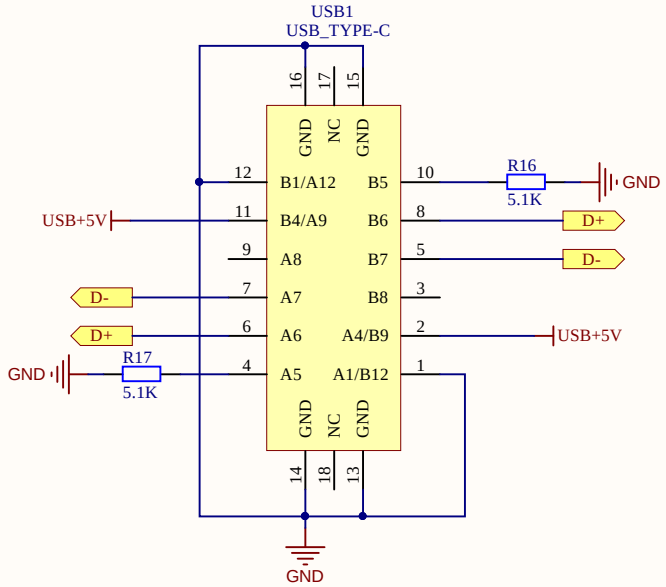
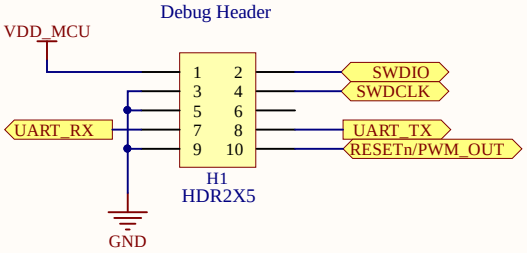
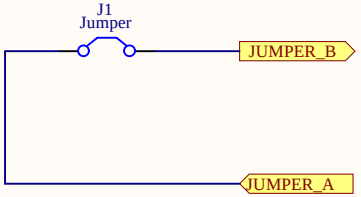
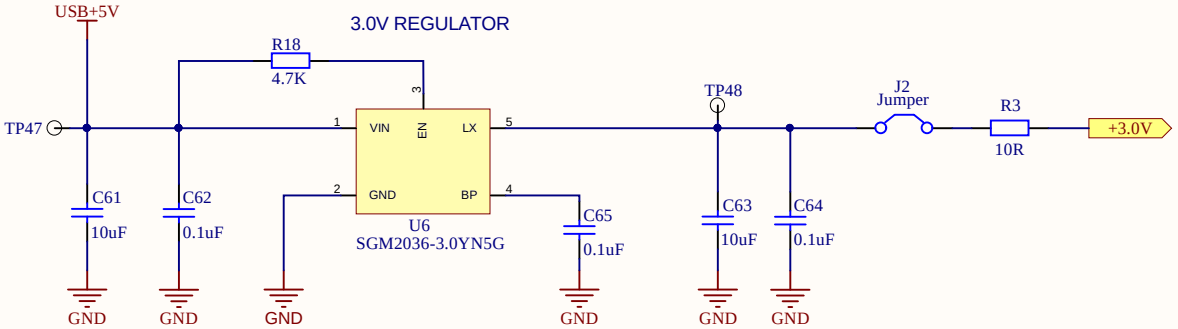
Ireland.









Hole Hole Hole Hole Hole

Title:	*Q-TAG*
Function:	Debug interface
Size: A4	Number: * Revision: V2.0
Date: 2021/9/23	Time: 18:15:47 Sheet: 5 of 6

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V1.0

- Inital version
- Q-TAG OPTION-1,OPTION-2 and OPTION-3 are using same Q-TAG PCB V1.0, The PSU can be a selectable.

V1.1

- Removed vias from sensitive region beneath DW3210.
- Changed C37-C48(47uF) in common parts.
- Added R21&R22 0 OHM for connect VDD1&VDD3 to battery directly.
- Added Tantalum Capacitor C70&C71 100uF(TLNK107M010#2500) for VDD2 on bottom side.

V1.2

- Used 0 OHM instead of 100 OHM for R2.
- Used 0 OHM instead of RB520S-30 for D1.
- Removed RB520S-30 for D2.

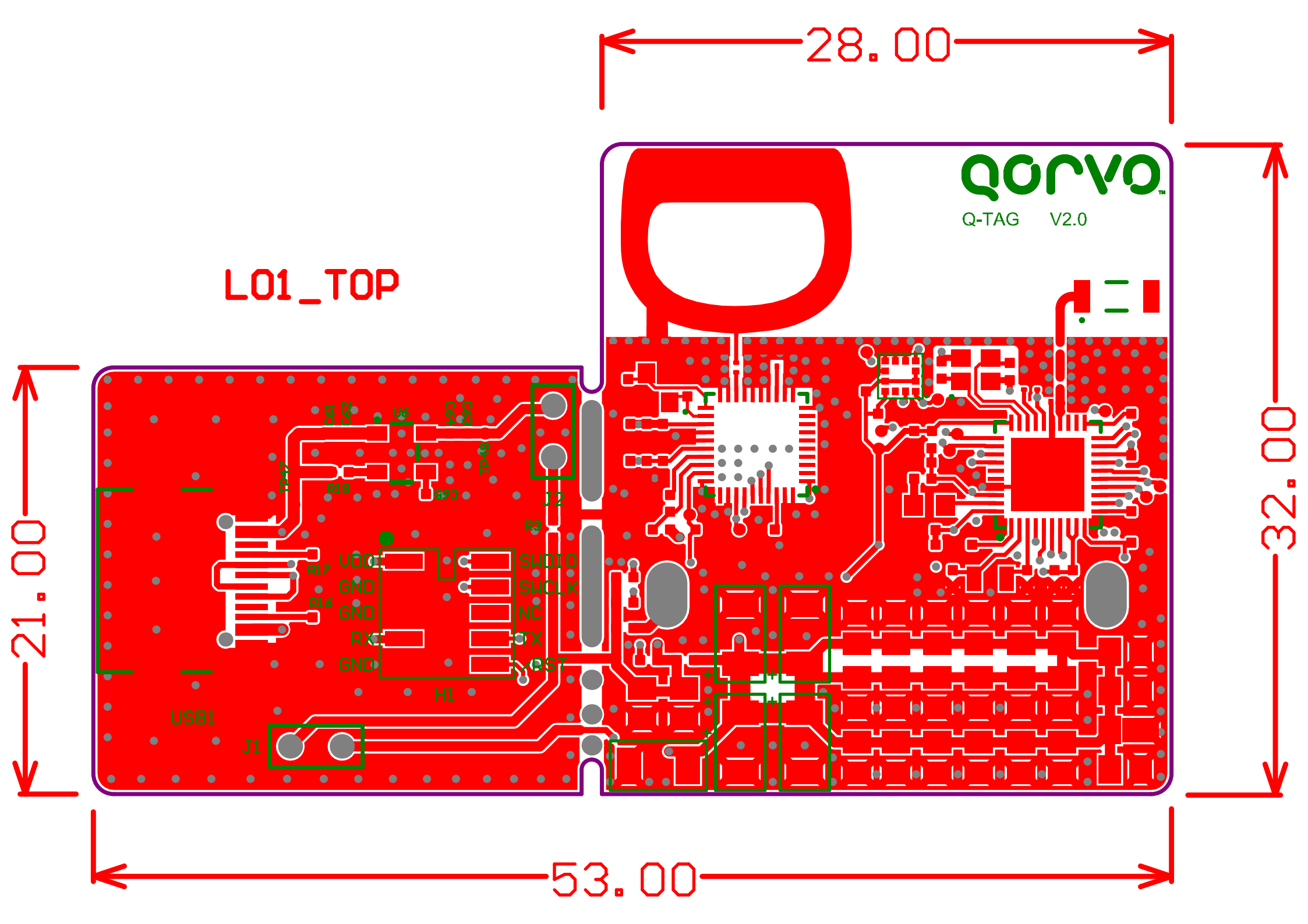
V1.3

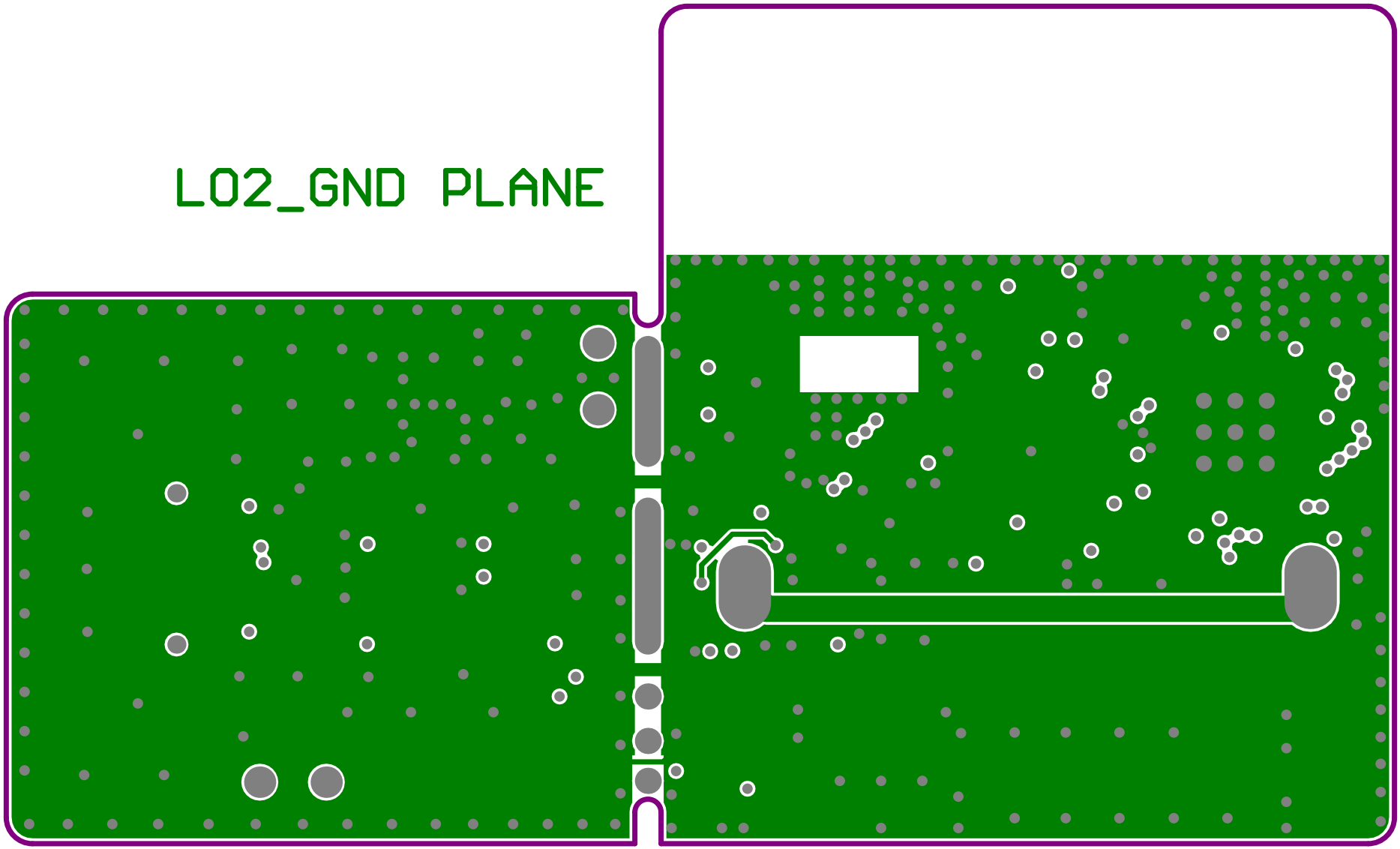
- Used 2.0 OHM instead of 0 OHM for R2,prevent the LDO going into protection mode during power up via USB.

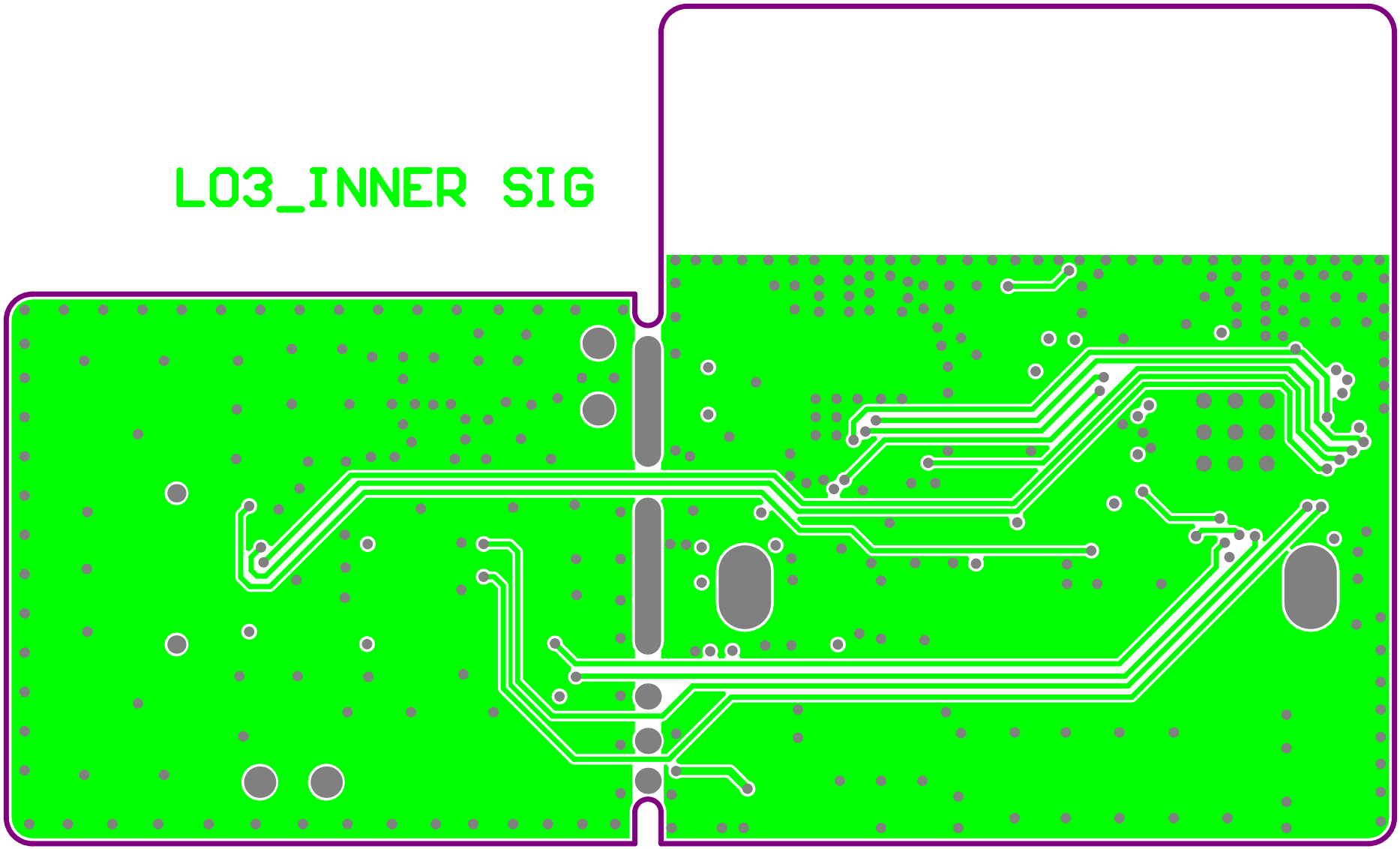
V2.0

- Removed OPTION-2 and OPTION-3 both in the schematic and PCB layout.
- Re-matching BLE antenna. Used 3nH instead of 2.2nH for L4,0.5pF instead of 1.2pF for C28,0R instead of 4.7nH for L3,Removed C27.
- Added more 10pcs of the 47uF(C49---C58) for option,Added more 3pcs of the 100uF(C72---C74) for option.
- Used a LDO SGM2036-3.0YN5G instead of XC9263A75CMR-G for power supply from Debug PCB.

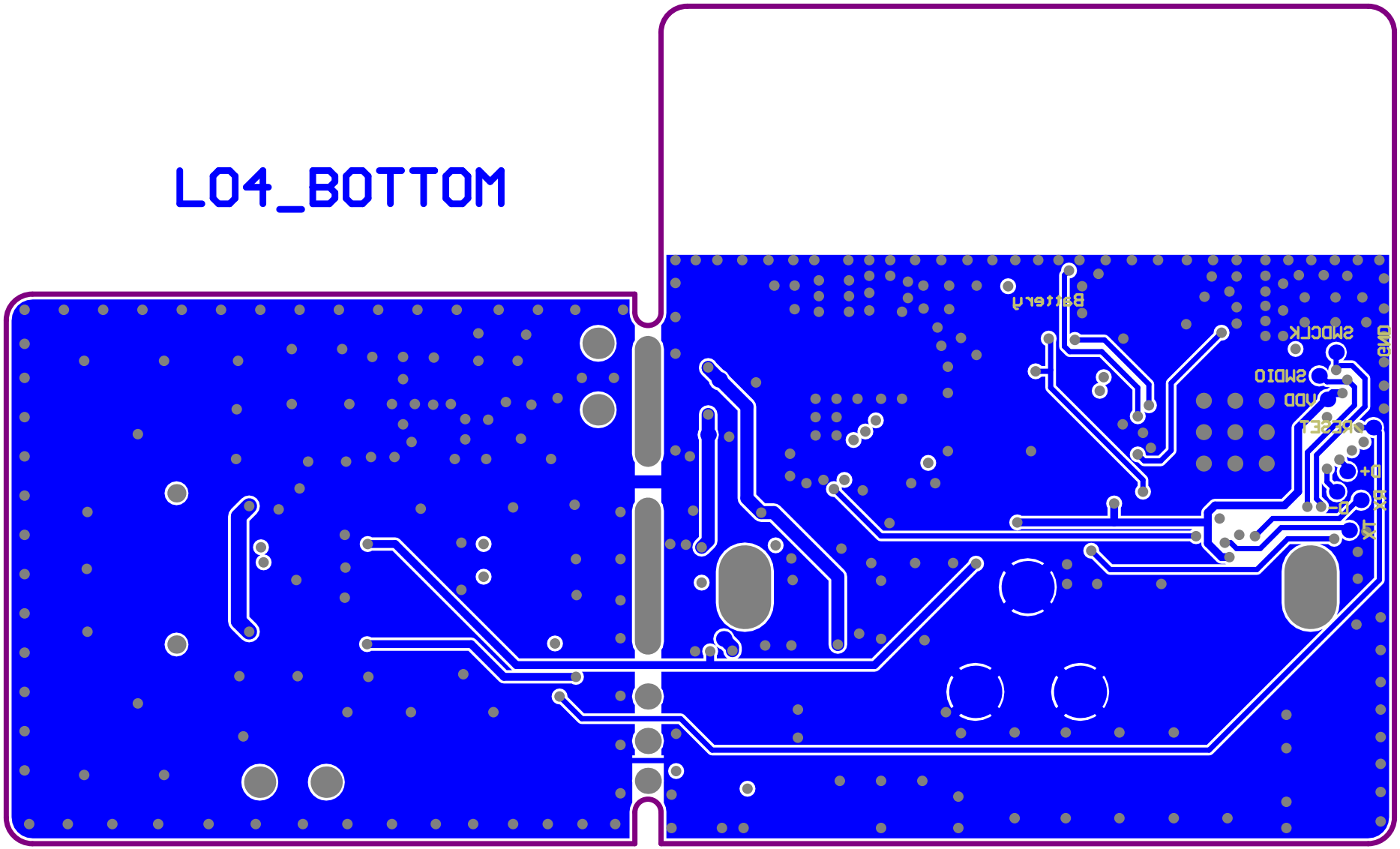
Title: ***Q-TAG***			
Function: History			*Qorvo Inc.* *Adelaide Chambers,* *Peter Street,* *Dublin,D08 T6YA* *Ireland.*
Size: A4	Number: *	Revision: V2.0	
Date: 2021/9/23	Time: 18:15:47	Sheet: 6 of 6	

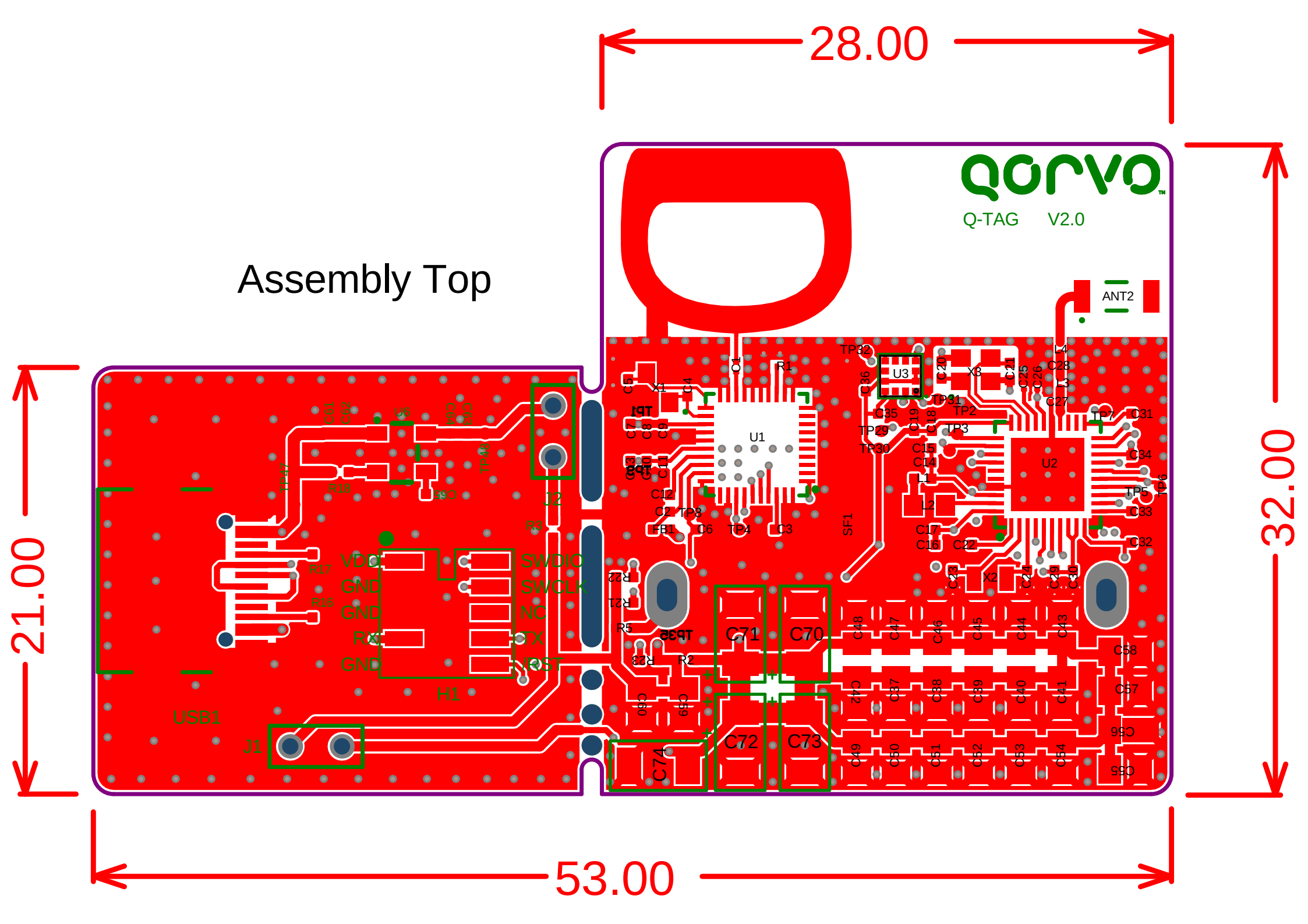

28.00
32.00
21.00
53.00
L01_TOP
qorvo
Q-TAG V2.0
U6
R18
J2
R3
R17
R16
VDD
GND
GND
RX
GND
SWDIO
SWCLK
NC
TX
RST
H1
USB1
J1


LO2_GND PLANE


L03_INNER SIG

L04_BOTTOM




28.00
32.00
21.00
53.00
Assembly Top
Q-TAG V2.0
ANT2
U1
U2
U3
J1
J2
H1
USB1
VDD
GND
GND
RX
GND
SWDIO
SWCLK
NC
TX
RST
C71
C70
C72
C73
C74

Assembly Bottom