

Pmod connector on Nexys 4 DUT

November 14, 2016

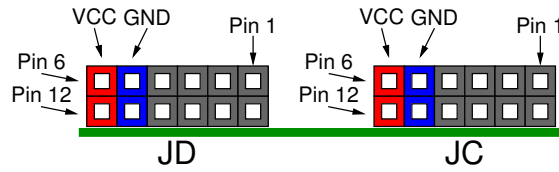


Figure 1: Pmod Connectors as Viewed from Front

Table 1: Pin Configuration on Nexys3							
Pin	LOC	Net	Comment	Pin	LOC	Net	Comment
JC1	K2	DUTClock	GCLK20	JD1	H4	reset	
JC2	E7	src_ready		JD2	H1	dst_ready	
JC3	J3	datain[3]		JD3	G1	dataout[3]	
JC4	J4	datain[1]		JD4	G3	dataout[1]	
JC5			GND	JD4		GND	
JC7	K1	src_read		JD7	H2		
JC8	E6			JD8	G4	dst_write	
JC9	J2	datain[2]		JD9	G2	dataout[2]	
JC10	G6	datain[0]		JD10	F3	dataout[0]	

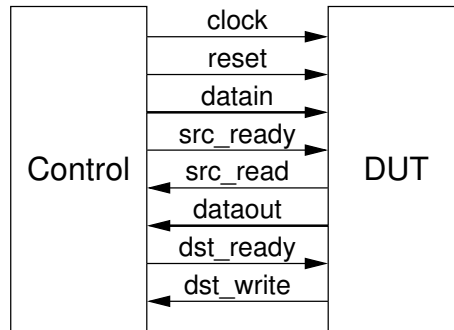


Figure 2: Connections between Control and DUT