

Pmod On Nexys3 Controller and DUT

November 14, 2016

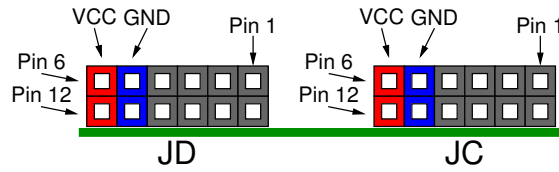


Figure 1: Pmod Connectors as Viewed from Front

Table 1: Pin Configuration on Nexys3							
Pin	LOC	Net	Comment	Pin	LOC	Net	Comment
JC1	H3	DUTClock	GCLK20	JD1	G11	reset	
JC2	L7	src_ready		JD2	F10	dst_ready	
JC3	K6	datain[3]		JD3	F11	dataout[3]	
JC4	G3	datain[1]		JD4	E11	dataout[1]	
JC5			GND	JD5		GND	
JC7	G1	src_read		JD7	D12		
JC8	J1			JD8	C12	dst_write	
JC9	J6	datain[2]		JD9	F12	dataout[2]	
JC10	F2	datain[0]		JD10	E12	dataout[0]	

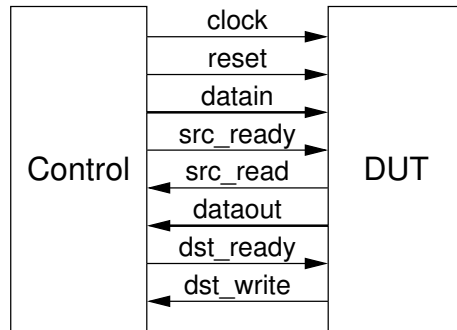


Figure 2: Connections between Control and DUT