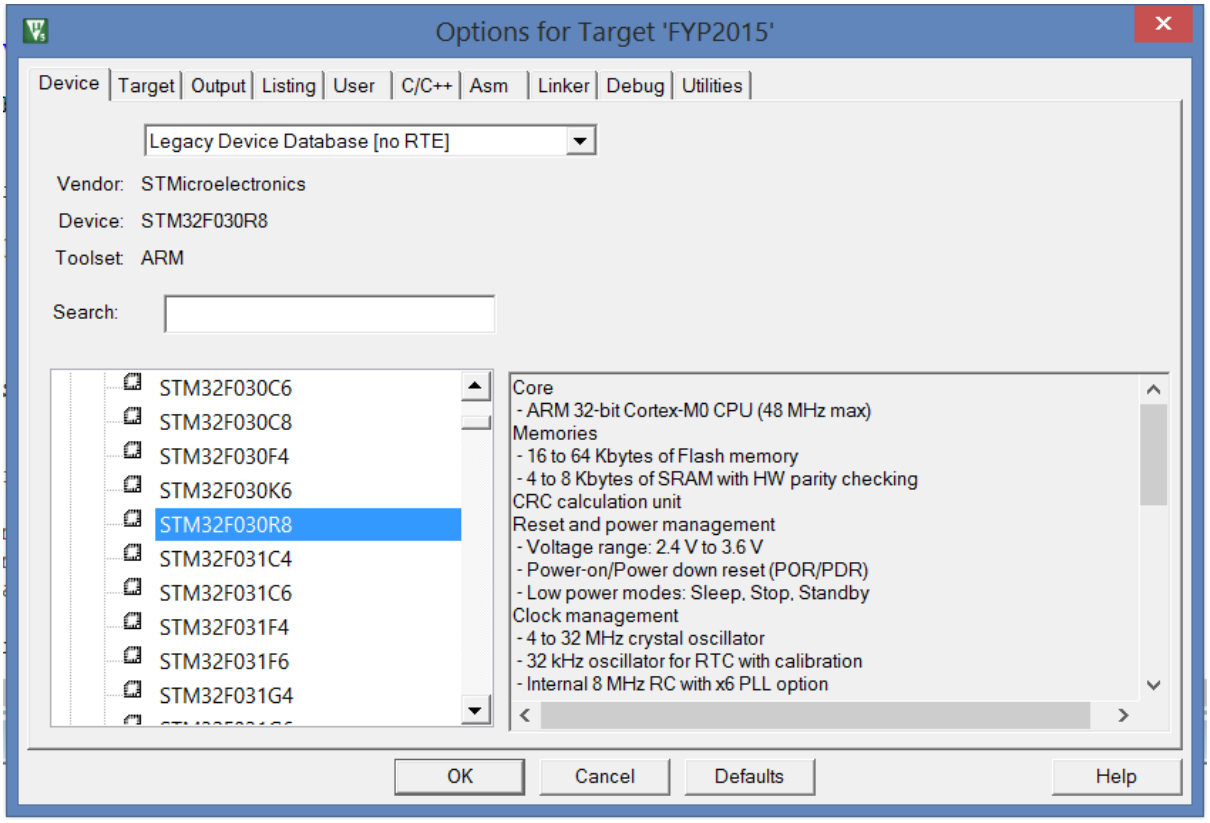
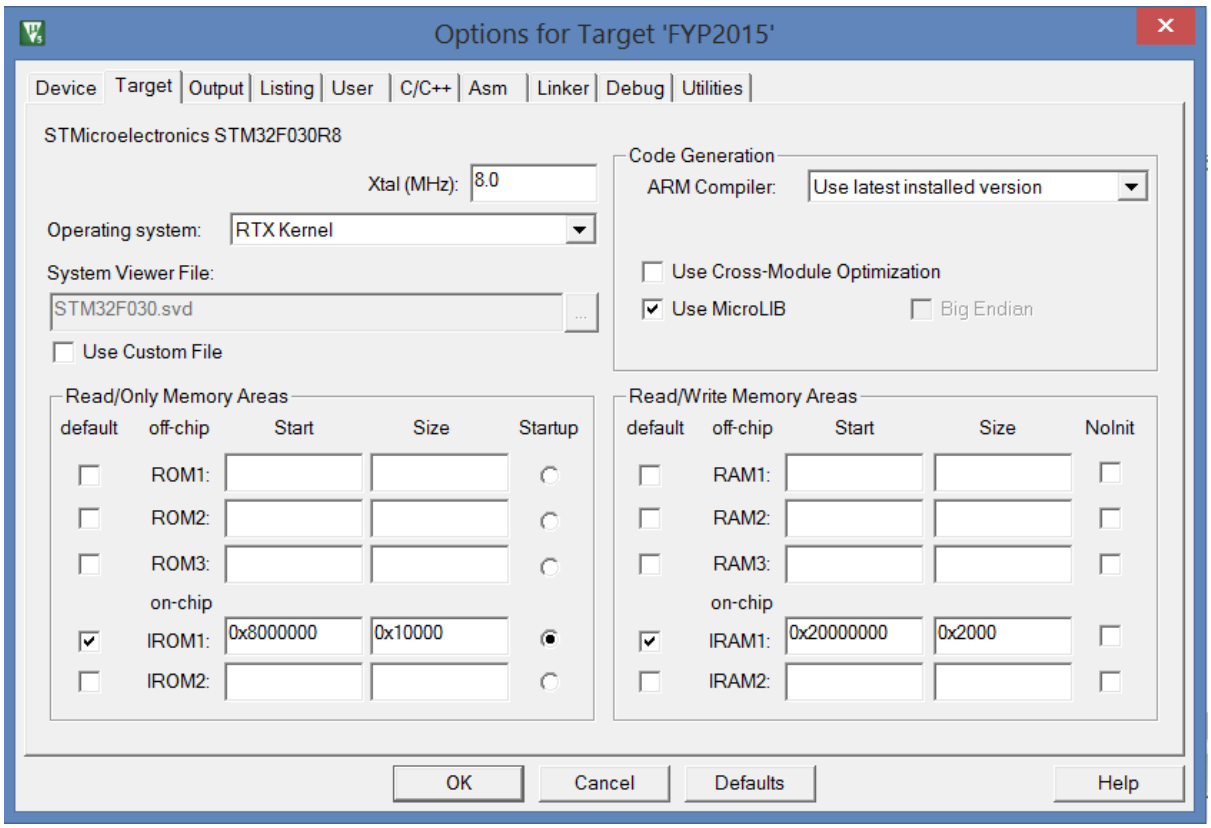
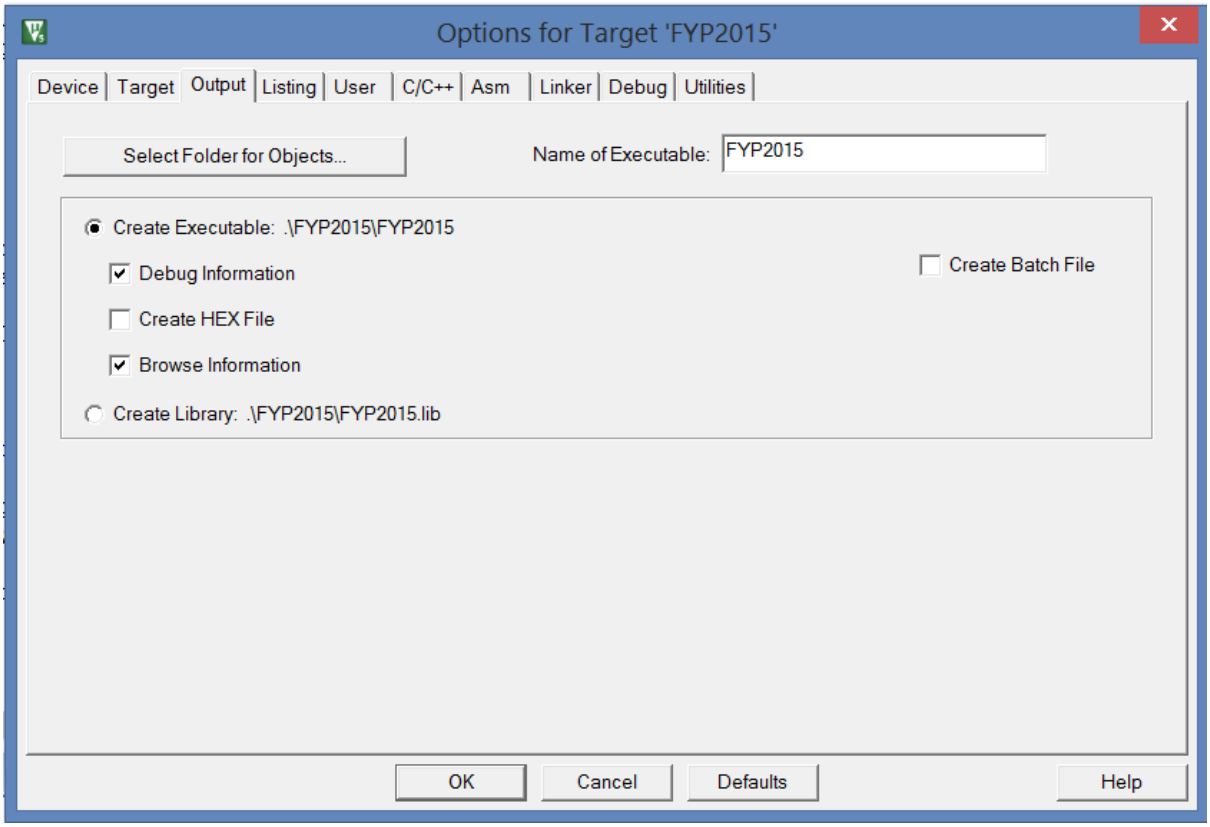
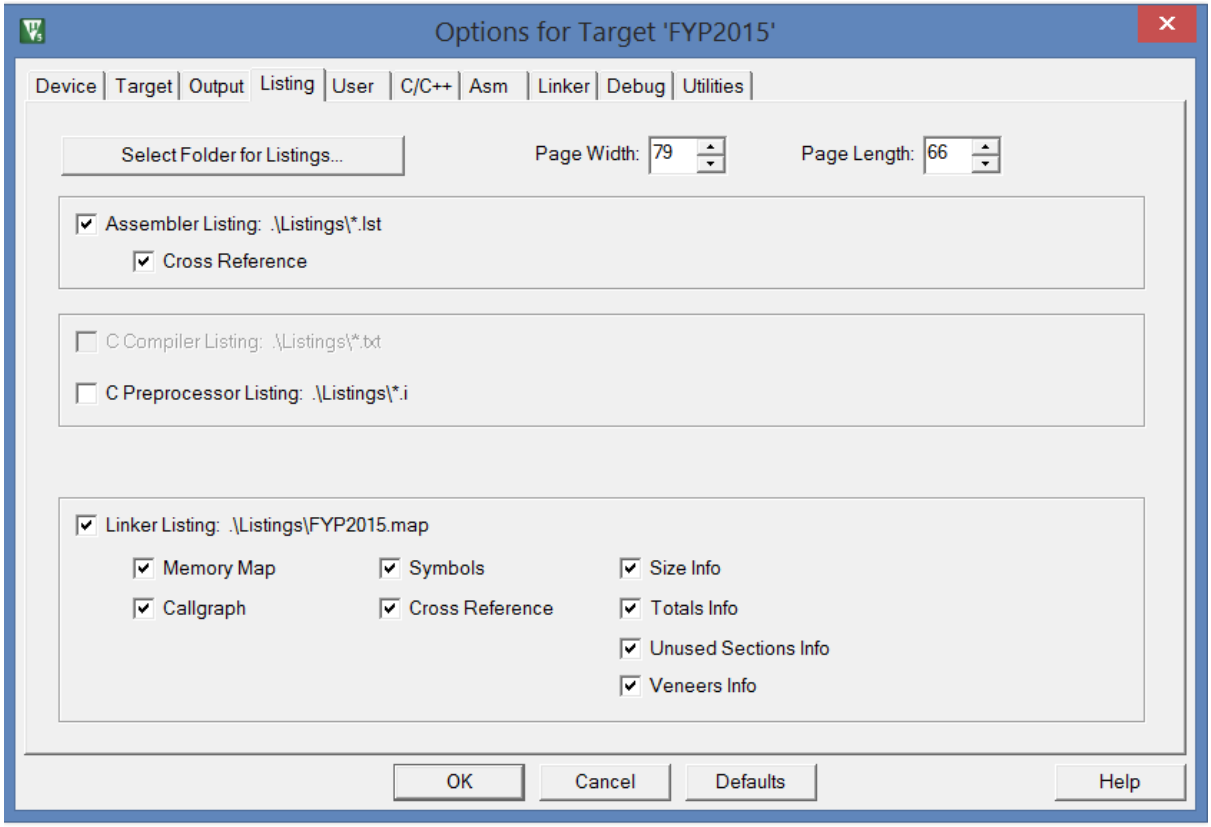
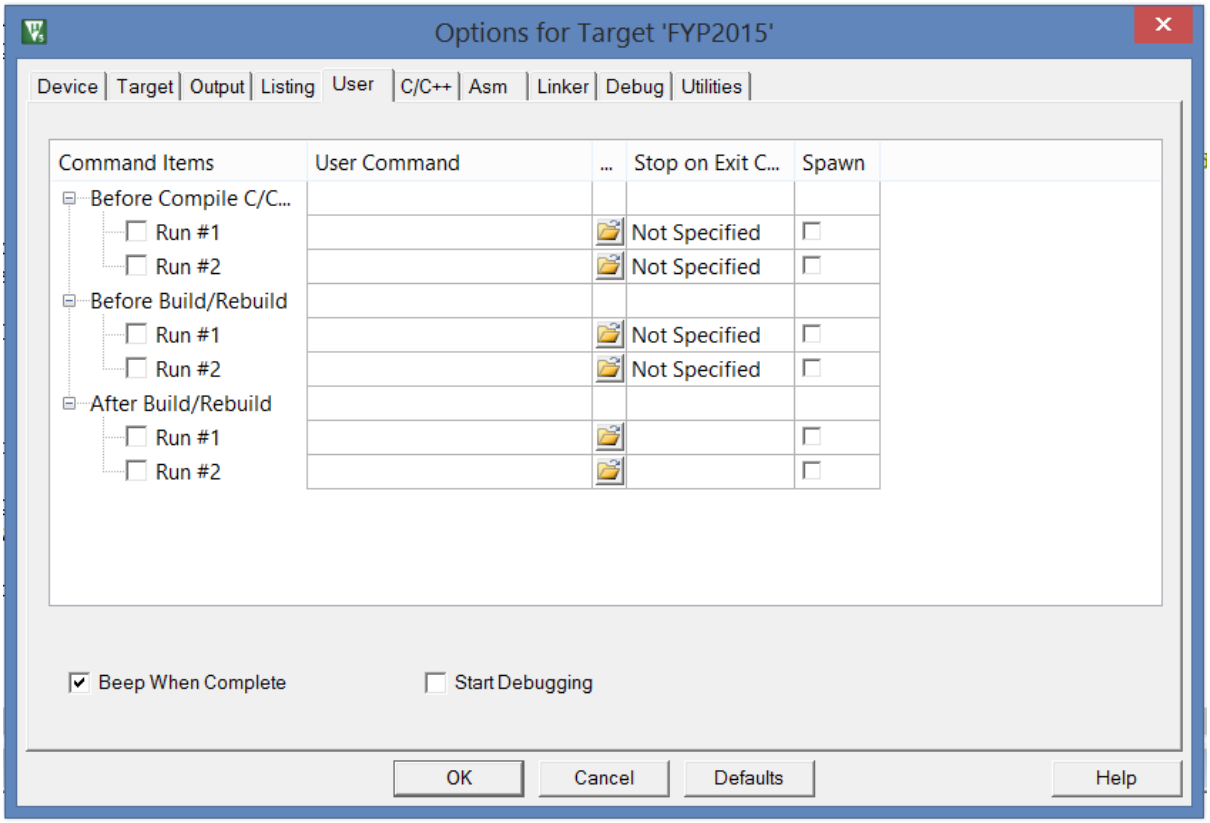
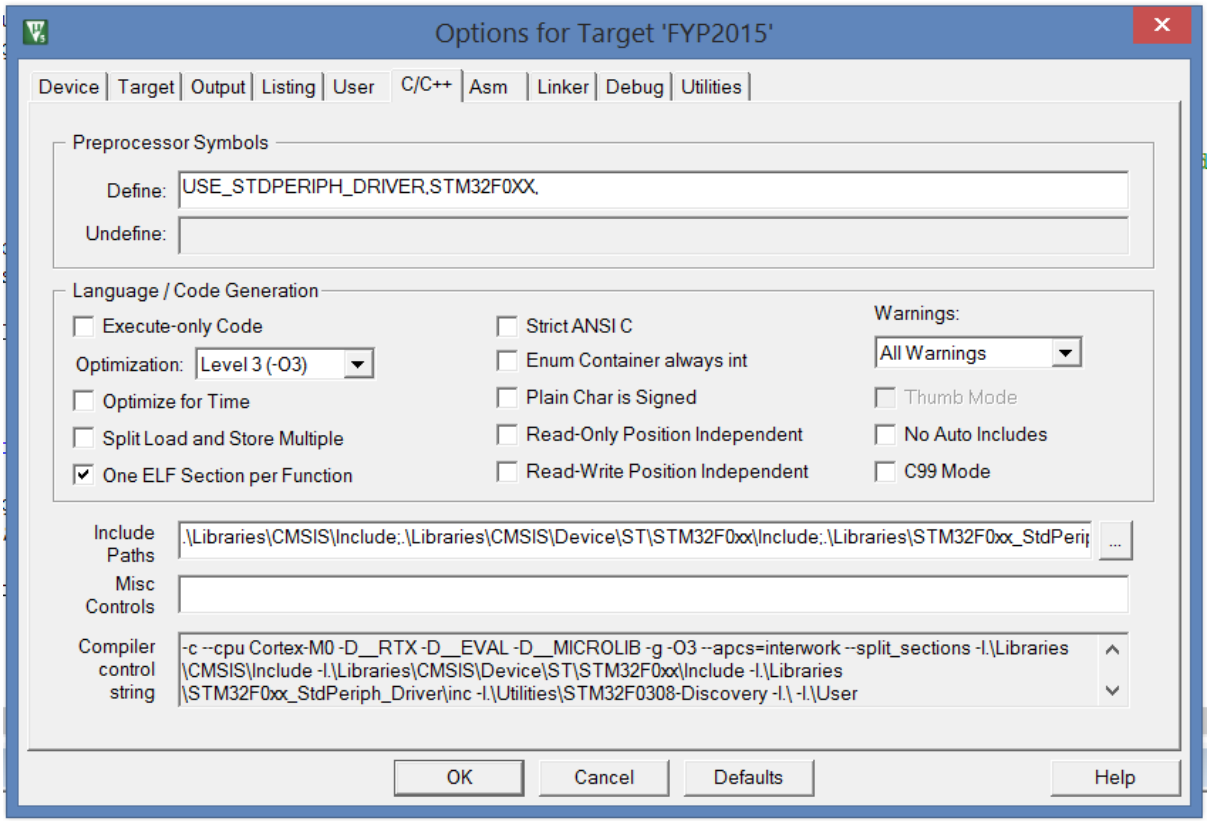

Options for Target 'FYP2015'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
Legacy Device Database [no RTE]
Vendor: STMicroelectronics
Device: STM32F030R8
Toolset: ARM
Search:
STM32F030C6
STM32F030C8
STM32F030F4
STM32F030K6
STM32F030R8
STM32F031C4
STM32F031C6
STM32F031F4
STM32F031F6
STM32F031G4
Core
- ARM 32-bit Cortex-M0 CPU (48 MHz max)
Memories
- 16 to 64 Kbytes of Flash memory
- 4 to 8 Kbytes of SRAM with HW parity checking
CRC calculation unit
Reset and power management
- Voltage range: 2.4 V to 3.6 V
- Power-on/Power down reset (POR/PDR)
- Low power modes: Sleep, Stop, Standby
Clock management
- 4 to 32 MHz crystal oscillator
- 32 kHz oscillator for RTC with calibration
- Internal 8 MHz RC with x6 PLL option
OK
Cancel
Defaults
Help


Options for Target 'FYP2015'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
STMicroelectronics STM32F030R8
Xtal (MHz): 8.0
Operating system: RTX Kernel
System Viewer File:
STM32F030.svd
Use Custom File
Code Generation
ARM Compiler: Use latest installed version
Use Cross-Module Optimization
Use MicroLIB
Big Endian
Read/Only Memory Areas
default off-chip Start Size Startup
ROM1:
ROM2:
ROM3:
on-chip
IROM1: 0x8000000 0x10000
IROM2:
Read/Write Memory Areas
default off-chip Start Size NoInit
RAM1:
RAM2:
RAM3:
on-chip
IRAM1: 0x20000000 0x2000
IRAM2:
OK
Cancel
Defaults
Help


Options for Target 'FYP2015'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
Select Folder for Objects...
Name of Executable:
FYP2015
Create Executable: .\FYP2015\FYP2015
Debug Information
Create Batch File
Create HEX File
Browse Information
Create Library: .\FYP2015\FYP2015.lib
OK
Cancel
Defaults
Help

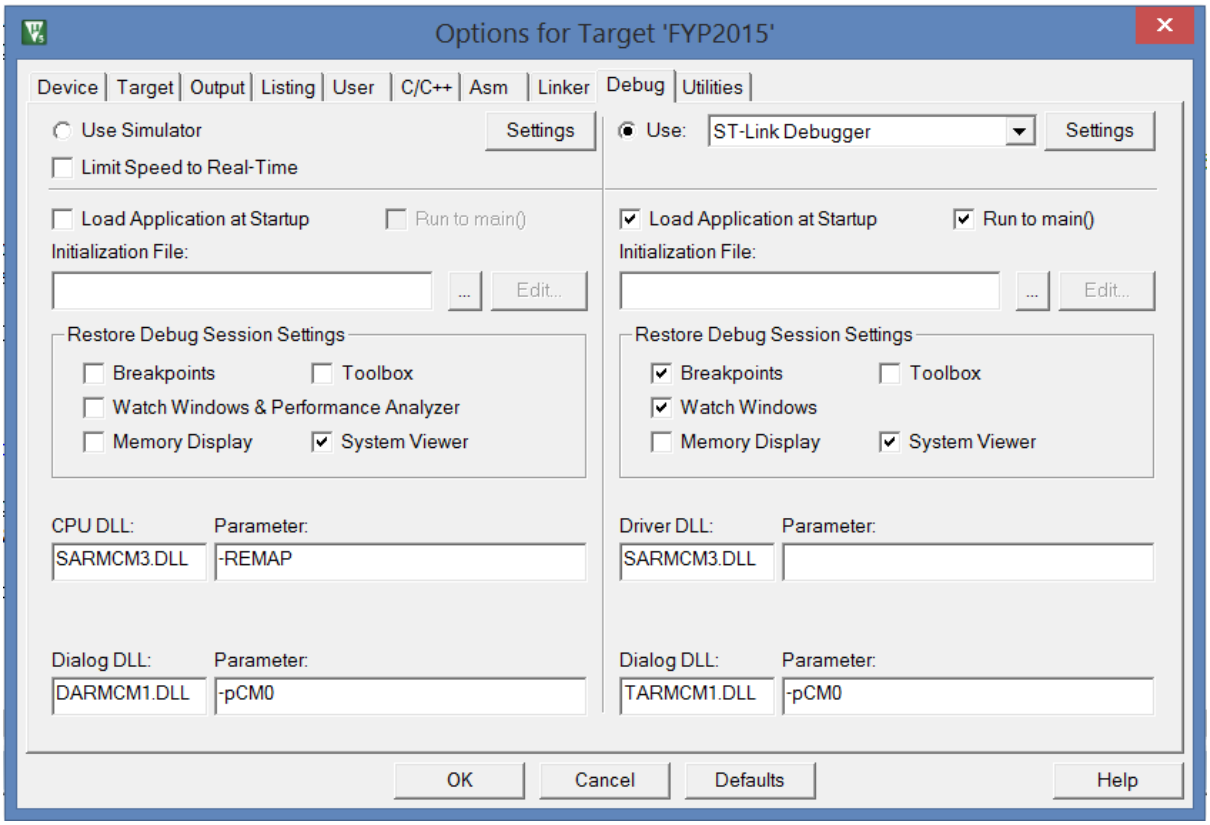

Options for Target 'FYP2015'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
Select Folder for Listings...
Page Width:
79
Page Length:
66
Assembler Listing: .\Listings*.lst
Cross Reference
C Compiler Listing: .\Listings*.txt
C Preprocessor Listing: .\Listings*.i
Linker Listing: .\Listings\FYP2015.map
Memory Map
Symbols
Size Info
Callgraph
Cross Reference
Totals Info
Unused Sections Info
Veneers Info
OK
Cancel
Defaults
Help

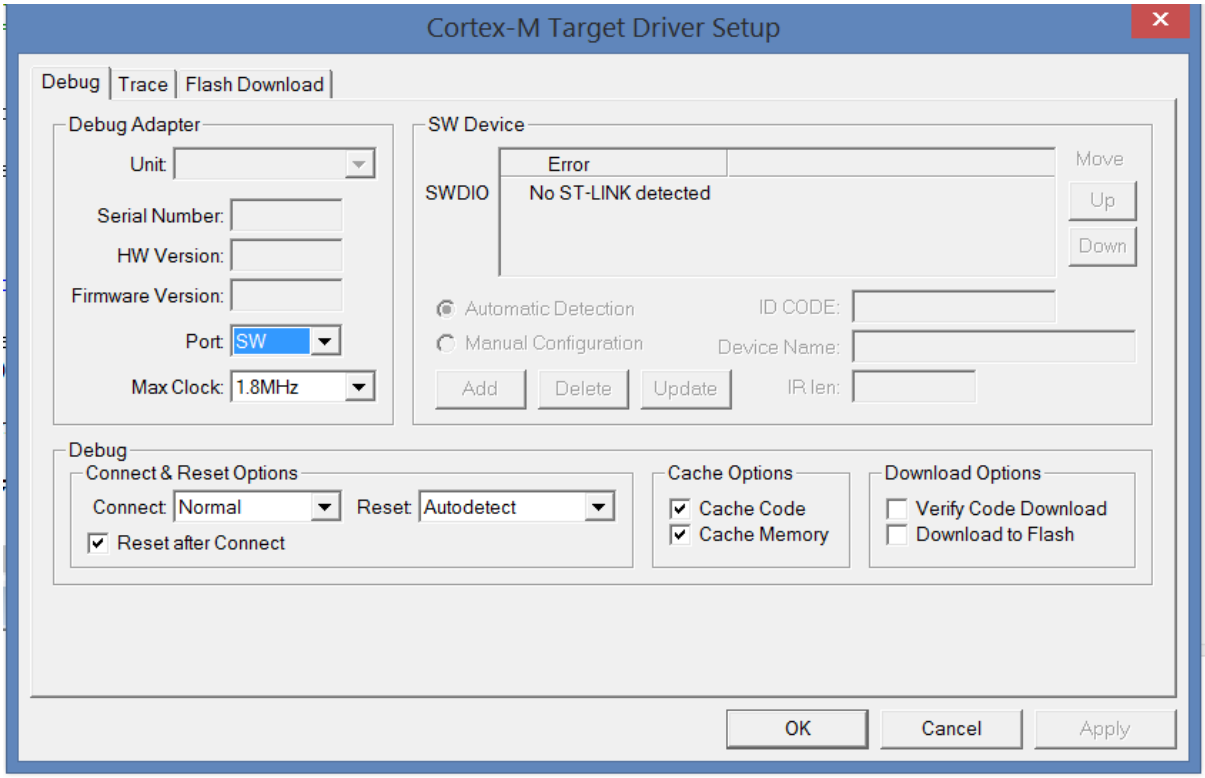


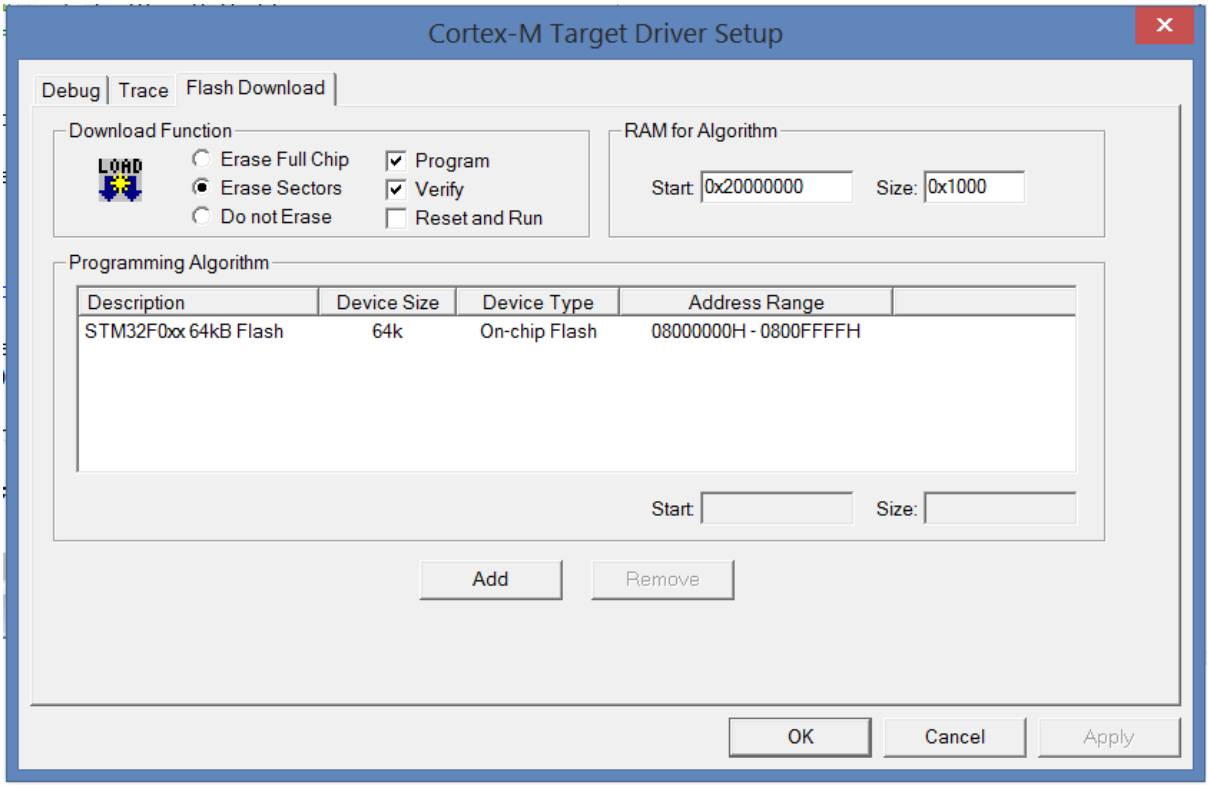


The full include path is:

.\Libraries\CMSIS\Include;.\Libraries\CMSIS\Device\ST\STM32F0xx\Include;.\Libraries\STM32F0xx_StdPeriph_Driver\inc;.\Utilities\STM32F0308-Discovery;.\;.\User


Options for Target 'FYP2015'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
Use Simulator
Settings
Limit Speed to Real-Time
Use:
ST-Link Debugger
Settings
Load Application at Startup
Run to main()
Initialization File:
Edit...
Restore Debug Session Settings
Breakpoints
Toolbox
Watch Windows & Performance Analyzer
Memory Display
System Viewer
Load Application at Startup
Run to main()
Initialization File:
Edit...
Restore Debug Session Settings
Breakpoints
Toolbox
Watch Windows
Memory Display
System Viewer
CPU DLL:
Parameter:
SARMCM3.DLL
-REMAP
Driver DLL:
Parameter:
SARMCM3.DLL
Dialog DLL:
Parameter:
DARMCM1.DLL
-pCM0
Dialog DLL:
Parameter:
TARMCM1.DLL
-pCM0
OK
Cancel
Defaults
Help


Cortex-M Target Driver Setup
Debug
Trace
Flash Download
Debug Adapter
Unit:
Serial Number:
HW Version:
Firmware Version:
Port:
SW
Max Clock:
1.8MHz
SW Device
Error
SWDIO
No ST-LINK detected
Move
Up
Down
Automatic Detection
Manual Configuration
ID CODE:
Device Name:
Add
Delete
Update
IR len:
Debug
Connect & Reset Options
Connect:
Normal
Reset:
Autodetect
Reset after Connect
Cache Options
Cache Code
Cache Memory
Download Options
Verify Code Download
Download to Flash
OK
Cancel
Apply


Cortex-M Target Driver Setup
Debug
Trace
Flash Download
Download Function
LOAD
Erase Full Chip
Erase Sectors
Do not Erase
Program
Verify
Reset and Run
RAM for Algorithm
Start: 0x20000000
Size: 0x1000
Programming Algorithm
Description
Device Size
Device Type
Address Range
STM32F0xx 64kB Flash
64k
On-chip Flash
08000000H - 0800FFFFH
Start:
Size:
Add
Remove
OK
Cancel
Apply