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M. L. Bushnell and V. D. Agrawal, Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits, Boston: Springer, 2005, ISBN 0-7923-7991-8; this printing includes corrections over the 2000, 2001 and 2004 printings by Kluwer Academic Publishers.

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Neural Models and Algorithms for Digital Testing (The Springer International Series in Engineering and Computer Science) by Chadradhar, S.T., Agrawal, Vishwani, Bushnell, M. and a great selection of related books, art and collectibles available now at AbeBooks.com.

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Text book: Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits by Bushnell and Agrawal, Springer, 2000. HOMEWORK (30%): Homework 1 -- Problems 1.2 and 1.4, assigned 1/12/06, due 1/26/06, solution

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