Chapter 2

Quantum Computation: Overview

In the simplest physical terms, quantum computation is an implementation of an arbitrary unitary operation on a finite collection of two-level quantum systems that are called *quantum bits* or *qubits* for short. It is typically depicted in a *quantum circuit diagram* as follows:

$$\begin{vmatrix} 0 \rangle & & & & & \\ |0 \rangle & & & & & \\ |0 \rangle & & & & & \\ |0 \rangle & & & & & \\ \end{vmatrix}$$

$$(2.1)$$

Each qubit is associated with a line that indicates the time evolution of the state specified on the left, and time flows from left to right. The quantum logic gate operations (or gates for short) on single or multiple qubits are denoted by a rectangular box often with labels indicating the types of the gates. Measurements are denoted by square boxes with needles. After a measurement, time-evolution is represented by dashed lines to remind that the information is classical, that is, there is no superposition.

The input state is prepared in one of the states in the logical basis, typically $|0\rangle \otimes \cdots \otimes |0\rangle$. After an overall unitary operation, the resulting state is measured in the logical basis, and the readouts are supposed to be the result of the computation.

In order for a quantum computer to be programmable, a given unitary operator \hat{U} must be implemented as a combination of other more elementary unitary operators

$$\hat{U} = \hat{U}_1 \hat{U}_2 \cdots \hat{U}_L \,, \tag{2.2}$$

where each \hat{U}_j is chosen from a small fixed set of elementary gate operations. The latter operations are the *elementary quantum logic gates* of the quantum computer.

In this chapter, we will examine widely-used choices for elementary gates and illustrate how a set of elementary gates achieve an arbitrary unitary operation to realize the so-called *universal quantum computation*.

Throughout the chapter, we denote by S the Hilbert space associated with a single qubit. The Hilbert space of an n-qubit system is given by $S^{\otimes n}$, a tensor-product space of multiple S. Each element $|x\rangle$ in the logical basis of $S^{\otimes n}$ will be labeled by an integer $x = 0, 1, \dots, 2^n - 1$, which should be understood to enumerate the tensor product form

$$|x\rangle \equiv |x_1 x_2 \cdots x_n\rangle := |x_1\rangle \otimes |x_2\rangle \otimes \cdots \otimes |x_n\rangle \tag{2.3}$$

in terms of the binary digits x_j $(j = 1, 2, \dots, n)$ of x, that is, $x \equiv (x_1 x_2 \cdots x_n)_2$.

2.1 Single-Qubit Gates

Unitary operators on the two-dimensional vector space S associated with a single qubit form the *unitary group* U(2). In the standard logical basis, they are represented by 2×2 unitary matrices. We first take a look at some special examples and discuss the general properties of the single-qubit unitary operations.

2.1.1 Pauli Gates

The Pauli gate operations (or Pauli operators for short) are defined by the corresponding Pauli matrices

$$\hat{X} \doteq \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}, \quad \hat{Y} \doteq \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}, \quad \hat{Z} \doteq \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}. \tag{2.4}$$

They form the most elementary single-qubit gate operations and are frequently used in many quantum algorithms. In this book, the Pauli gates will be denoted sometimes by \hat{X} , \hat{Y} , and \hat{Z} , and other times by \hat{S}^x , \hat{S}^y , and \hat{S}^z , depending on the context. In quantum circuit model, they are typically depicted by the circuit elements

Pauli \hat{X} maps the logical basis states as

$$\hat{X}: |0\rangle \mapsto |1\rangle, |1\rangle \mapsto |0\rangle,$$
 (2.6)

and is similar to the classical logic gate NOT. It is also customary to write Pauli \hat{X} in bra-ket notation as

$$\hat{X} = |1\rangle \langle 0| + |0\rangle \langle 1|. \tag{2.7}$$

It is important to remember that like any other quantum gate operations, it can take a linear superposition as input and transform the two logical basis states "simultaneously",

$$\hat{X}(|0\rangle c_0 + |1\rangle c_1) = |1\rangle c_0 + |0\rangle c_1, \qquad (2.8)$$

which is not possible with the classical counterpart NOT.

The Pauli X gate is represented by S[...,1]. In[-]:= **S[1**] Out[o]= SX It corresponds to the Pauli X matrix. In[*]:= Matrix[S[1]] // MatrixForm Out[=]//MatrixForm= (0 1 In the quantum circuit model, it is denoted by the following quantum circuit element. In[*]:= QuantumCircuit[S[1]] Operating on the logical basis states, it flips the states and is similar to the classical logical gate NOT. In[=]:= bs = Basis[S]; out = S[1] ** bs; Thread[bs → out] // LogicalForm // TableForm Outf=1//TableFor $|0_S\rangle \rightarrow |1_S\rangle$ $|1_S\rangle \rightarrow |0_S\rangle$ Operating on a superposition state, it flips the state "simultaneously". $ln[\cdot]:=$ in = Ket[] \times c[0] + Ket[S \rightarrow 1] \times c[1]; in // LogicalForm out = S[1] ** in; out // LogicalForm

Operating on the logical basis states, Pauli \hat{Z} only changes the relative phase of $|1\rangle$,

$$\hat{Z}: |0\rangle \mapsto |0\rangle, |1\rangle \mapsto -|1\rangle,$$
 (2.9)

and hence in bra-ket notation, it reads as

$$\hat{Z} = |0\rangle \langle 0| - |1\rangle \langle 1| . \tag{2.10}$$

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The phase change is meaningless on classical bits, but it makes a significant difference on a superposition as illustrated in the following example

$$\hat{Z}(|0\rangle c_0 + |1\rangle c_1) = |0\rangle c_0 - |1\rangle c_1. \tag{2.11}$$

The Pauli Z gate is represented by S[...,3].

$$ln[\cdot]:= S[3]$$

$$Out[\cdot]:= S^{Z}$$

It corresponds to the Pauli Z matrix.

In the quantum circuit model, it is denoted by the following quantum circuit element.

Operating on the logical basis states, it "flips the phase", that is, it changes the phase factor to -1 of the logical basis state $|1\rangle$.

Here is an example how the Pauli Z gate acts on a superposition state.

```
\begin{array}{ll} \mbox{$l\eta(\cdot)$:= in = Ket[] \times c[0] + Ket[S \to 1] \times c[1];$} \\ & \mbox{in // LogicalForm} \\ & \mbox{out = S[3] ** in;} \\ & \mbox{out // LogicalForm} \\ \mbox{$Out(\circ)$:= $c_0 & |0_S\rangle + c_1 & |1_S\rangle$} \\ \mbox{$Out(\circ)$:= $c_0 & |0_S\rangle - c_1 & |1_S\rangle$} \end{array}
```

Pauli \hat{Y} combines the bit-flip feature of \hat{X} and the phase-flip feature of \hat{Z} to get

$$|0\rangle \mapsto i \, |1\rangle \; , \quad |1\rangle \mapsto -i \, |0\rangle \; . \tag{2.12}$$

This can also be seen in the operator identity, $\hat{Y}=i\hat{X}\hat{Z}.$ In the bra-ket notation, it reads as

$$\hat{Y} = i |1\rangle \langle 0| - i |0\rangle \langle 1|. \qquad (2.13)$$

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```
The Pauli Y gate is represented by S[...,2].
In[0]:= S[2]
Outfol= Sy
      It corresponds to the Pauli Y matrix.
    In[:]:= Matrix[S[2]] // MatrixForm
 Out[=]//MatrixForm=
           /0 -i
          i 0
      In the quantum circuit model, it is denoted by the following quantum circuit element.
In[*]:= QuantumCircuit[S[2]]
      Pauli Y is a combination of the bit-flip (Pauli X) and phase-flip (Pauli Z) operation.
In[*]:= qc = QuantumCircuit[S[1], S[3]]
      op = ExpressionFor[qc]
                       Ζ
Out[=]=
Out[\circ]= i S^y
      This shows more explicitly how Pauli Y "flips" both the bit and phase of the logical basis states.
    In[0]:= bs = Basis[S];
         out = S[2] ** bs;
         Thread[bs → out] // LogicalForm // TableForm
 Outf=1//TableFor
          |0_S\rangle \rightarrow i |1_S\rangle
          |1_S\rangle \rightarrow -i |0_S\rangle
      Here is an example how the Pauli Y gate acts on a superposition state.
ln[\circ]:= in = Ket[] \times c[0] + Ket[S \rightarrow 1] \times c[1];
      in // LogicalForm
      out = S[2] ** in;
      out // LogicalForm
Out[\circ]= c_0 \mid 0_S \rangle + c_1 \mid 1_S \rangle
Out[\circ]= -i c_1 | \Theta_S \rangle + i c_0 | 1_S \rangle
```

The Pauli gates can also be regarded as rotations by π around the x-, y-, and z-axis, respectively, in the Bloch sphere as illustrated in Fig. 2.1 and demonstrated in the following:

The Pauli gates also correspond, up to a global phase factor (-i), to rotations by the corresponding axes by angle π . Here **Rotation** $[\phi, S[..., \mu]]$ represents the rotation operator around the μ -axis by angle ϕ .

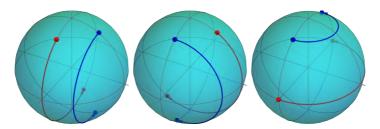


Figure 2.1: Illustration of the actions of Pauli gates as rotations by angle π . From the left the actions of the Pauli \hat{X} , \hat{Y} , \hat{Z} .

Here we main focus on their roles as unitary operators. However, the Pauli operators play another important role as orthogonal *basis vectors* of the vector space of all linear operators on a two-dimensional vector space (see Section 2.1.3 and Appendix B.1).

2.1.2 Hadamard Gate

The Hadamard gate is one of the most frequently used elementary gates in many quantum algorithms. The Hadamard gate \hat{H} is defined by the mapping:

$$|0\rangle \mapsto \frac{|0\rangle + |1\rangle}{\sqrt{2}}, \quad |1\rangle \mapsto \frac{|0\rangle - |1\rangle}{\sqrt{2}}.$$
 (2.14)

That is, it constructs linear superpositions of the two logical basis states. This feature makes the Hadamard gate so useful, and it is exploited in a wide range of quantum algorithms. In the logical basis, it is represented by the 2×2 Hadamard matrix

$$\hat{H} \doteq \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1\\ 1 & -1 \end{bmatrix} \tag{2.15}$$

In quantum circuit model, the Hadamard gate is depicted by an element with label "H"

Note that the output states in (2.14) are the eigenstates of the Pauli X operator. One can thus regard the Hadamard gate as a basis transformation from the logical basis to the eigenbasis of the Pauli X gate.

The Hadamard gate is represented by S[..., 6].

Let us consider all the logical basis states.

$$lo[\cdot]:=$$
 bs = Basis[S[1]];
bs // LogicalForm
 $Out[\cdot]:=$ $\{ | 0_{S_1} \rangle$, $| 1_{S_1} \rangle \}$

Operating the Hadamard gate on them gives the two superposition states.

$$log(s) = \text{ out } = S[1, 6] **bs;$$

$$\text{ out } // \text{ LogicalForm}$$

$$Out(s) = \left\{ \frac{\left| \Theta_{S_1} \right\rangle}{\sqrt{2}} + \frac{\left| 1_{S_1} \right\rangle}{\sqrt{2}}, \frac{\left| \Theta_{S_1} \right\rangle}{\sqrt{2}} - \frac{\left| 1_{S_1} \right\rangle}{\sqrt{2}} \right\}$$

In the quantum circuit model, it is denoted by the following circuit element.

To provide further insight, note that it can be regarded as a rotation by angle π around the axis (1,0,1) on the Bloch sphere (up to a global phase factor). This can be seen from the following:

$$\hat{H} = \frac{1}{\sqrt{2}} \left(\hat{X} + \hat{Z} \right) = i \exp \left[-i \frac{\pi}{2} (\hat{X} + \hat{Z}) \right]$$
 (2.17)

This is illustrated in Fig. 2.2.

Geometrically, the Hadamard gate can be regarded as a rotation around the axis (1,0,1) in the Pauli space by angle π .

$$In[\cdot] = \text{ op = I Rotation}[\pi, S, \{1, 0, 1\}] \text{ // Elaborate }$$

$$mat = \text{Matrix}[\text{op]};$$

$$mat \text{ // MatrixForm}$$

$$Cut[\cdot] = \frac{S^{x}}{\sqrt{2}} + \frac{S^{z}}{\sqrt{2}}$$

$$Cut[\cdot] \text{ // MatrixForm} = \left(\frac{1}{\sqrt{2}} - \frac{1}{\sqrt{2}}\right)$$

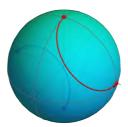


Figure 2.2: Illustration of the action of the Hadamard gate on the Bloch sphere. The Hadamard gate corresponds to a rotation around the axis (1,0,1) in the xz-plane by angle π .

An obvious but very useful feature is that it makes a linear superposition of all states in the logical basis: Consider a quantum register consisting of n qubits. When applied to each qubit in $|0\rangle$, it generates a linear superposition of all states in the logical basis

$$\hat{H}^{\otimes n} |0\rangle^{\otimes n} = \frac{|0\rangle + |1\rangle}{\sqrt{2}} \otimes \cdots \otimes \frac{|0\rangle + |1\rangle}{\sqrt{2}} = \frac{1}{2^{n/2}} \sum_{x=0}^{2^n - 1} |x\rangle , \qquad (2.18)$$

where $|x\rangle := |x_1\rangle \otimes |x_2\rangle \otimes \cdots \otimes |x_n\rangle$ for an integer x represented by $x = (x_1x_2 \dots x_n)_2$ in the binary digits. More generally, for an arbitrary state $|y\rangle$ in the logical basis,

$$\hat{H}^{\otimes n} |y\rangle = \frac{1}{2^{n/2}} \sum_{x=0}^{2^{n}-1} |x\rangle (-1)^{x \cdot y}, \qquad (2.19)$$

where we have used a short-hand notation

$$x \cdot y := x_1 y_1 + \dots + x_n y_n \pmod{2}.$$
 (2.20)

Suppose the Hadamard gates are applied to three qubits. $In_{[-]:=} \text{ op = HoldForm@Multiply}[S[1, 6], S[2, 6], S[3, 6]]$ $Out_{[-]:=} S_1^H S_2^H S_3^H$ This shows the overall operation in the quantum circuit model. $In_{[-]:=} \text{ qc = QuantumCircuit}[S[\{1, 2, 3\}, 6], \text{Null}]$ $Out_{[-]:=} H$ $Out_{[-]:=} H$

Operating the Hadamard gate on each qubit produces a superposition state consisting all logical basis states.

$$\begin{aligned} &\inf_{-\frac{1}{2}} \text{ out = ReleaseHold[op] ** Ket[];} \\ &\text{ out // LogicalForm} \end{aligned} \right] \\ &\sup_{-\frac{1}{2}} \frac{\left| 0_{S_1} 0_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 0_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} \end{aligned}$$

This show the same result in the quantum circuit model.

 $m_{[a]=}$ qc = QuantumCircuit[LogicalForm[Ket[], S@{1, 2, 3}], S[{1, 2, 3}, 6]] ExpressionFor[qc] // LogicalForm

$$\begin{array}{c} \textit{Out[\circ]$=} & \frac{\left| \, \Theta_{S_1} \Theta_{S_2} \Theta_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \Theta_{S_1} \Theta_{S_2} \mathbf{1}_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \Theta_{S_1} \mathbf{1}_{S_2} \Theta_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \Theta_{S_1} \mathbf{1}_{S_2} \mathbf{1}_{S_3} \right\rangle}{2 \, \sqrt{2}} + \\ & \frac{\left| \, \mathbf{1}_{S_1} \Theta_{S_2} \Theta_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \mathbf{1}_{S_1} \Theta_{S_2} \mathbf{1}_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \mathbf{1}_{S_1} \mathbf{1}_{S_2} \Theta_{S_3} \right\rangle}{2 \, \sqrt{2}} + \frac{\left| \, \mathbf{1}_{S_1} \mathbf{1}_{S_2} \mathbf{1}_{S_3} \right\rangle}{2 \, \sqrt{2}} \end{array}$$

Let us compare it with an explicit construction. To do it first prepare the indices of the logical basis states in binary digits.

This gives an explicit construction (unnormalized) of the superposition of all local basis states.

$$\begin{aligned} & \text{Im}_{[\cdot]} = \text{ vec = Total[Ket[S@\{1, 2, 3\} \rightarrow \#] \& /@bit];} \\ & \text{ vec // LogicalForm} \end{aligned} \\ & \text{Out}_{[\cdot]} = \begin{vmatrix} \theta_{S_1} \theta_{S_2} \theta_{S_3} \rangle + \left| \theta_{S_1} \theta_{S_2} 1_{S_3} \rangle + \left| \theta_{S_1} 1_{S_2} \theta_{S_3} \rangle + \left| 1_{S_1} 1_{S_2} \theta_{S_3} \rangle + \left| 1_{S_1} 1_{S_2} \theta_{S_3} \rangle + \left| 1_{S_1} 1_{S_2} 1_{S_3} \right\rangle + \left| 1_{S_1} 1_{S_2} 1_{S_3} \rangle + \left| 1_{S_1} 1_{S_2} 1_{S_3} \right\rangle \end{aligned}$$

On other elements of the logical basis, the sign of each term is determined by the bitwise dot product of the its bit-string with that of the input state.

$$\begin{array}{l} \mbox{\it Info} = \mbox{\bf out} = \mbox{\bf ReleaseHold[op] ** in;} \\ \mbox{\bf out} \mbox{\it // LogicalForm} \\ \mbox{\it Culfo} = \mbox{\bf } & \frac{\left| 0_{S_1} 0_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} - \frac{\left| 0_{S_1} 0_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} - \frac{\left| 0_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} - \\ \mbox{\bf } & \frac{\left| 1_{S_1} 0_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 0_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} - \frac{\left| 1_{S_1} 1_{S_2} 0_{S_3} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 1_{S_2} 1_{S_3} \right\rangle}{2 \sqrt{2}} \end{array}$$

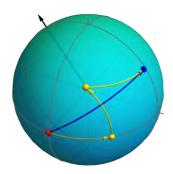


Figure 2.3: Visualization of the transformations of states under single-qubit operations. Up to a global phase factor, a single-qubit unitary operation is a rotation on the Bloch sphere. The rotation around the axis indicated by the black arrow is depicted by the blue arrow. The same rotation can be achieved by combining three rotations around the y- or z-axis depicted by the yellow arrows.

2.1.3 Rotations

Any unitary operator \hat{U} can always be written in the form $\hat{U} = \exp(-i\hat{H})$ with a Hermitian operator \hat{H} . On a two-dimensional vector space \mathcal{S} associated with a qubit, any Hermitian operator \hat{H} can be expanded in terms of the Pauli operators \hat{S}^{μ} as

$$\hat{H} = \phi_0 + \hat{S}^x B_x + \hat{S}^y B_y + \hat{S}^z B_z \tag{2.21}$$

where ϕ_0, B_x, B_y, B_z are real parameters. Regarding $\mathbf{B} := (B_x, B_y, B_z)$ as a three-dimensional vector, we consider the unit vector \mathbf{n} pointing to the same direction as \mathbf{B} and another real parameter $\phi := 2|\mathbf{B}|$, where the factor 2 is just for later convenience. In terms of these new parameterization, \hat{H} reads as

$$\hat{H} = \phi_0 + \hat{\mathbf{S}} \cdot \mathbf{n} \,\phi/2\,,\tag{2.22}$$

where $\hat{\mathbf{S}}:=(\hat{S}^x,\hat{S}^y,\hat{S}^z)$. In short, any unitary operator on \mathcal{S} has the form $\hat{U}=e^{-i\phi_0}\hat{U}_{\mathbf{n}}(\phi)$ with

$$\hat{U}_{n}(\phi) := \exp(-i\hat{\boldsymbol{S}} \cdot \boldsymbol{n} \,\phi/2). \tag{2.23}$$

Here $e^{-i\phi_0}$ changes the global phase factor and is physically irrelevant. The more important and interesting part is $\hat{U}_{n}(\phi)$, which as we will see below, describes a "rotation" around the axis n by the angle ϕ . The rotations here are not in the real three-dimensional world but on the Bloch sphere (see Fig. 2.3 for an illustration) corresponding to the two-dimensional vector space \mathcal{S} . We will further denote the rotations around the μ -axis—n parallel to the μ -axis—of the Bloch sphere by $\hat{U}_{\mu}(\phi)$.

To see that the unitary operation $\hat{U}_n(\phi)$ in (2.23) corresponds to a rotation, recall that the Pauli operators \hat{S}^{μ} are the spin angular momentum operators of

spin 1/2. That is, they are the generators of rotations and satisfy the commutation relations

$$[\hat{S}^{\mu}, \hat{S}^{\nu}] = 2i \sum_{\lambda} \hat{S}^{\lambda} \epsilon_{\lambda\mu\nu}. \tag{2.24}$$

The connection of the unitary operator $\hat{U}_{\lambda}(\phi)$ to rotation is seen more explicitly in the equivalent relation

$$\hat{U}_{\lambda}(\phi)\hat{S}^{\nu}\hat{U}_{\lambda}^{\dagger}(\phi) = \sum_{\mu} \hat{S}^{\mu}[R_{\lambda}(\phi)]_{\mu\nu}, \qquad (2.25)$$

where $R_{\lambda}(\phi)$ is the 3 × 3 orthogonal matrix describing the rotation of three-dimensional coordinates around the λ -axis by angle ϕ .

The Pauli operators are generators of the rotational transformations in a two-dimensional complex vector space, and hence satisfy the fundamental commutation relations of angular momentum operators (up to a normalization factor).

$$ln[\cdot] = \text{ op = S[All]}$$

$$Out[\cdot] = \left\{S^{x}, S^{y}, S^{z}\right\}$$

$$\begin{aligned} & \textit{In} = \texttt{Outer}[\texttt{HoldForm@*Commutator}, \, \mathsf{op}, \, \mathsf{op}]; \\ & \mathsf{out} = \texttt{ReleaseHold[in]}; \\ & \mathsf{Thread}[\texttt{Flatten[in]} \to \texttt{Flatten[out]}] \; / / \; \mathsf{TableForm} \end{aligned}$$

$$\begin{aligned} & \textit{Couf(*)/TableForm} = \\ & & \textit{Commutator}\left[S^x, \, S^x\right] \to 0 \\ & & \textit{Commutator}\left[S^x, \, S^y\right] \to 2 \, \text{i} \, S^z \\ & & \textit{Commutator}\left[S^x, \, S^z\right] \to -2 \, \text{i} \, S^y \\ & & \textit{Commutator}\left[S^y, \, S^x\right] \to -2 \, \text{i} \, S^z \\ & & \textit{Commutator}\left[S^y, \, S^y\right] \to 0 \\ & & \textit{Commutator}\left[S^y, \, S^z\right] \to 2 \, \text{i} \, S^x \\ & & \textit{Commutator}\left[S^z, \, S^x\right] \to 2 \, \text{i} \, S^y \\ & & \textit{Commutator}\left[S^z, \, S^y\right] \to -2 \, \text{i} \, S^x \\ & \textit{Commutator}\left[S^z, \, S^z\right] \to 0 \end{aligned}$$

In \mathbb{R}^3 , any 3×3 rotation matrix can be decomposed into three factors

$$R = R_z(\alpha)R_y(\beta)R_z(\gamma), \qquad (2.26)$$

where α , β , γ are the so-called *Euler angles* and such a combination of rotations is called the *Euler rotation*. In the same manner, any unitary operator on \mathcal{S} can also be written as

$$\hat{U} = e^{-i\phi_0} \hat{U}_z(\alpha) \hat{U}_y(\beta) \hat{U}_z(\gamma), \qquad (2.27)$$

that is, a combination of elementary "rotations" around the y- and z-axis and an additional overall phase shift. The unitary operator $\hat{U}(\alpha, \beta, \gamma) := \hat{U}_z(\alpha)\hat{U}_y(\beta)\hat{U}_z(\gamma)$ is called the Euler rotation in the two-dimensional vector space S. Figure 2.3 illustrates an Euler rotation $\hat{U}(\pi/3, -\pi/3, \pi/4)$. It transforms $|v\rangle = (|0\rangle + |1\rangle)/\sqrt{2}$

(the red dot in Fig. 2.3) into $|w\rangle = \hat{U}|v\rangle$ (the blue dot in Fig. 2.3). The transformation is displayed by the blue arrow. The yellow dots and arrows depicts the transformations under $\hat{U}_z(\alpha)$, $\hat{U}_y(\beta)$, and $\hat{U}_z(\gamma)$, which combine to reproduce \hat{U} .

Consider a unitary operator represented by the following matrix. In[0]:= mat = { {3 - I Sqrt[3], I - Sqrt[3]}, {I + Sqrt[3], 3 + I Sqrt[3]} mat // MatrixForm $\begin{pmatrix} \frac{1}{4} \times \left(3 - i \sqrt{3}\right) & \frac{1}{4} \left(i - \sqrt{3}\right) \\ \frac{1}{4} \left(i + \sqrt{3}\right) & \frac{1}{4} \times \left(3 + i \sqrt{3}\right) \end{pmatrix}$ This gives its expression in terms of the Pauli operators. In[*]:= op = Elaborate@ExpressionFor[mat, S] Out[*]= $\frac{3}{4} + \frac{i S^{x}}{4} - \frac{1}{4} i \sqrt{3} S^{y} - \frac{1}{4} i \sqrt{3} S^{z}$ In[0]:= Dagger[op] ** op Out[-]= 1 This gives the Euler angles of the unitary operator. In[*]:= angs = TheEulerAngles[mat] Out[\circ]= $\left\{\frac{\pi}{3}, \frac{\pi}{3}, 0\right\}$ Indeed, the Euler angles reproduces the original unitary operator. In[*]:= new = EulerRotation[angs, S] // Elaborate Out[*]= $\frac{3}{4} + \frac{i S^{x}}{4} - \frac{1}{4} i \sqrt{3} S^{y} - \frac{1}{4} i \sqrt{3} S^{z}$

The rotations $\hat{U}_z(\phi)$ around the z-axis in the Bloch space induces the relative phase difference ϕ between the two logical basis states $|0\rangle$ and $|1\rangle$. In this sense, such rotations are called (relative) phase gates by phase angle ϕ . Two phase gates \hat{Q} and \hat{O} by angles $2\pi/4$ and $2\pi/8$, respectively, are particularly common, and they are often called the *quadrant* and *octant phase gate*. In the logical basis, they are represented by the following diagonal matrices

$$\hat{Q} \doteq \begin{bmatrix} 1 & 0 \\ 0 & i \end{bmatrix}, \quad \hat{O} \doteq \begin{bmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{bmatrix}, \tag{2.28}$$

respectively. Obviously, $\hat{O}^2 = \hat{Q}$ and $\hat{Q}^2 = \hat{Z}$.

```
Among (relative) phase gates, the quadrant and octant phase gates are most common.

Inf_{*} \models qd = S[1, 7] \\
Matrix[qd] // MatrixForm

Ouf_{*} \models S_{1}^{S}
Ouf_{*} \models dd ** qd
Ouf_{*} \models S_{1}^{T}
Inf_{*} \models oc = S[1, 8] \\
Matrix[oc] // MatrixForm

Ouf_{*} \models S_{1}^{T}
```

2.2 Two-Qubit Gates

Next, let us consider quantum logic gate operations acting on two qubits. Such operations are represented by 4×4 unitary matrices. We will see that any two-qubit gate operations can be decomposed into controlled-unitary gates. A controlled-unitary gate acts a unitary operator on one qubit depending on the logical state of the other qubit. A controlled-unitary gate on two qubits can be further decomposed into factors including only CNOT gate and single-qubit rotation gates. In this sense, the CNOT gate alone is sufficient for any two-qubit gate.

The controlled-unitary and CNOT gates have various interesting properties that make them useful in implementing quantum algorithms. In this section, we first examine the basic properties of the CNOT gate, and in particular, how it is used to generate an entanglement between two qubits. Then, we discuss the properties of the controlled-unitary gate and describe how to implement a controlled-unitary gate in terms of the CNOT gate and single-qubit rotations. Finally, we discuss how an arbitrary two-qubit unitary operation can be decomposed into controlled-unitary gates.

2.2.1 CNOT, CZ, and SWAP

The CNOT or controlled-NOT gate is a quantum logic gate on two qubits that maps the logical basis states as

$$|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes |c \oplus t\rangle \; ; \quad c, t \in \{0, 1\} \; ,$$
 (2.29)

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where the first qubit is typically called the *control qubit* (c) and the second qubit the *target qubit* (t). It has the following matrix representation in the logical basis

$$CNOT \doteq \begin{bmatrix} 1 & & & \\ & 1 & & \\ & & 0 & 1 \\ & & 1 & 0 \end{bmatrix}, \tag{2.30}$$

and is expressed in terms of the Pauli operators on the control and target qubit as

$$CNOT = \frac{1}{2} \left(\hat{I} + \hat{S}_c^z + \hat{S}_t^z - \hat{S}_c^z \hat{S}_t^x \right).$$
 (2.31)

In quantum circuit model, it is represented as the following circuit element:

$$(2.32)$$

where the smaller filled circle indicates the dependence on the state of the control qubit and the circled-plus sign denotes the conditional NOT action on the target qubit.

CNOT[control, target] denotes the CNOT gate in the quantum circuit model. $[a_1^{-1}]^{-2} \text{ op = CNOT}[S[1], S[2]]$ $Out_1^{-1} = \text{CNOT}[\{S_1\}, \{S_2\}]$ This displays the quantum circuit model of the CNOT gate. $[a_1^{-1}]^{-2} \text{ qc = QuantumCircuit[op]}$ $Out_1^{-1} = \text{ op = ExpressionFor[qc]}$ $Out_1^{-1} = \frac{1}{2} - \frac{1}{2} S_1^z S_2^x + \frac{S_1^z}{2} + \frac{S_2^x}{2}$ This is the matrix representation of the CNOT gate in the logical basis.

This shows how the CNOT gate operates on the logical basis states.

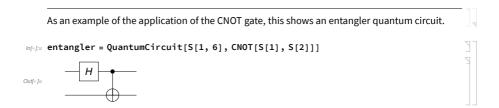
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$$\begin{split} & \textit{ln}[*] = \text{ in = Basis[S@\{1,2\}];} \\ & \text{ out = op ** in;} \\ & \text{ Thread[in \to out] // LogicalForm // TableForm} \\ & \textit{Out[*]/TableForm=} \\ & \left| 0_{S_1}0_{S_2} \right\rangle \to \left| 0_{S_1}0_{S_2} \right\rangle \\ & \left| 0_{S_1}1_{S_2} \right\rangle \to \left| 0_{S_1}1_{S_2} \right\rangle \\ & \left| 1_{S_1}0_{S_2} \right\rangle \to \left| 1_{S_1}1_{S_2} \right\rangle \\ & \left| 1_{S_1}1_{S_2} \right\rangle \to \left| 1_{S_1}1_{S_2} \right\rangle \\ & \left| 1_{S_1}1_{S_2} \right\rangle \to \left| 1_{S_1}0_{S_2} \right\rangle \\ \end{split}$$

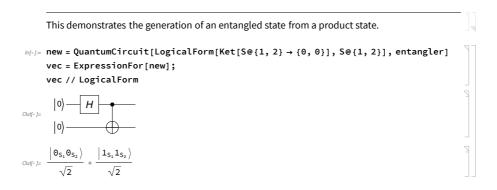
A simple yet important feature of CNOT is to copy the logical state of the control qubit to the target bit provided that the target bit is initially set to $|0\rangle$; or the reversed state when the target qubit is in $|1\rangle$. A vital implication is that CNOT generates an entangled state when the control qubit is in a superposition:

$$(|0\rangle c_0 + |1\rangle c_1) \otimes |0\rangle \mapsto |0\rangle \otimes |0\rangle c_0 + |1\rangle \otimes |1\rangle c_1. \tag{2.33}$$

Applying a single qubit rotation such the Hadamard gate on the control qubit prior to CNOT, one can thus generate entangled states from logical states. In this sense, such a circuit is called a *quantum entangler circuit*.



For example, when the input state is $|0\rangle \otimes |0\rangle$, the outcome of the circuit is given by one of the so-called Bell states.



In general, the different product states in the logical basis are transformed to different Bell states.

This lists the mapping between the standard tensor-product basis states and the Bell states.

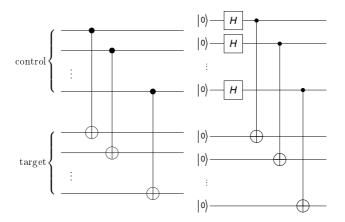


Figure 2.4: (a) A quantum circuit which makes a copy of the logical state of the "control" quantum register to the "target" quantum register. The quantum circuit transforms the logical basis states as $|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes |c \oplus t\rangle$, where c and t are b-bit strings. (b) A quantum circuit generating maximally entangled states between two quantum registers each of which consists of n qubits.

$$\begin{array}{ll} \textit{In}_{\{\cdot\}} \coloneqq bs = \texttt{Basis@S@\{1,2\};} \\ & \textit{op} = \texttt{ExpressionFor[entangler];} \\ & \textit{out} = \textit{op} ** \textit{bs;} \\ & \textit{table} = \texttt{Thread[bs} \to \textit{out];} \\ & \textit{table} \neq / \texttt{LogicalForm} // \texttt{TableForm} \\ \\ \textit{Out}_{\{\cdot\}} // \texttt{TableForm} = \\ & \left| 0_{S_1} 0_{S_2} \right\rangle \to \frac{\left| 0_{S_1} 0_{S_2} \right\rangle}{\sqrt{2}} + \frac{\left| 1_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} \\ & \left| 0_{S_1} 1_{S_2} \right\rangle \to \frac{\left| 0_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} + \frac{\left| 1_{S_1} 0_{S_1} \right\rangle}{\sqrt{2}} \\ & \left| 1_{S_1} 0_{S_2} \right\rangle \to \frac{\left| 0_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} - \frac{\left| 1_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} \\ & \left| 1_{S_1} 1_{S_2} \right\rangle \to \frac{\left| 0_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} - \frac{\left| 1_{S_1} 0_{S_1} \right\rangle}{\sqrt{2}} \\ & \left| 1_{S_1} 1_{S_2} \right\rangle \to \frac{\left| 0_{S_1} 1_{S_2} \right\rangle}{\sqrt{2}} - \frac{\left| 1_{S_1} 0_{S_1} \right\rangle}{\sqrt{2}} \end{array}$$

One can further generalize the above procedure to generate maximally entangled states between larger systems: Consider two quantum registers, each consisting of n qubits. We respectively call them the "control" and "target" register for the reason that will be clear below. Upon applying the CNOT gate on each pair of the corresponding qubits in the two registers as in the quantum circuit shown in Fig. 2.4 (a), the logical basis states are transformed as

$$|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes |c \oplus t\rangle$$
 . (2.34)

The above association rule is *formally* the same as the one in (2.29) for a single pair of qubits. Here, however, $|c\rangle := |c_1\rangle \otimes |c_2\rangle \otimes \cdots \otimes |c_n\rangle$ and $|t\rangle := |t_1\rangle \otimes |t_2\rangle \otimes \cdots \otimes |t_n\rangle$, and $c \oplus t$ denotes the bit-wise exclusive OR (or XOR),

$$c \oplus t := (c_1 \oplus t_1, c_2 \oplus t_2, \dots, c_n \oplus t_n), \tag{2.35}$$

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for the *n*-bit strings $c \equiv (c_1, c_2, \dots, c_n)$ and $t \equiv (t_1, t_2, \dots, t_n)$. When the target register is prepared in the state $|0\rangle \equiv |0\rangle^{\otimes n}$, the logical basis state of the control register is copied to the target register,^{2.1}

$$|x\rangle \otimes |0\rangle \mapsto |x\rangle \otimes |x\rangle \tag{2.36}$$

under the set of CNOT gates on paired qubits. More interestingly, when the control register is prepared in a superposition, say, $2^{-n/2}\sum_{x=0}^{2^n-1}|x\rangle$, and the target register is in the state $|0\rangle\equiv|0\rangle$, the transformation in (2.34) makes a copy of each logical state of the control register to the target register, leading to a maximally entangled state,

$$\frac{1}{2^{n/2}} \sum_{x=0}^{2^n - 1} |x\rangle \otimes |0\rangle \mapsto |\Phi\rangle := \frac{1}{2^{n/2}} \sum_{x=0}^{2^n - 1} |x\rangle \otimes |x\rangle \tag{2.37}$$

between the two registers (rather than single qubits). Since the superposition $2^{-n/2}\sum_{x=0}^{2^n-1}|x\rangle$ is obtained just by the Hadamard gates as shown in (2.18), the maximally entangled state $|\Phi\rangle$ in (2.37) can be generated from the logical state $|0\rangle\otimes|0\rangle$ through the quantum circuit illustrated in Fig. 2.4 (b). It is also interesting to note that even though $|\Phi\rangle$ is a maximally entangled state between the two control and target registers, it is a product state of n maximally entangled pairs

$$\frac{1}{2^{n/2}} \sum_{x=0}^{2^n - 1} |x\rangle \otimes |x\rangle = \bigotimes_{j=1}^n \left(\frac{|0\rangle_j \otimes |0\rangle_{n+j} + |1\rangle_j \otimes |1\rangle_{n+j}}{\sqrt{2}} \right). \tag{2.38}$$

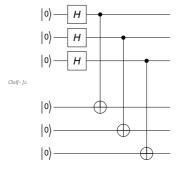
The entanglement strongly depends on how you partition the system.

Here we want to generate a maximally entangled state between two quantum registers (not just qubits) each of which is consisting of two qubits. $In[\cdot]:= \text{qc} = \text{QuantumCircuit}[\text{LogicalForm}[\text{Ket}[], S@\{1, 2, 3, 4\}], \\ S[\{1, 2\}, 6], \text{CNOT}[S[1], S[3]], \text{CNOT}[S[2], S[4]], "Invisible" <math>\rightarrow$ S[2.5]] $|0\rangle \qquad H \qquad \qquad |0\rangle \qquad H \qquad |0\rangle \qquad |0\rangle \qquad H \qquad |0\rangle \qquad |$

^{2.1}It is important to note here that only *logical basis states* can be copied, but not a superposition of them. The latter is forbidden by the no-cloning theorem (Wooters & Zurek, 1982; Zurek, 2000).

This example illustrate that the entanglement depends on the partition of the system. Indeed, the above system is a product state for the partition (1,3) and (2,4) qubits.

The above construction can be generalized for a pair of *n*-qubit registers.



$$\begin{aligned} & \textit{logicalForm} \\ & \textit{Out} \ / \ \textit{ExpressionFor} \ [\texttt{qc}] \ ; \\ & \textit{description} \\ & \frac{\left| 0_{S_1} 0_{S_2} 0_{S_3} 0_{S_4} 0_{S_5} 0_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 0_{S_2} 1_{S_3} 0_{S_4} 0_{S_5} 1_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 0_{S_3} 0_{S_4} 1_{S_5} 0_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 0_{S_1} 1_{S_2} 0_{S_3} 1_{S_4} 1_{S_5} 1_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 0_{S_2} 1_{S_3} 1_{S_4} 0_{S_5} 1_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 1_{S_2} 0_{S_3} 1_{S_4} 1_{S_5} 0_{S_6} \right\rangle}{2 \sqrt{2}} + \frac{\left| 1_{S_1} 1_{S_2} 0_{S_3} 1_{S_4} 1_{S_5} 0_{S_6} \right\rangle}{2 \sqrt{2}} \end{aligned}$$

Another generalization of copying logical basis states to a series of qubits by consecutively applying CNOT gates with a single control qubit on multiple target qubits is shown in the quantum circuit in Fig. 2.5 (a), and another equivalent quantum circuit is given in Problem 2.5. When the target qubits are all prepared in the logical basis state $|0\rangle$, the series of CNOT gates makes the transformation

$$|x\rangle \otimes |0\rangle \otimes \cdots \otimes |0\rangle \mapsto |x\rangle \otimes |x\rangle \otimes \cdots \otimes |x\rangle$$
, (2.39)

where x = 0, 1. Therefore, a simply application of the Hadamard gate in the control qubit before the CNOT gates can generate the state of the form

$$|GHZ\rangle = \frac{|0\rangle \otimes |0\rangle \otimes \cdots \otimes |0\rangle + |1\rangle \otimes |1\rangle \otimes \cdots \otimes |1\rangle}{\sqrt{2}}.$$
 (2.40)

The state in (2.40), which is known as the Greenberger-Horne-Zeilinger (GHZ) state (Greenberger *et al.*, 1989), exhibits an entanglement of more than two particles. It has stimulated tests of non-locality of quantum mechanics beyond Bell inequalities (Bouwmeester *et al.*, 1999; Pan *et al.*, 2000).

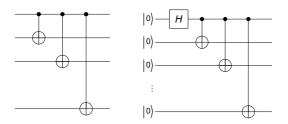


Figure 2.5: (a) A quantum circuit copying the logical basis state of a single control qubit to multiple target qubits. (b) A quantum circuit model to generate the Greenberger-Horne-Zeilinger state among multiple qubits.

Quantum entanglement is a valuable resource in quantum information processing and in quantum communication. The most popular example of such is quantum teleportation, which will be discussed in Section 4.1. The features elucidated in (2.36), (2.37) and (2.39)—and the related quantum circuits in Figs. 2.4 and 2.5—will be used frequently in later parts of the book.

An interesting variant of CNOT gates is the so-called CZ or controlled-Z gate. This is a quantum logic gate on two qubits that maps the logical basis states as

$$CZ: |c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes |t\rangle (-1)^{ct}$$
. (2.41)

The matrix representation of the CZ gate in the logical basis is given by

$$CZ \doteq \begin{bmatrix} 1 & & & \\ & 1 & & \\ & & 1 & \\ & & & -1 \end{bmatrix}. \tag{2.42}$$

Since it is symmetric for the two qubits, a distinction of the control and target qubit is meaningless. Accordingly, in quantum circuit model, the gate is depicted by the following quantum circuit element

The filled circles on both qubit lines (rather than a square box on either qubit) indicate that the bit values of both qubits remain unchanged. Noting the identity $\hat{H}\hat{Z}\hat{H} = \hat{X}$, one can regard that the CZ gate is equal to the CNOT gate up to the Hadamard gate on the target qubit. The relation between the CNOT and CZ gate is illustrated in the following quantum circuits

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Depending on the Hamiltonian of a particular physical system, direct realization of the CNOT gate may be considerably difficult while the CZ gate is relatively easier to implement. In such a case, identity (2.44) offers a straightforward workaround for a physical implementation of the CNOT gate.

```
The CZ (or controlled-Z) gate is a variant of the CNOT gate.
ln[\circ]:= op = CZ[S[1], S[2]]
Out[\circ] = CZ[S_1, S_2]
        This shows how it transforms the logical basis states.
     In[*]:= bs = Basis@S@{1, 2};
            out = op ** bs;
            Thread[bs → out] // LogicalForm // TableForm
 Out[=]//TableForn
             | \Theta_{S_1} \Theta_{S_2} \rangle \rightarrow | \Theta_{S_1} \Theta_{S_2} \rangle
            \left| 0_{S_1} 1_{S_2} \right\rangle \rightarrow \left| 0_{S_1} 1_{S_2} \right\rangle
            \left| \mathbf{1}_{S_1} \mathbf{0}_{S_2} \right\rangle \rightarrow \left| \mathbf{1}_{S_1} \mathbf{0}_{S_2} \right\rangle
            \left| \mathbf{1}_{\mathsf{S}_{1}} \mathbf{1}_{\mathsf{S}_{2}} \right\rangle \rightarrow - \left| \mathbf{1}_{\mathsf{S}_{1}} \mathbf{1}_{\mathsf{S}_{2}} \right\rangle
        Here is the matrix representation of the CZ gate.
     In[*]:= mat = Matrix[Elaborate@op];
            mat // MatrixForm
 Outf=1//MatrixForm=
             (100
                            0
              0 1 0 0
              0 0 1 0
        This is the quantum circuit model of the CZ gate.
 In[*]:= cz = QuantumCircuit[CZ[S[1], S[2]]]
Out[=]=
        Note the following identity.
ln[n]:= expr = HoldForm[S[1, 6] **S[1, 3] **S[1, 6] == S[1, 1]]
        ReleaseHold[expr] // Elaborate
\textit{Out[o]} = S_1^H ** S_1^Z ** S_1^H = S_1^X
Out[=]= True
        It leads to the following relation between the CNOT gate and CZ gate.
 In[*]:= new = QuantumCircuit[S[2, 6], cz, S[2, 6]]
Out[=]=
```

Another interesting two-qubit gate is the SWAP gate that "swaps" the states of the two qubits and maps the logical basis states as

$$SWAP: |x_1\rangle \otimes |x_2\rangle \mapsto |x_2\rangle \otimes |x_1\rangle. \tag{2.45}$$

Given that the states $|0\rangle \otimes |0\rangle$ and $|1\rangle \otimes |1\rangle$ are not altered by the operation, the matrix representation is given by

$$SWAP \doteq \begin{bmatrix} 1 & & & \\ & 0 & 1 & \\ & 1 & 0 & \\ & & & 1 \end{bmatrix} . \tag{2.46}$$

In the quantum circuit model, it is depicted as

$$(2.47)$$

The SWAP gate can be implemented using the CNOT gate. To see this, first note that a simultaneous exchange of second and fourth columns and rows of the matrix in (2.46) leads to

$$\begin{bmatrix} 1 & & & \\ & 1 & & \\ & & 0 & 1 \\ & & 1 & 0 \end{bmatrix}, \tag{2.48}$$

which is nothing other than the matrix representation of the CNOT gate. On the other hand, the exchange of the second and fourth columns and rows is described by the transformation matrix

$$\begin{bmatrix} 1 & & & \\ & 0 & & 1 \\ & & 1 & \\ & 1 & & 0 \end{bmatrix}, \tag{2.49}$$

which flips the bit values of the first qubit only when the second qubit is set to $|1\rangle$. Hence, it corresponds to the CNOT gate with the second and first qubit as the

control and target qubit, respectively. In short, the SWAP gate can be achieved by a combination of the CNOT gates as follows

The SWAP gate exchanges the states of two qubits.

$$m[\cdot]:= \text{op} = \text{SWAP}[S[1], S[2]]$$

$$Out[\cdot]:= \text{SWAP}[S_1, S_2]$$

$$\begin{array}{l} \textit{In}[\cdot]:= bs = \texttt{Basis@S@\{1,2\};} \\ \text{new} = \texttt{op} ** bs; \\ \text{Thread[bs} \to \texttt{new]} \text{ // LogicalForm // TableForm} \\ \\ \textit{Cut}[\cdot] \textit{/TableForm} = & \left| 0_{S_1} 0_{S_2} \right\rangle \to \left| 0_{S_1} 0_{S_2} \right\rangle \\ & \left| 0_{S_1} 1_{S_2} \right\rangle \to \left| 1_{S_1} 0_{S_2} \right\rangle \\ & \left| 1_{S_1} 0_{S_2} \right\rangle \to \left| 0_{S_1} 1_{S_2} \right\rangle \\ & \left| 1_{S_1} 1_{S_2} \right\rangle \to \left| 1_{S_1} 1_{S_2} \right\rangle \\ & \left| 1_{S_1} 1_{S_2} \right\rangle \to \left| 1_{S_1} 1_{S_2} \right\rangle \end{array}$$

This is the matrix representation of the SWAP gate in the logical basis.

In the quantum circuit model, the SWAP gate is represented as following.

The SWAP gate can be implemented by means of the CNOT gate.

In[*]:= Elaborate[qc - new]
Out[*]:= 0

Interestingly, the SWAP gate itself is not universal, but the $\sqrt{\text{SWAP}}$ gate—the gate that equals SWAP when squared—is universal. That is, any quantum gate on a multi-qubit system can be implemented by combining $\sqrt{\text{SWAP}}$ and single-qubit rotations (see also Section 2.4 and Section 3.2.2). Indeed, one can combine the $\sqrt{\text{SWAP}}$ gate with single-qubit rotations to construct the CZ gate (Section 3.2.2). As discussed above, the CZ gate requires just two more Hadamard gates to implement the CNOT gate, and it is hence universal.

Let us construct the CZ gate with the $\sqrt{\text{SWAP}}$ gate. This is the matrix representation of the the $\sqrt{\text{SWAP}}$ gate.

$$\label{eq:local_sum} \begin{split} &\textit{In[e]} = \text{ mat = MatrixPower[Matrix@Elaborate@SWAP[S[1], S[2]], 1/2];} \\ &\text{mat // MatrixForm} \\ &\textit{Out[e]/MatrixForm=} \\ &\begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & \frac{1}{2} + \frac{i}{2} & \frac{1}{2} - \frac{i}{2} & 0 \\ 0 & \frac{1}{2} - \frac{i}{2} & \frac{1}{2} + \frac{i}{2} & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} \end{split}$$

This is an explicit operator expression of the $\sqrt{\text{SWAP}}$ gate in terms of the Pauli operators.

$$\log_{|z|=1} \text{ sqrtSWAP} = \left\{ \text{ExpressionFor[mat, S@{1,2}], , "Label"} \rightarrow \text{"} \sqrt{\text{U}} \text{"} \right\}$$

$$\text{Out[s]} = \left\{ \left(\frac{3}{4} + \frac{i}{4} \right) + \left(\frac{1}{4} - \frac{i}{4} \right) \text{ S}_{1}^{z} \text{ S}_{2}^{z} + \left(\frac{1}{2} - \frac{i}{2} \right) \text{ S}_{1}^{+} \text{ S}_{2}^{-} + \left(\frac{1}{2} - \frac{i}{2} \right) \text{ S}_{1}^{-} \text{ S}_{2}^{+}, \text{ Null, Label} \rightarrow \sqrt{\text{U}} \right\}$$

This is a quantum circuit model to construct the CZ gate from the $\sqrt{\text{SWAP}}$ gate and single-qubit gates. In this diagram, \sqrt{U} denotes the $\sqrt{\text{SWAP}}$ gate and U_z the rotation around the z-axis by angle $\pi/2$.

$$\begin{aligned} & \text{Im}_{z} = \text{qc} = \text{QuantumCircuit}[\text{sqrtSWAP}, \, S[1, 3], \, \text{sqrtSWAP}, \\ & & \left\{ \text{Rotation}[\text{Pi} \, / \, 2, \, S[1, \, 3]], \, \text{Rotation}[-\text{Pi} \, / \, 2, \, S[2, \, 3], \, \text{"Label"} \rightarrow \text{"U}_z^{\dagger \text{"}}] \right\} \end{aligned}$$

To check if it indeeds implement the CZ gate, take a look at the matrix representation of the quantum circuit model.

```
Inf= := Matrix[qc] // MatrixForm

Out = MMatrixForm=

\begin{pmatrix}
1 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 1 & 0 \\
0 & 0 & 0 & -1 \end{pmatrix}
```

2.2.2 Controlled-Unitary Gates

Consider two qubits that are once again called control and target qubits. Let \hat{U} be a unitary operator on the target qubit. The controlled- \hat{U} gate is a unitary operator on the two-qubit Hilbert space defined analogously to CNOT by

$$\operatorname{Ctrl}(\hat{U}) : |c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes \hat{U}^c |t\rangle ,$$
 (2.51)

or equivalently, by

$$\operatorname{Ctrl}(\hat{U}) := |0\rangle \langle 0| \otimes \hat{I} + |1\rangle \langle 1| \otimes \hat{U}. \tag{2.52}$$

If the control qubit is in $|0\rangle$, then it does nothing. On the other hand, if the control qubit is in $|1\rangle$, then it operates the unitary operator \hat{U} on the target qubit. Analogously to the CNOT gate, the matrix representation of the controlled-unitary gate is thus given by

$$\operatorname{Ctrl}(\hat{U}) \doteq \begin{bmatrix} 1 & & & & \\ & 1 & & & \\ & & U_{11} & U_{12} \\ & & U_{21} & U_{22} \end{bmatrix}, \qquad (2.53)$$

where U is the matrix representation of \hat{U} . In quantum circuit model, a controlledunitary gate is depicted as

$$(2.54)$$

The filled circle connected to the quantum circuit element on the target qubit indicates the conditional operation of the element conditioned on the state of the control qubit.

Consider a single-qubit rotation acting on the qubit S [2,None]. It is a rotation around the yaxis by angle ϕ .

$$In[\cdot]:= \text{Let}[\text{Real}, \phi]$$

$$U = \text{Rotation}[\phi, S[2, 2], \text{"Label"} \rightarrow \text{"U"}];$$

$$U // \text{Elaborate // Matrix // MatrixForm}$$

$$Out[\cdot]/MatrixForm= \begin{pmatrix} \cos\left[\frac{\phi}{2}\right] & -\sin\left[\frac{\phi}{2}\right] \\ \sin\left[\frac{\phi}{2}\right] & \cos\left[\frac{\phi}{2}\right] \end{pmatrix}$$

This shows the controlled-U gate.

This is the explicit expression of the controlled-U gate operation in terms of the Pauli operators.

$$\begin{array}{l} \mathit{In[*]} := & \mathsf{op} = \mathsf{Elaborate[qc]} \\ \\ \mathit{Out[*]} := & \mathsf{Cos}\left[\frac{\phi}{4}\right]^2 + \mathsf{S}_1^\mathsf{z} \, \mathsf{Sin}\left[\frac{\phi}{4}\right]^2 + \frac{1}{2} \, \mathtt{i} \, \mathsf{S}_1^\mathsf{z} \, \mathsf{S}_2^\mathsf{y} \, \mathsf{Sin}\left[\frac{\phi}{2}\right] - \frac{1}{2} \, \mathtt{i} \, \mathsf{S}_2^\mathsf{y} \, \mathsf{Sin}\left[\frac{\phi}{2}\right] \end{array} \right] \end{array}$$

The controlled-U gate maps the logical basis states as following.

$$\begin{aligned} & \log_{|x| = 0} \text{bs = Basis}[Se\{1, 2\}]; \\ & \text{bs // LogicalForm} \\ & \text{out = op ** bs;} \\ & \text{out // LogicalForm} \end{aligned}$$

$$& \text{Out}(x) = \left\{ \left| 0_{S_1} 0_{S_2} \right\rangle, \left| 0_{S_1} 1_{S_2} \right\rangle, \left| 1_{S_1} 0_{S_2} \right\rangle, \left| 1_{S_1} 1_{S_2} \right\rangle \right\}$$

$$& \text{Out}(x) = \left\{ \left| 0_{S_1} 0_{S_2} \right\rangle, \left| 0_{S_1} 1_{S_2} \right\rangle, \left| 0_{S_1} 1_{S_2} \right\rangle, \left| 1_{S_1} 0_{S_2} \right\rangle + \left| 1_{S_1} 1_{S_2} \right\rangle \right\}$$

$$& \text{Cos}\left[\frac{\phi}{2}\right] \left| 1_{S_1} 1_{S_2} \right\rangle - \left| 1_{S_1} 0_{S_2} \right\rangle \right\}$$

To make the mapping clearer, this tabulates the above result.

$$\begin{array}{l} \textit{In}_{[\cdot]:=} \text{ new = KetFactor}[\#, S[1]] \& /@ \text{ out;} \\ \hline \textit{Thread}[bs \rightarrow \text{new}] \text{ // LogicalForm // TableForm} \\ \\ \textit{Out}_{[\cdot]/TableForm=} \\ & | \theta_{S_1} \theta_{S_2} \rangle \rightarrow | \theta_{S_1} \rangle \otimes | \theta_{S_2} \rangle \\ & | \theta_{S_1} 1_{S_2} \rangle \rightarrow | \theta_{S_1} \rangle \otimes | 1_{S_2} \rangle \\ & | 1_{S_1} \theta_{S_2} \rangle \rightarrow | 1_{S_1} \rangle \otimes \left(\text{Cos} \left[\frac{\phi}{2} \right] \; | \theta_{S_2} \rangle + | 1_{S_2} \rangle \; \text{Sin} \left[\frac{\phi}{2} \right] \right) \\ & | 1_{S_1} 1_{S_2} \rangle \rightarrow | 1_{S_1} \rangle \otimes \left(\text{Cos} \left[\frac{\phi}{2} \right] \; | 1_{S_2} \rangle - | \theta_{S_2} \rangle \; \text{Sin} \left[\frac{\phi}{2} \right] \right) \\ \end{array}$$

Let us take a look at the mapping more closely. When the first qubit is set to Ket[0], it does nothing.

$$\begin{aligned} &\mathit{Inf} \cdot \mathit{j=} \ \ \, \mathsf{Let}[\mathsf{Complex}, \, c] \\ & \ \ \, \mathsf{vec} = \mathsf{Ket}[] \times \mathsf{c}[\theta] + \mathsf{Ket}[\mathsf{S}[2] \to 1] \times \mathsf{c}[2]; \\ & \ \ \, \mathsf{LogicalForm}[\mathsf{vec}, \, \mathsf{S@}\{1, \, 2\}] \end{aligned}$$

$$lo[*]:= new = op ** vec;$$

$$LogicalForm[new, S@\{1, 2\}]$$

$$Out[*]:= c_0 |0_{S_1}0_{S_2}\rangle + c_2 |0_{S_1}1_{S_2}\rangle$$

When the control qubit -- the first qubit in this case -- is set to Ket[1], it operates the unitary operator on the second qubit.

$$\begin{aligned} & \text{In}[\cdot] = \text{ new = op ** vec;} \\ & \text{LogicalForm}[\text{new, S@\{1, 2\}}] \end{aligned}$$

$$& \text{Out}[\cdot] = \left| \mathbf{1}_{S_1} \mathbf{1}_{S_2} \right\rangle \left(\mathbf{c}_2 \operatorname{Cos}\left[\frac{\phi}{2}\right] + \mathbf{c}_0 \operatorname{Sin}\left[\frac{\phi}{2}\right] \right) + \left| \mathbf{1}_{S_1} \mathbf{0}_{S_2} \right\rangle \left(\mathbf{c}_0 \operatorname{Cos}\left[\frac{\phi}{2}\right] - \mathbf{c}_2 \operatorname{Sin}\left[\frac{\phi}{2}\right] \right) \end{aligned}$$
 When the control qubit is in a superposition, the resulting state is an entangled state in general.

$$\begin{split} & \ln[\cdot] = \text{ vec = Ket}[] + \text{Ket}[S[1] \rightarrow 1]; \\ & \text{LogicalForm}[\text{vec, } S@\{1, 2\}] \\ & \text{Out}[\cdot] = \left| \left| \Theta_{S_1} \Theta_{S_2} \right\rangle + \left| 1_{S_1} \Theta_{S_2} \right\rangle \\ & \ln[\cdot] = \text{ new = op ** vec;} \\ & \text{LogicalForm}[\text{new, } S@\{1, 2\}] \\ & \text{Out}[\cdot] = \left| \left| \Theta_{S_1} \Theta_{S_2} \right\rangle + \text{Cos}\left[\frac{\phi}{2}\right] \left| 1_{S_1} \Theta_{S_2} \right\rangle + \left| 1_{S_1} 1_{S_2} \right\rangle \text{Sin}\left[\frac{\phi}{2}\right] \\ & \text{Out}[\cdot] = \left| \left| \Theta_{S_1} \Theta_{S_2} \right\rangle + \text{Cos}\left[\frac{\phi}{2}\right] \left| 1_{S_1} \Theta_{S_2} \right\rangle + \left| 1_{S_1} 1_{S_2} \right\rangle \text{Sin}\left[\frac{\phi}{2}\right] \\ & \text{Out}[\cdot] = \left| \left| \Theta_{S_1} \Theta_{S_2} \right\rangle + \text{Cos}\left[\frac{\phi}{2}\right] \left| 1_{S_1} \Theta_{S_2} \right\rangle + \left| 1_{S_1} 1_{S_2} \right\rangle \text{Sin}\left[\frac{\phi}{2}\right] \end{split}$$

An important aspect of a controlled-unitary operator is that it induces relative phase shifts on the *control* qubit when the target qubit has been prepared in an eigenstate of \hat{U} . At first glace, it may sound counter intuitive since the definition in (2.51) seems to indicate that it changes only the target qubit depending on the state of the control qubit and keeps the latter intact. This is another feature distinguishing quantum gates from their classical counterparts. Let us take a closer look to see how this works. Prepare the control qubit in a superposition $|\psi\rangle = |0\rangle + |1\rangle$ and the target qubit in an eigenstate $|u\rangle$ of \hat{U} with eigenvalue $e^{i\phi}$. The controlled-unitary gate transforms the state to

$$\begin{split} |\psi\rangle\otimes|u\rangle\rightarrow|0\rangle\otimes|u\rangle+|1\rangle\otimes\hat{U}\,|u\rangle\\ &=|0\rangle\otimes|u\rangle+|1\rangle\otimes|u\rangle\,e^{i\phi}=\left(|0\rangle+|1\rangle\,e^{i\phi}\right)\otimes|u\rangle\,. \quad (2.55) \end{split}$$

This feature is extended to multi-control unitary gates and plays a crucial role in many quantum algorithms. In particular, the quantum phase estimation algorithm (Section 4.4) is a direct consequence of this feature.

When the target qubit is set to an eigenstate of the unitary operator, it does not change but the control qubit acquires the phase factor given by the eigenvalue of the target state.

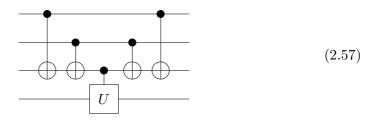
```
\begin{split} & \mathit{In}(\cdot) := \ \mathsf{vec} = \ (\mathsf{Ket}[] + \mathsf{Ket}[\mathsf{S}[1] \to 1]) \ ** \ (\mathsf{Ket}[] - \mathsf{I} \, \mathsf{Ket}[\mathsf{S}[2] \to 1]) \ ; \\ & \ \mathsf{LogicalForm}[\mathsf{KetFactor@vec}, \, \mathsf{S@}\{1, \, 2\}] \end{split}  & \mathcal{O}_{\mathit{Ul}(\cdot)} := \ \left( \left| \, \mathsf{O}_{\mathsf{S}_1} \right\rangle + \left| \, \mathsf{1}_{\mathsf{S}_1} \right\rangle \right) \otimes \left( \left| \, \mathsf{O}_{\mathsf{S}_2} \right\rangle - \mathrm{i} \, \left| \, \mathsf{1}_{\mathsf{S}_2} \right\rangle \right) \\ & \mathcal{O}_{\mathit{Ul}(\cdot)} := \ \mathsf{new} = \ \mathsf{op} ** \ \mathsf{vec} \ /' \ \mathsf{TrigToExp}; \\ & \mathsf{LogicalForm}[\mathsf{KetFactor@new}, \, \mathsf{S@}\{1, \, 2\}] \\ & \mathcal{O}_{\mathit{Ul}(\cdot)} := \ \left( \left| \, \mathsf{O}_{\mathsf{S}_1} \right\rangle + \, \mathrm{e}^{\frac{\mathrm{i}\,\vartheta}{2}} \, \left| \, \mathsf{1}_{\mathsf{S}_1} \right\rangle \right) \otimes \left( \left| \, \mathsf{O}_{\mathsf{S}_2} \right\rangle - \mathrm{i} \, \left| \, \mathsf{1}_{\mathsf{S}_2} \right\rangle \right) \\ & \mathcal{O}_{\mathit{Ul}(\cdot)} := \ \left( \left| \, \mathsf{O}_{\mathsf{S}_1} \right\rangle + \, \mathrm{e}^{\frac{\mathrm{i}\,\vartheta}{2}} \, \left| \, \mathsf{1}_{\mathsf{S}_1} \right\rangle \right) \otimes \left( \left| \, \mathsf{O}_{\mathsf{S}_2} \right\rangle - \mathrm{i} \, \left| \, \mathsf{1}_{\mathsf{S}_2} \right\rangle \right) \end{aligned}
```

The CNOT gate and the controlled-unitary gate can be combined to achieve a variety of conditional gate operations. For example, consider a system consisting of a control register of n qubits and a target register of a single qubit. Suppose

that you want to operate a unitary gate \hat{U} on the target qubit only when an odd number of the control qubits is set to $|1\rangle$,

$$|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes \hat{U}^{c_1 \oplus \cdots \oplus c_n} |t\rangle$$
 (2.56)

First operate the CNOT gates consecutively with the first (n-1) qubits in the control register as the control qubit and the last qubit in the control register as the target qubit. This transforms the *n*th qubit to $|c_1 \oplus \cdots \oplus c_n\rangle$ (Problem 2.9). Then, the desired operation is implemented by applying the controlled- \hat{U} gate controlled by the *n*th qubit. To get the control qubits back to the original state, operate the CNOT gates in the reverse order. Overall, the following quantum circuit implements the conditional operation



for the case of n = 3. This method is used to implement a multi-control unitary gate based on the Gray code (see Section 2.3).

This shows a quantum circuit model conditionally operating the logic gate U on the target qubit.

```
In[-]:= $n = 3;
    cc = Table[CNOT[S[j], S[$n]], {j, 1, $n - 1}];
    op = Rotation[φ, S[$n + 1, 2]];
    cU = ControlledU[S[$n], op];
    qc = QuantumCircuit[Sequence @@ Flatten@{cc, cU, Reverse@cc}]
```

This shows how the above quantum circuit model maps the logical basis states. It affects the target qubit only when $c_1 \oplus c_2 \oplus c_3 = 1$.

```
\label{eq:mean_series} \begin{split} &\text{hs} = \text{Basis}[\text{S@Range}[\$n]\,,\,\text{None}];\\ &\text{bs} = \text{Basis}[\text{S@Range}[\$n+1]];\\ &\text{out} = \text{Elaborate}[\text{qc}]\, **\, \text{bs};\\ &\text{new} = \text{KetFactor}[\#,\,\text{ss}]\, \&\,/\text{@}\,\text{out};\\ &\text{tbl} = \text{Thread}[\text{bs} \to \text{new}]\,\,/\,\,\text{LogicalForm};\\ &\text{tbl}[\#;;\,5]\,\,//\,\,\text{TableForm}\\ &\text{Out}[\cdot]//\text{TableForm}\\ &\text{Out}[\cdot]//\text{TableForm}\\ &\text{Os}_1\text{Os}_2\text{Os}_3\text{Os}_4\,\rangle \to \left|\,\text{Os}_1\text{Os}_2\text{Os}_3\,\rangle \otimes \left|\,\text{Os}_4\,\right\rangle}\\ &\text{Os}_1\text{Os}_2\text{Os}_3\text{Os}_4\,\rangle \to \left|\,\text{Os}_1\text{Os}_2\text{Os}_3\,\right\rangle \otimes \left|\,\text{Is}_4\,\right\rangle}\\ &\text{Os}_1\text{Os}_2\text{Is}_3\text{Os}_4\,\rangle \to \left|\,\text{Os}_1\text{Os}_2\text{Is}_3\,\right\rangle \otimes \left(\text{Cos}\left[\frac{\phi}{2}\right]\,\left|\,\text{Os}_4\,\right\rangle + \left|\,\text{Is}_4\,\right\rangle\,\,\text{Sin}\left[\frac{\phi}{2}\right]\right)\\ &\text{Os}_1\text{Os}_2\text{Is}_3\text{Is}_4\,\rangle \to \left|\,\text{Os}_1\text{Os}_2\text{Is}_3\,\right\rangle \otimes \left(\text{Cos}\left[\frac{\phi}{2}\right]\,\left|\,\text{Is}_4\,\right\rangle - \left|\,\text{Os}_4\,\right\rangle\,\,\text{Sin}\left[\frac{\phi}{2}\right]\right)\\ &\text{Os}_1\text{Is}_2\text{Os}_3\text{Os}_4\,\rangle \to \left|\,\text{Os}_1\text{Is}_2\text{Os}_3\,\right\rangle \otimes \left(\text{Cos}\left[\frac{\phi}{2}\right]\,\left|\,\text{Os}_4\,\right\rangle + \left|\,\text{Is}_4\,\right\rangle\,\,\text{Sin}\left[\frac{\phi}{2}\right]\right) \end{split}
```

How can you implement a controlled-unitary gate? The operation involves only two qubits, and in principle, it should be possible to implement any specific controlled-unitary gate. However, it will become clear in Chapter 3, the requirements for a physical implementation of two-qubit gates is far more difficult to fulfill on realistic systems than for single-qubit gates. Fortunately, any controlled-unitary gate can be implemented using only a CNOT gate and single-qubit gates This is one of the basic steps in establishing universal quantum computation.

Let \hat{U} be a unitary gate on the second (target) qubit controlled by the first (control) qubit. Suppose that \hat{U} has Euler angles α , β , and γ and an additional phase factor $e^{i\varphi}$ [see (2.27)], $\hat{U} = e^{i\varphi}\hat{U}_z(\alpha)\hat{U}_y(\beta)\hat{U}_z(\gamma)$. Then one can always find three unitary operators \hat{A} , \hat{B} , and \hat{C} such that

$$\hat{U} = e^{i\varphi} \hat{A} \hat{X} \hat{B} \hat{X} \hat{C} , \quad \hat{A} \hat{B} \hat{C} = \hat{I} , \qquad (2.58)$$

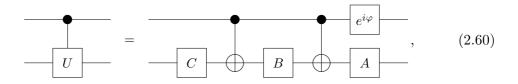
where \hat{X} is the Pauli X operator. More explicitly, one common choice is

$$\hat{A} = \hat{U}_z(\alpha)\hat{U}_y(\beta/2), \qquad (2.59a)$$

$$\hat{B} = \hat{U}_y(-\beta/2)\hat{U}_z(-(\alpha+\gamma)/2),$$
 (2.59b)

$$\hat{C} = \hat{U}_z(-(\alpha - \gamma)/2). \tag{2.59c}$$

Since $\hat{U}_{y/z}(\phi) = \hat{X}\hat{U}_{y/z}(-\phi)\hat{X}$ for any ϕ , the above choice satisfies the desired properties in (2.58). These properties imply that the controlled-unitary gate can be implemented as in the following quantum circuit



where the last gate on the first qubit is the *relative* phase shift by φ , $|0\rangle\langle 0| + |1\rangle\langle 1|e^{i\varphi}$. Indeed, when the control qubit is in $|0\rangle$, the two CNOT gates in the

middle do nothing, and the combined operator $\hat{A}\hat{B}\hat{C}$ on the target qubit ends up with the identity operator. With the control qubit in $|1\rangle$, on the other hand, the two CNOT gates are operational and the overall operator on the target qubit becomes $\hat{A}\hat{X}\hat{B}\hat{X}\hat{C} = e^{-i\varphi}\hat{U}$, where the phase factor is cancel by the opposite phase from the control qubit.

```
Consider a controlled-U gate.
   Info ]:= matU = RandomUnitary[2];
        matU // MatrixForm
         (-0.491304 + 0.43834 i -0.385361 + 0.64651 i
         -0.23368 + 0.715452 i 0.00718061 - 0.658384 i
In[*]:= opU = ExpressionFor[matU, S[2]];
     qc1 = QuantumCircuit[ControlledU[S[1], opU, "Label" \rightarrow "U"]]
     For the decomposition, first find the Euler angles of the unitary operator.
In[*]:= detU = Det[matU];
     vphi = Arg[detU] / 2;
     {a, b, c} = TheEulerAngles[matU / Sqrt[detU]]
Out[\circ]= {-0.526614, 1.70415, -3.44638}
     From the Euler angles, choose the component operators.
     opA = EulerRotation[\{a, b/2, 0\}, S[2], "Label" \rightarrow "A"];
     opB = EulerRotation[\{0, -b/2, -(a+c)/2\}, S[2], "Label" \rightarrow "B"];
     opC = EulerRotation[\{0, 0, -(a-c)/2\}, S[2], "Label" \rightarrow "C"];
     opD = Phase[vphi, S[1]];
     Finally, construct the equivalent quantum circuit model.
ln[s] = qc2 = QuantumCircuit[opC, CNOT[S[1], S[2]], opB, CNOT[S[1], S[2]], opA, opD]
     Check the result.
In[*]:= Elaborate[qc1 - qc2] // Chop
Out[=]= 0
```

2.2.3 General Unitary Gate

Here we decompose an arbitrary two-qubit unitary gate into factors of controlledunitary gates only. This is a remarkable advantage when one tries to build a quantum computer as you can just focus on how to implement the CNOT gate and single-qubit gates. Moreover, the same idea eventually leads to the proof of universal quantum computation on a larger system, which will be discussed in Section 2.4.

Consider an arbitrary two-qubit unitary operator \hat{U} . In the logical basis, it is represented by a unitary matrix

$$U = \begin{bmatrix} U_{11} & U_{12} & U_{13} & U_{14} \\ U_{21} & U_{22} & U_{23} & U_{24} \\ U_{31} & U_{32} & U_{33} & U_{34} \\ U_{41} & U_{42} & U_{43} & U_{44} \end{bmatrix}.$$
 (2.61)

To break the unitary operation \hat{U} down into more elementary quantum logic gates, we make use of two-level unitary transformations. A two-level unitary transformation is a unitary operation with matrix representation that acts only on the two columns and rows of other matrices or column or row vectors. The descriptive word "two-level" should not confused with "two-qubit". A two-level transformation acts on multiple qubits, but it just transforms only two rows or columns at a time in the representation. For example, consider a two-level unitary transformation of the form

$$T_{1} = \begin{bmatrix} 1 & & & & \\ & 1 & & & \\ & & \tilde{U}_{13}^{*} & \tilde{U}_{14} \\ & & \tilde{U}_{14}^{*} & -\tilde{U}_{13} \end{bmatrix}, \tag{2.62}$$

where $\tilde{U}_{ij} \propto U_{ij}$ with normalization factor (unspecified) such that $T_1^{\dagger}T_1 = T_1T_1^{\dagger} = I$. When it multiplies U from the right, it does not change the first two columns of U. It only alters the last two columns, and hence the name "two-level transformation". The elements in the lower-right sub-block of T_1 have been chosen so that the first element of the last column is canceled:

$$UT_{1} = \begin{bmatrix} U_{11} & U_{12} & U'_{13} & 0 \\ U_{21} & U_{22} & U'_{23} & U'_{24} \\ U_{31} & U_{32} & U'_{33} & U'_{34} \\ U_{41} & U_{42} & U'_{43} & U'_{44} \end{bmatrix}.$$
 (2.63)

Now take another two-level unitary transformation, this time, of the form

$$T_2 = \begin{bmatrix} 1 & & & & \\ & \tilde{U}_{12}^* & \tilde{U}_{13}' & & \\ & \tilde{U}_{13}'^* & -\tilde{U}_{12} & & \\ & & & & 1 \end{bmatrix}.$$
 (2.64)

It removes U_{13}^{\prime} while keeping the first and last column as they are:

$$UT_{1}T_{2} = \begin{bmatrix} U_{11} & U_{12}'' & 0 & 0 \\ U_{21} & U_{22}'' & U_{23}'' & U_{24}' \\ U_{31} & U_{32}'' & U_{33}'' & U_{34}' \\ U_{41} & U_{42}'' & U_{43}'' & U_{44}' \end{bmatrix} .$$
 (2.65)

Similarly, we go further with the two-level unitary matrix

$$T_{2} = \begin{bmatrix} \tilde{U}_{12}^{*} & \tilde{U}_{13}' \\ \tilde{U}_{13}^{**} & -\tilde{U}_{12} \\ & & 1 \\ & & & 1 \end{bmatrix}.$$
 (2.66)

to remove the element U_{12}'' and get

$$UT_{1}T_{2}T_{3} = \begin{bmatrix} U_{11}^{\prime\prime\prime} & 0 & 0 & 0\\ 0 & U_{22}^{\prime\prime\prime} & U_{23}^{\prime\prime} & U_{24}^{\prime}\\ 0 & U_{32}^{\prime\prime\prime} & U_{33}^{\prime\prime} & U_{34}^{\prime}\\ 0 & U_{42}^{\prime\prime\prime} & U_{43}^{\prime\prime} & U_{44}^{\prime} \end{bmatrix}.$$
 (2.67)

At this stage, all elements except for the first of the first column vanish $-U_{21}''' = U_{31}''' = U_{41}''' = 0$ —because the product $UT_1T_2T_3$ is a unitary matrix. Repeating the procedure, we can remove all off-diagonal elements to get

$$UT_1T_2\dots T_L = I. (2.68)$$

As T_j are all unitary, it enables us to rewrite U in a combination of two-level unitary transformations, that is,

$$U = T_L^{\dagger} \dots T_2^{\dagger} T_1^{\dagger}. \tag{2.69}$$

Consider a Hermitian operator on a two-qubit system. Physically, it corresponds to a transverse-field Ising model.

$$\begin{aligned} & \text{In}_{\{\cdot\}} = \text{H} = \text{S}[1, 1] ** \text{S}[2, 1] + \text{S}[1, 2] + \text{S}[2, 2] + \text{S}[1, 3] + \text{S}[2, 3] \\ & \text{matH} = \text{Matrix}[H]; \\ & \text{matH} // \text{MatrixForm} \end{aligned}$$

$$\begin{aligned} & \text{Out}_{\{\cdot\}} = \text{S}_{1}^{\text{X}} \text{S}_{2}^{\text{X}} + \text{S}_{1}^{\text{Y}} + \text{S}_{1}^{\text{Y}} + \text{S}_{2}^{\text{Y}} + \text{S}_{2}^{\text{Y}} \\ & \text{Out}_{\{\cdot\}} = \text{S}_{1}^{\text{X}} \text{S}_{2}^{\text{X}} + \text{S}_{1}^{\text{Y}} + \text{S}_{1}^{\text{Y}} + \text{S}_{2}^{\text{Y}} \\ & \text{Out}_{\{\cdot\}} = \text{Matrix} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} = \text{In}_{\{\cdot\}} \\ & \text{In}_{\{\cdot\}} = \text$$

This is the unitary operator generated by the above Hermitian operator.

$$lo[+]:= \mathbf{U} = \mathbf{MultiplyExp[-I HPi/4]}$$

$$lou[+]:= e^{-\frac{1}{4} i \pi \left(\mathbf{S}_{1}^{x} \mathbf{S}_{2}^{x} + \mathbf{S}_{1}^{y} + \mathbf{S}_{2}^{y} + \mathbf{S}_{2}^{x} \right)}$$

This shows the matrix representation of **u** in the logical basis.

$$\begin{array}{lll} & \textit{In}\{e\} \coloneqq \mbox{ matU} = \mbox{MatrixForm} \\ & \textit{Out}\{e\} \mbox{MatrixForm} \\ & \textit{Out}\{e\} \mbox{MatrixForm} \\ & & = \left(-\frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{6} - \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{6} - \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{6} - \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{2} - \frac{1}{2}}{\sqrt{2}} \\ & & & = \left(-\frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} \right) \\ & & & = \left(-\frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} \right) \\ & & & = \left(-\frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} \right) \\ & & & = \left(-\frac{\frac{1}{2} + \frac{5}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{6}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} - \frac{\frac{1}{2} + \frac{1}{2}}{\sqrt{2}} \right) \end{array}$$

This shows the decomposition of the unitary matrix into two-level matrices (displaying the first three elements). For the purpose of efficiency, the resulting list is given in terms of TwoLevelu.

$$\begin{aligned} & \text{Implify;} \\ & \text{twl} \text{ [; ; 3] } \\ & \text{Outsp} \text{ } \left\{ \text{TwoLevelDecomposition[matU] // Simplify;} \\ & \text{twl} \text{ [; ; 3] } \right\} \\ & \text{Outsp} \text{ } \left\{ \text{TwoLevelU} \left[\left\{ \left\{ \frac{1}{2} - \frac{3\,\dot{i}}{2}}{\sqrt{7}} \,,\, -\frac{\frac{3}{2} + \frac{3\,\dot{i}}{2}}{\sqrt{7}} \right\},\, \left\{ \frac{\frac{3}{2} - \frac{3\,\dot{i}}{2}}{\sqrt{7}} \,,\, \frac{\frac{1}{2} + \frac{3\,\dot{i}}{2}}{\sqrt{7}} \right\} \right\},\, \left\{ 3,\, 4 \right\},\, 4 \right], \\ & \text{TwoLevelU} \left[\left\{ \left\{ -\frac{1-3\,\dot{i}}{\sqrt{38}} \,,\, \sqrt{\frac{14}{19}} \right\},\, \left\{ -\sqrt{\frac{14}{19}} \,,\, -\frac{1+3\,\dot{i}}{\sqrt{38}} \right\} \right\},\, \left\{ 2,\, 3 \right\},\, 4 \right] \right\} \\ \end{aligned}$$

For a more intuitive form, you can convert TwoLevelU into the normal matrix form using Matrix. Here the first three are shown.

$$\text{MatrixForm/@Matrix/@twl[;;3]} \\ \text{Out[*]=} \left\{ \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}, \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & \frac{1}{2} - \frac{3i}{2} & \frac{3}{2} + \frac{3i}{2} \\ 0 & 0 & \frac{\frac{3}{2} - \frac{3i}{2}}{\sqrt{7}} & \frac{1}{2} + \frac{3i}{2} \\ 0 & 0 & \frac{\frac{3}{2} - \frac{3i}{2}}{\sqrt{7}} & \frac{1}{2} + \frac{3i}{2} \\ 0 & 0 & 0 & 0 & 1 \end{pmatrix}, \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & -\frac{1-3i}{\sqrt{38}} & \sqrt{\frac{14}{19}} & 0 \\ 0 & -\sqrt{\frac{14}{19}} & -\frac{1+3i}{\sqrt{38}} & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} \right\}$$

Indeed, they reconstruct the original matrix.

We are still to express the two-level unitary transformation in terms of a controlled-unitary gate. The two-level unitary matrix—for example, T_1 in (2.62)—of the form

$$\begin{bmatrix} 1 & & & & \\ & 1 & & & \\ & & U_{11} & U_{12} \\ & & U_{21} & U_{22} \end{bmatrix}, \tag{2.70}$$

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is already in the form of the matrix representation of a controlled-unitary gate in (2.53), and just corresponds to a single controlled-unitary:

Consider a two-level matrix of the form.

It corresponds to a single controlled-U gate.

A two-level unitary matrix of the form

$$\begin{bmatrix} 1 & & & & \\ & U_{11} & & U_{12} & & \\ & & 1 & & \\ & & U_{21} & & U_{22} \end{bmatrix}$$
 (2.72)

also corresponds to a single controlled-unitary gate with the control and target qubit exchanged. Here, the first qubit is the target qubit and the second the control qubit:

Consider a two-level matrix of the form.

$$\label{eq:local_$$

It corresponds to a single controlled-U gate with the control and target qubit exchanged.

More complicated is the two-level unitary matrix of the form

$$\begin{bmatrix} 1 & & & & \\ & U_{11} & U_{12} & & \\ & U_{21} & U_{22} & & \\ & & & 1 \end{bmatrix} . (2.74)$$

As it affects both qubits simultaneously, it cannot be represented by a single controlled-unitary gate. However, it is possible to bring it to the form of (2.71) by exchanging the second and fourth columns and rows. The specified exchanges correspond to flipping the bit values of the first qubit only when the second qubit has a value of 1, that is, the CNOT gate with the second qubit as the control qubit and the first qubit as the target qubit. Through this exchange, the unitary matrix U itself is modified and the rows and columns are exchanged, which corresponds to the basis change by the Pauli X matrix, $U \to U' = XUX$. Putting all together, the two-level unitary matrix is implemented as

$$\begin{bmatrix} 1 & & & & & \\ & U_{11} & U_{12} & & & \\ & U_{21} & U_{22} & & & \\ & & & & 1 \end{bmatrix} = \underbrace{U'} U' \underbrace{U'} . \tag{2.75}$$

Consider a two-level matrix of the form.

In this case, we need to apply the CNOT gate before and after the controlled-U gate.



In a similar manner, we can implement another form of two-level unitary matrix as

$$\begin{bmatrix} U_{11} & & U_{12} \\ & 1 & \\ & & 1 \\ U_{21} & & U_{22} \end{bmatrix} = \underbrace{U} \qquad (2.76)$$

Here we have adopted a short-hand diagram for the modified-CNOT gate, which flips the bit value of the target qubit when the control qubit is in the state $|0\rangle$ rather than $|1\rangle$. It is achieved by operating the Pauli X gate before and after the usual CNOT gate,

$$= \begin{array}{c} X \\ \hline \end{array}$$
 (2.77)

Consider a two-level matrix of the form.

So far, we have determined the implementations of 4×4 two-level unitary matrices of different forms just by simple inspection. For more than two qubits, the size of the two-level unitary matrices is much larger and it is difficult to find proper implementations in such a way. Fortunately, there is a systematic way based on the Gray code, which will be discussed later in Section 2.4.

In summary, an arbitrary two-qubit unitary gate can be carried out by first decomposing its matrix representation into two-level unitary matrices and then implementing the two-level unitary matrices by means of the CNOT gate and the controlled-unitary gate.

Let us consider again the two-qubit model demonstrated before.

$$\begin{aligned} & \text{M} \{ \cdot \} = \text{H} = \text{S}[1, 1] ** \text{S}[2, 1] + \text{S}[1, 2] + \text{S}[2, 2] + \text{S}[1, 3] + \text{S}[2, 3] \\ & \text{U} = \text{MultiplyExp}[-\text{I H Pi / 4}] \\ & \text{matU} = \text{Matrix}[\text{U}]; \\ & \text{Out} \{ \cdot \} = \text{S}_1^{\text{X}} \text{S}_2^{\text{X}} + \text{S}_1^{\text{Y}} + \text{S}_1^{\text{Z}} + \text{S}_2^{\text{Y}} + \text{S}_2^{\text{Z}} \\ & \text{Out} \{ \cdot \} = \text{e}^{-\frac{1}{4}} \text{in } (\text{S}_1^{\text{X}} \text{S}_2^{\text{X}} + \text{S}_1^{\text{X}} + \text{S}_2^{\text{Y}} + \text{S}_2^{\text{Z}} + \text{S}_2^{\text{X}}) \end{aligned}$$

This is the decomposition into the two-level matrices, just showing the first four.

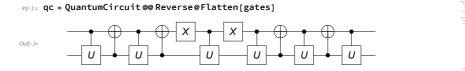
$$\begin{aligned} & \text{MatrixForm /@ Matrix /@ twl[]; 3]} \\ & \text{MatrixForm /@ Matrix /@ twl[]; 3]} \\ & \text{Out[s]} = \left\{ \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}, \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & \frac{1}{2} - \frac{3i}{2} & \frac{3}{2} + \frac{3i}{2} \\ 0 & 0 & \frac{\frac{3}{2} - \frac{3i}{2}}{\sqrt{7}} - \frac{\frac{3}{2} + \frac{3i}{2}}{\sqrt{7}} \\ 0 & 0 & \frac{\frac{3}{2} - \frac{3i}{2}}{\sqrt{7}} & \frac{1}{\sqrt{7}} \end{pmatrix}, \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & -\frac{1-3i}{\sqrt{38}} & \sqrt{\frac{14}{19}} & 0 \\ 0 & -\sqrt{\frac{14}{19}} & -\frac{1+3i}{\sqrt{38}} & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} \right\}$$

The two-level matrices are written in terms of the controlled-U and CNOT gate, again showing the first four. The first of the list twl happens to be the identity matrix in this case, and it is excluded in the further analysis.

$$\begin{split} & \text{Interpolation of the problem of the problem$$

This shows the overall quantum circuit model. Here the same label "U" has been used for different controlled-U gates just for

convenience. Do not forget to reverse the order before plugging the gates in the quantum circuit model.



Check the above quantum circuit by converting it into the matrix representation.

2.3 Multi-Control Unitary Gates

Let $\mathcal{S}^{\otimes m}$ and $\mathcal{S}^{\otimes n}$ be the Hilbert spaces of the control and target register consisting of m and n qubits, respectively. Suppose that \hat{U} is a unitary operator on the target register. The multi-control unitary operator is defined by

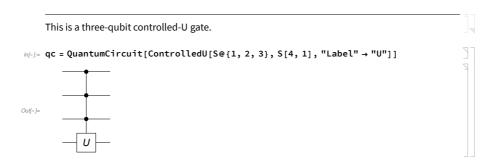
$$\operatorname{Ctrl}(\hat{U}) : |c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes \hat{U}^{c_1 c_2 \cdots c_m} |t\rangle ,$$
 (2.78)

where $c \equiv (c_1 c_2 \dots c_m)_2$. The unitary transformation \hat{U} acts on the target qubits only when every control qubit is set to $|1\rangle$. We will be most interested in the case with n = 1, where the matrix representation takes the form

$$\operatorname{Ctrl}(\hat{U}) \doteq \begin{bmatrix} 1 & & & & \\ & 1 & & & \\ & & \ddots & & \\ & & & U_{11} & U_{12} \\ & & & U_{21} & U_{22} \end{bmatrix}.$$
 (2.79)

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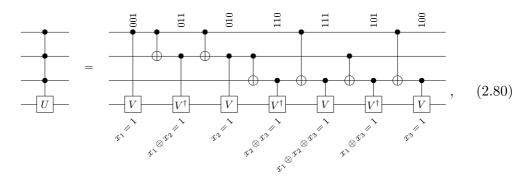
The above is the prototype form of a two-level unitary matrix on (m+1) qubits. Indeed, any two-level unitary matrix can be put into this form by exchanging columns and rows—equivalent to basis changes. The multi-control unitary gates thus arise naturally as we discuss the universal quantum computation in Section 2.4.



A reliable implementation of multi-control unitary gates is essential in many quantum algorithms. A notable example is the quantum oracle (see Section 4.2.1), which is a key component of quantum decision problems and quantum search problems. Here, we introduce some widely known methods to implement it efficiently.

2.3.1 Gray Code

We first discuss a systematic method based on the Gray code to decompose a multi-control unitary gate into factors of either the single-qubit controlled-unitary or CNOT gate. For example, consider a 3-qubit controlled-unitary gate as shown in the following quantum circuit (Barenco et al., 1995)



where \hat{V} is another unitary operator such that $\hat{V}^4 = \hat{U}$. When every control qubit is set to $|1\rangle$, all \hat{V} gates in the diagram take effects on the target qubit while \hat{V}^{\dagger} gates are ineffective. To examine the case with the control qubits in a general state $|x_1\rangle \otimes \cdots \otimes |x_n\rangle$, note that the gates on the target qubit are effective under the conditions specified in terms of the bit values at the bottom of the diagram [see also Eq. (2.57)]. The conditions are systematically fulfilled by following the

Gray code sequence,^{2,2} the strings of which are indicated at the top of the diagram. The identity for the bitwise AND,

$$2^{n-1}(x_1 x_2 \cdots x_n) = \sum_{k_1} x_{k_1} - \sum_{k_1 < k_2} (x_{k_1} \oplus x_{k_2})$$

$$+ \sum_{k_1 < k_2 < k_3} (x_{k_1} \oplus x_{k_2} \oplus x_{k_3}) - \dots + (-1)^{n-1}(x_1 \oplus x_2 \oplus \dots \oplus x_n), \quad (2.81)$$

ensures that the quantum circuit on the right-hand side of (2.80) reproduces the desired multi-control unitary gate on the left-hand side.

In general, an n-qubit controlled-unitary gate can be implemented by combining 2^{n-1} controlled-V gates, $(2^{n-1}-1)$ controlled- V^{\dagger} gates, and (2^n-2) CNOT gates, where \hat{V} is a unitary operator satisfying $\hat{V}^{2^{n-1}}=\hat{U}$. For a relatively small n $(n \leq 8)$, the Gray code is known to be the most efficient method. However, it grows exponentially and eventually becomes impractical. For such cases, several methods have been proposed where the computational cost increases quadratically with the size of the quantum register (Barenco et al., 1995; Nielsen & Chuang, 2011).

Consider a three-qubit register as an example.

This is the gate to operate on the target qubit.

$$\begin{aligned} & \textit{In}[\cdot] = \text{T} = \text{S}[\$\text{L} + 1, \text{None}]; \\ & \text{op} = \text{Rotation}[\text{Pi} / 3, \text{T}[1]] \text{ // Elaborate} \end{aligned}$$

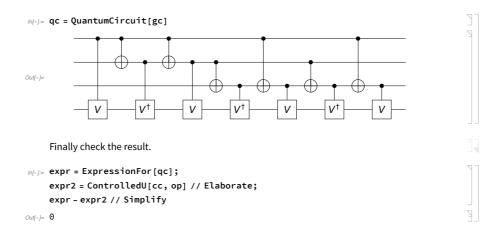
$$\begin{aligned} & \textit{Out}[\cdot] = \frac{\sqrt{3}}{2} - \frac{\text{i } \text{S}_{4}^{\text{X}}}{2} \end{aligned}$$

This decomposes the multi-control unitary gate based on the Gray code -- showing only a part of it. Each component is either CNOT or a two-qubit controlled-U gate.

$$\begin{split} & \inf_{|z|} \text{ gc = GrayControlledU[cc, op]; gc[;; 3]} \\ & \text{ out}_{|z|} \text{ } \left\{ \text{ControlledU} \Big[\left\{ S_1 \right\}, \right. \\ & \frac{2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} + 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}}}{2 \times 2^{1/4}} + \frac{\left(2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} - 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}} \right) \, S_4^+}{2 \times 2^{1/4}} + \frac{\left(2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} - 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}} \right) \, S_4^-}{2 \times 2^{1/4}}, \, \text{Label} \to V \Big] \,, \\ & \text{CNOT[} \left\{ S_1 \right\}, \, \left\{ S_2 \right\} \Big], \, \text{ControlledU} \Big[\left\{ S_2 \right\}, \\ & \frac{2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} + 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}}}{2 \times 2^{1/4}} + \frac{\left(-2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} + 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}} \right) \, S_4^-}{2 \times 2^{1/4}} + \frac{\left(-2^{1/4} \, \mathrm{e}^{-\frac{\mathrm{i}\,\pi}{24}} + 2^{1/4} \, \mathrm{e}^{\frac{\mathrm{i}\,\pi}{24}} \right) \, S_4^-}{2 \times 2^{1/4}}, \, \text{Label} \to V^\dagger \Big] \Big\} \,. \end{split}$$

This is a quantum circuit model of the decomposition.

^{2.2}The *Gray code sequence* is an arrange of bits such that two successive values differ in only one bit.



2.3.2 Multi-Control NOT Gate

The multi-control NOT gate is an important special case of the multi-control unitary gate, where the unitary operator \hat{U} equals to the Pauli \hat{X} . It transforms the logical basis states as [see Eq. (2.78)]

$$|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes \hat{X}^{c_1 c_2 \cdots c_m} |t\rangle ,$$
 (2.82a)

or equivalently,

$$|c\rangle \otimes |t\rangle \mapsto |c\rangle \otimes |(c_1c_2\cdots c_n) \oplus t\rangle$$
 (2.82b)

The multi-control NOT gate commonly occurs when one converts the two-level unitary transformation (see Section 2.2.3) on more than two qubits. Indeed, we will generalize the procedure and discuss a systematic way of conversion based on the Gray code in Section 2.4.

This shows a generalized CNOT gate, controlled by three qubits rather than a single qubit.

In[-]:= qc = QuantumCircuit[CNOT[S@{1, 2, 3}, S[4]]]

Out[-]:=

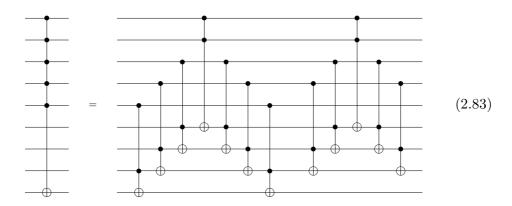
Here is the explicit expression of the multi-control-NOT gate in terms of the Pauli operators.

$$\begin{aligned} & \text{In}[\cdot]:= \text{ op = ExpressionFor}[qc] \\ & \text{Out}[\cdot]:= \frac{7}{8} - \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{2}^{z} - \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{3}^{z} - \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{4}^{x} - \frac{1}{8} \, \text{S}_{2}^{z} \, \text{S}_{3}^{z} - \frac{1}{8} \, \text{S}_{2}^{z} \, \text{S}_{3}^{x} - \frac{1}{8} \, \text{S}_{2}^{z} \, \text{S}_{3}^{x} + \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{2}^{z} \, \text{S}_{3}^{z} + \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{2}^{z} \, \text{S}_{3}^{x} + \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{3}^{z} \, \text{S}_{4}^{x} + \frac{1}{8} \, \text{S}_{2}^{z} \, \text{S}_{3}^{z} \, \text{S}_{4}^{x} - \frac{1}{8} \, \text{S}_{1}^{z} \, \text{S}_{2}^{z} \, \text{S}_{3}^{z} \, \text{S}_{4}^{x} + \frac{S_{1}^{z}}{8} + \frac{S_{2}^{z}}{8} + \frac{S_{3}^{z}}{8} + \frac{S_{4}^{z}}{8} \end{aligned}$$

Here we compare it with the expression explicitly constructed in terms of a projection operator.

$$\begin{split} & \mathit{In}(\cdot) = \text{prj} = \text{Multiply} @@ S[\{1,\,2,\,3\},\,11]; \\ & \mathsf{new} = (1 - \mathsf{prj}) + \mathsf{prj} ** S[4,\,1] \\ & \mathsf{op} - \mathsf{new} \, / \, \mathsf{Elaborate} \\ & \mathit{Out}(\cdot) = 1 - \frac{1}{8} \, S_1^z \, S_2^z - \frac{1}{8} \, S_1^z \, S_3^z - \frac{1}{8} \, S_1^z \, S_4^x - \frac{1}{8} \, S_2^z \, S_3^z - \frac{1}{8} \, S_2^z \, S_4^x - \frac{1}{8} \, S_3^z \, S_4^x + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^x + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^x + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^x + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_1^z \, S_2^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_1^z \, S_1^z \, S_1^z \, S_2^z \, S_3^z \, S_3^z \, S_4^z + \frac{1}{8} \, S_1^z \, S_1$$

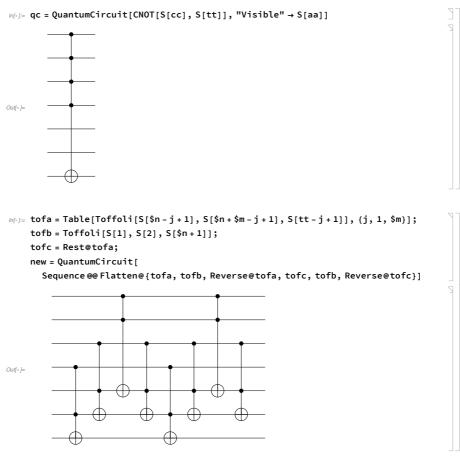
An efficient implementation of a multi-control NOT gate uses additional qubits not directly involved in the gate operation itself as in the following quantum circuit (Barenco *et al.*, 1995):



It is emphasized that the quantum states of the extra qubits should not be confused with the so-called "ancillary qubits" in the sense that they do not have to be initialized in a certain fixed quantum state and their state is not altered. The desired gate operation is performed properly regardless of the initial state of the extra qubits and their quantum state is restored after the gate operation.

```
Here we demonstrate a multi-control-NOT gate.
```

```
$n = 4;
$m = 2;
cc = Range[$n];
aa = Range[$n+1, $n+$m];
tt = $n+$m+1;
```



 \(\rho_{i} \) := Timing[Elaborate[qc - new]]
 \(\rho_{i} \) := \(\frac{11.6545, 0}{} \) \(\rightarrow \)

In the quantum circuit shown in (2.83), we have introduced the *Toffoli gate*, another special case of multi-control NOT gates denoted by the quantum circuit element



The Toffoli gate has attracted interest because it is universal for classical reversible computation. Unfortunately, however, it is not universal for quantum computation.

$$\textit{Out} (s) = \frac{3}{4} - \frac{1}{4} \, S_1^z \, S_2^z - \frac{1}{4} \, S_1^z \, S_3^x - \frac{1}{4} \, S_2^z \, S_3^x + \frac{1}{4} \, S_1^z \, S_2^z \, S_3^x + \frac{S_1^z}{4} + \frac{S_2^z}{4} + \frac{S_3^x}{4} \, S_3^z \, S_3^x + \frac{S_2^z}{4} + \frac{S_3^z}{4} \, S_3^z \, S_3^z + \frac{S_3^z$$

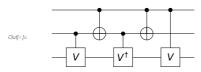
It can be implemented by a combination of two-qubit gates.

Out[=]= 0

Here $V = \sqrt{X}$, and its matrix representation looks like this.

Outfole $\left(\frac{1}{2} + \frac{\dot{\mathbb{I}}}{2}\right) + \left(\frac{1}{2} - \frac{\dot{\mathbb{I}}}{2}\right) S_3^x$

Reversing the above circuit gives the identical result.

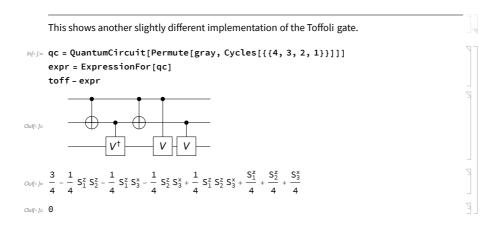


$$\textit{Out}[*] = \frac{3}{4} - \frac{1}{4} \, \, S_{1}^{z} \, \, S_{2}^{z} - \frac{1}{4} \, \, S_{1}^{z} \, \, S_{3}^{x} - \frac{1}{4} \, \, S_{2}^{z} \, \, S_{3}^{x} + \frac{1}{4} \, \, S_{2}^{z} \, \, S_{3}^{x} + \frac{S_{1}^{z}}{4} + \frac{S_{2}^{z}}{4} + \frac{S_{2}^{x}}{4} + \frac{S_{3}^{x}}{4} + \frac{S_{3}^{x}}{4}$$

Out[=]= 0

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As noted by Smolin & DiVincenzo (1996), it can also be reordered as follows, and this rearrangement is useful in optimizing the *Fredkin gate*, another universal gate for classical reversible computation.



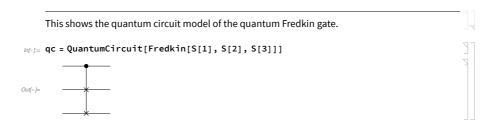
The Fredkin gate "swaps" the states of the two target qubits when the control qubit is set in the state $|1\rangle$, as depicted by the following two equivalent quantum circuit elements

$$\equiv \qquad (2.85)$$

In fact, the relation between the SWAP gate and the CNOT gate suggests that there is another equivalent quantum circuit of the Fredkin gate,

$$= \frac{1}{2}$$

which is simpler than the one given above. This equivalent quantum circuit was used by Smolin & DiVincenzo (1996) for an efficient implementation of the Fredkin gate in terms of the elementary gates: Like the Toffoli gate, the Fredkin gate is not universal for quantum computation.



This is an explicit operator expression of the Fredkin gate in terms of the Pauli operators.

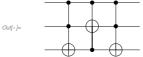
$$lo[*] = \text{ op = Elaborate[qc]}$$

$$Out[*] = \frac{3}{4} + \frac{1}{4} S_2^x S_3^x + \frac{1}{4} S_2^y S_3^y + \frac{1}{4} S_2^z S_3^z - \frac{1}{4} S_1^z S_2^x S_3^x - \frac{1}{4} S_1^z S_2^y S_3^y - \frac{1}{4} S_1^z S_2^z S_3^z + \frac{S_1^z}{4}$$

This is the matrix representation of the Fredkin gate in the logical basis.

The Fredkin gate is equivalent to a combination of three Toffoli gates.

Infalse qc2 = QuantumCircuit[
 Toffoli[S[1], S[2], S[3]],
 Toffoli[S[1], S[3], S[2]],
 Toffoli[S[1], S[2], S[3]]]



$$\begin{split} & \textit{In}[\cdot] = \text{ op2 = Elaborate[qc2]} \\ & \textit{Out}[\cdot] = \frac{3}{4} + \frac{1}{4} \, S_2^x \, S_3^x + \frac{1}{4} \, S_2^y \, S_3^y + \frac{1}{4} \, S_2^z \, S_3^z - \frac{1}{4} \, S_1^z \, S_2^x \, S_3^x - \frac{1}{4} \, S_1^z \, S_2^y \, S_3^y - \frac{1}{4} \, S_1^z \, S_2^z \, S_3^z + \frac{S_1^z}{4} \, S_2^z \, S_3^z + \frac{S_1^z}{4} \, S_2^z \, S_3^z + \frac{S_2^z}{4} \, S_3^z \, S_3^z$$

In fact, the relation between the SWAP gate and the CNOT gate suggests that there is another equivalent quantum circuit model of the Fredkin gate.

```
In[•]:= qc - new // Elaborate
Out[•]:= 0
```

2.4 Universal Quantum Computation

In classical computation, it is known that a finite set of logic gates—typically including AND, OR, and NOT—is sufficient to calculate any binary function. The set is said to be *universal* for classical computation. One can ask whether there exists a universal set of elementary quantum logic gates for quantum computation that enables to implement any arbitrary quantum unitary operation? In this section, we examine this question.

Consider a system of n qubits. Suppose that we want to implement an arbitrary unitary operation \hat{U} on the n-qubit register. We start by decomposing \hat{U} into a set of two-level unitary transformations, which we discussed for the case of two-qubit systems in Section 2.2.3.

```
Consider a three-qubit system, and an arbitrary unitary operation on it.
  ln[-]:= n = 3;
      SS = S[Range[$n], None];
      mat = RandomUnitary[2^$n];
      mat[;; 3, ;; 3] // MatrixForm
        -0.194963 + 0.165843 i 0.0547873 - 0.197219 i 0.136692 + 0.369997 i
        0.0608429 - 0.0637433 i 0.0290529 + 0.643139 i 0.435025 - 0.0328515 i
       0.0235601 - 0.274771 i 0.012495 - 0.199766 i 0.203625 + 0.0609559 i
In[*]:= op = ExpressionFor[mat, SS];
    qc = QuantumCircuit[{op, "Label" → "U"}]
  In[*]:= twl = TwoLevelDecomposition[mat];
      twl[3] // Matrix // MatrixForm
        1 0 0 0 0
                                                               0
        0 1 0 0 0
                             Θ
                                                               0
        0 0 1 0 0
                                                               0
        0 0 0 1 0
                             0
          0 0 0 1
                            0
        0 0 0 0 0 0.151664 - 0.402111 i
                                            0.902942
                     -0.902942 0.151664 + 0.402111 i
        0 0 0 0 0
                                                              0
        0 0 0 0 0
```

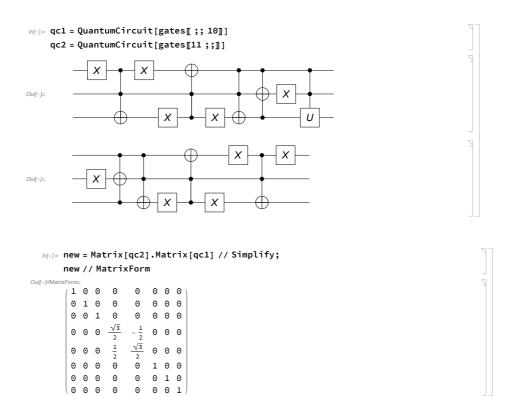
Now a remaining question is to implement the two-level unitary transformation in terms of elementary gates.

Let us consider a particular two-level unitary matrix on a three-qubit system. We want to implement it in terms of elementary quantum logic gates.

$$\begin{aligned} & \textit{Int-} := \text{ op = ExpressionFor [mat, SS]} \\ & \textit{Out(*)} := \frac{1}{8} \times \left(6 + \sqrt{3}\right) + \frac{1}{8} \times \left(2 - \sqrt{3}\right) S_1^z S_2^z + \\ & \frac{1}{8} \times \left(2 - \sqrt{3}\right) S_1^z S_3^z + \frac{1}{8} \times \left(-2 + \sqrt{3}\right) S_2^z S_3^z - \frac{1}{2} S_1^+ S_2^- S_3^- + \frac{1}{2} S_1^- S_2^+ S_3^+ \end{aligned}$$

Our implementation is based on the Gray code sequence. Notice the function Reverse.

$$\begin{aligned} &\textit{Imp} = \text{ gates = Flatten@GrayTwoLevelU[U, \{x,y\}, SS]} \\ &\textit{Out} \in \left\{S_1^x, \, \text{CNOT}[\{S_1, S_2\}, \, \{S_3\}], \, S_1^x, \, S_3^x, \, \text{CNOT}[\{S_2, S_3\}, \, \{S_1\}], \right. \\ &S_3^x, \, \text{CNOT}[\{S_1, S_2\}, \, \{S_3\}], \, \text{CNOT}[\{S_1, S_3\}, \, \{S_2\}], \, S_2^x, \\ &\text{ControlledU}\Big[\{S_1, S_2\}, \, \frac{\sqrt{3}}{2} + \frac{\mathrm{i}}{2} \, \mathrm{supp} \, \mathrm{sup$$



So far, we have seen that one can implement any unitary operation combining the CNOT and single-qubit gates. A subtle issue is that single-qubit gates form a continuum of unitary operators. It is impractical to implement them to an infinite accuracy. Fortunately, it is known that a discrete set of gates is sufficient to perform universal quantum computation. The Hadamard, quadrant phase, octant phase, and CNOT gates form a universal set of gates in the sense that an arbitrary gate on n qubits can be approximated to arbitrary accuracy using a quantum circuit composed of only such gates. Furthermore, the set remains universal if octant phase gates are replaced with Toffoli gates.

2.5 Measurements

We conclude this chapter with a few discussions on measurement. In quantum computers, measurement is assumed to be performed independently on individual qubits in the logical basis, $\{|x\rangle : x = 0, 1, \dots, 2^n - 1\}$.

What if a measurement in another basis, say, $\{|\alpha_x\rangle = \hat{U}|x\rangle\}$ is required? We require that the input state $|\alpha_x\rangle$ should end up with the logical basis state $|x\rangle$ with unit probability so that the measurement yields outcome x. This process is described by the measurement operators $\hat{M}_x := |x\rangle \langle \alpha_x| = |x\rangle \langle x| \hat{U}^{\dagger}$. Evidently they satisfy the condition $\sum_x \hat{M}_x^{\dagger} \hat{M}_x = \hat{I}$ for measurement operators (see Postulate 1.3). Now we note that the operators $\hat{P}_x := |x\rangle \langle x|$ describe nothing but the

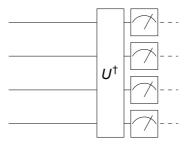
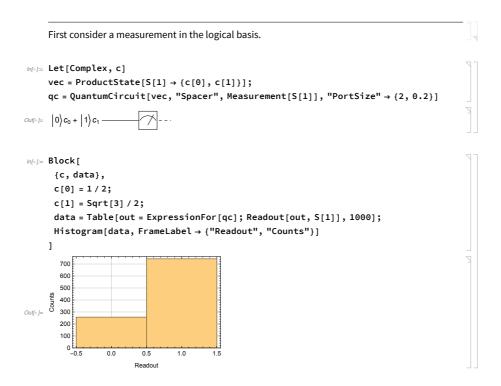


Figure 2.6: Measurement in a basis $\{|\alpha_x\rangle\}$ other than the logical basis. The unitary operator \hat{U} here corresponds to the basis change $|\alpha_y\rangle = \hat{U}|y\rangle = \sum_x |x\rangle \langle x|\alpha_y\rangle$.

measurement in the logical basis. This implies that by simply applying the inverse unitary operation \hat{U}^{\dagger} before the measurement, the measurement in the new basis $\{|\alpha_x\rangle\}$ can be achieved through measurement in the logical basis (see Fig. 2.6). For example, suppose that a qubit is in the state $|\psi\rangle = |0\rangle c_0 + |1\rangle c_1$. By default, a measurement is assumed to be in the logical basis and the measurement statistics reflect the probability distribution $P_0 = |c_0|^2$ and $P_1 = |c_1|^2$ as illustrated in the demonstration below.

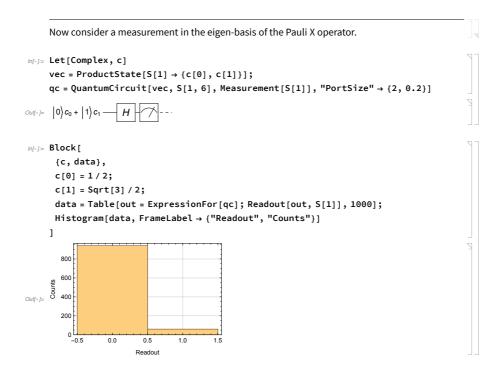


We now want to make a measurement on the eigenbasis $\{|\pm\rangle\}$ of the Pauli X operator. In this case, it is the Hadamard gate that gives the desired basis change.

In the new basis, the state vector $|\psi\rangle$ is given by

$$|\psi\rangle = |+\rangle \frac{c_0 + c_1}{\sqrt{2}} + |-\rangle \frac{c_0 - c_1}{\sqrt{2}},$$
 (2.87)

Now the measurement statistics are in accordance with the probability distribution $P_{\pm} = |c_0 \pm c_1|^2/2$.



Another interesting example is the *Bell measurement*. The Bell measurement is a measurement on two qubits in the basis of Bell states,

$$|\beta_0\rangle = \frac{|00\rangle + |11\rangle}{\sqrt{2}}$$

$$|\beta_1\rangle = \frac{|01\rangle + |10\rangle}{\sqrt{2}}$$

$$|\beta_2\rangle = \frac{|01\rangle - |10\rangle}{\sqrt{2}}$$

$$|\beta_3\rangle = \frac{|00\rangle - |11\rangle}{\sqrt{2}}$$
(2.88)

Recall that the Bell states can be generated from the logical basis states by the so-called quantum entangler circuit, a combination of the Hadamard gate and the CNOT gate discussed in Section 2.2.1. Therefore, the Bell measurement can be achieved by applying the inverse of the entangling operation.

This is the "disentangler" quantum circuit, which is the inverse of the entangler quantum circuit $\frac{1}{2} \left(\frac{1}{2} \right) = \frac{1}{2} \left(\frac{1}{2} \right) \left(\frac{1}{2}$

Inf=j= disentangler = QuantumCircuit[CNOT[S[1], S[2]], S[1, 6]]

This shows that the disentangler quantum circuit maps the Bell states into the logical basis states.

$$\begin{split} & \text{In}_{[\cdot]:=} \text{ op = ExpressionFor[disentangler];} \\ & \text{bs = BellState@S@}\{1,2\}; \\ & \text{Thread[bs } \rightarrow \text{ op ** bs] // LogicalForm // TableForm} \\ & \text{CMI}_{[\cdot]:/TableForm:} \\ & \frac{\left|\theta_{S_1}\theta_{S_2}\right\rangle + \left|1_{S_1}1_{S_2}\right\rangle}{\sqrt{2}} \rightarrow \left|\theta_{S_1}\theta_{S_2}\right\rangle \\ & \frac{\left|\theta_{S_1}1_{S_2}\right\rangle + \left|1_{S_1}\theta_{S_2}\right\rangle}{\sqrt{2}} \rightarrow \left|\theta_{S_1}1_{S_2}\right\rangle \\ & \frac{\left|\theta_{S_1}1_{S_2}\right\rangle + \left|1_{S_1}\theta_{S_2}\right\rangle}{\sqrt{2}} \rightarrow \left|1_{S_1}1_{S_2}\right\rangle \\ & \frac{\left|\theta_{S_1}\theta_{S_2}\right\rangle - \left|1_{S_1}1_{S_2}\right\rangle}{\sqrt{2}} \rightarrow \left|1_{S_1}\theta_{S_2}\right\rangle \\ & \frac{\left|\theta_{S_1}\theta_{S_2}\right\rangle - \left|1_{S_1}1_{S_2}\right\rangle}{\sqrt{2}} \rightarrow \left|1_{S_1}\theta_{S_2}\right\rangle \end{split}$$

More sophisticated measurements may be necessary for fast quantum algorithms and quantum error corrections, A common example is the quantum phase estimation, which is one of the core parts of Shor's factorization algorithm. We will discuss it in Section 4.4.

Problems

2.1. Let $\hat{\Phi}(\phi)$ be the *phase gate*, which gives rise to a *relative* phase shift by $\phi \in [0, 2\pi)$,

$$\hat{\Phi}(\phi): |0\rangle \mapsto |0\rangle , \quad |1\rangle \mapsto |1\rangle e^{i\phi} .$$
 (2.89)

(a) Show that on a *n*-qubit quantum register,

$$[\hat{\Phi}(\phi)]^{\otimes n} |x\rangle = |x\rangle e^{i\phi(x_1 + \dots + x_n)}$$
(2.90)

for any $x = 0, 1, \dots, 2^n - 1$.

(b) Explicitly evaluate the state $[\hat{\Phi}(\phi)]^{\otimes n}\hat{H}^{\otimes n}|0\rangle$, where \hat{H} is the Hadamard gate.

2.2. Let $\hat{U}_x(\phi)$ be the rotation around the x-axis by angle ϕ on a single qubit. Explicitly analyze and evaluate the following quantum circuit

$$|x_{1}\rangle - H - U_{x} - H - |x_{2}\rangle - H - U_{x} - H - |x_{3}\rangle - H - |U_{x}| - H - |x_{3}\rangle - |x_{$$

where $|x\rangle := |x_1\rangle \otimes |x_2\rangle \otimes |x_3\rangle$ is a 3-qubit logical basis state and the labels "H" and " U_x " indicate the Hadamard gate \hat{H} and the rotation gate $\hat{U}_x(\phi)$, respectively. Generalize the result and show that

$$\hat{H}^{\otimes n}[\hat{U}_x(\phi)]^{\otimes n}\hat{H}^{\otimes n}|x\rangle = e^{-i\phi n/2}|x\rangle e^{i\phi(x_1 + \dots + x_n)}$$
(2.92)

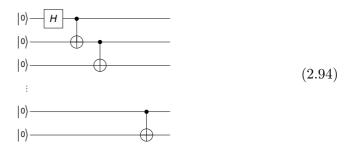
for any $x = 0, 1, \dots, 2^n - 1$.

2.3. Let \hat{S}^{μ} be the Pauli operators. Show that

$$e^{i\hat{S}^{\mu}\phi/2}\hat{S}^{\nu}e^{-i\hat{S}^{\mu}\phi/2} = \hat{S}^{\nu}\cos(\phi) + \hat{S}^{\lambda}\epsilon_{\lambda\mu\nu}\sin(\phi)$$
 (2.93)

for all $\mu, \nu = x, y, z$ and $\mu \neq \nu$.

- 2.4. The lever on Bob's quantum computer is stuck in the "forward" position, so it can only perform CNOT gates controlled by qubit 1 on qubit 2 (target). His computer can still perform single-qubit operations normally. How can he perform a CNOT gate controlled by qubit 2 on qubit 1?^{2.3}
- 2.5. Consider the following quantum circuit with n qubits:

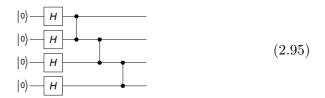


Show that it generates the Greenberger-Horne-Zeilinger state in Eq. (2.40), identical to the one from the quantum circuit in Fig. 2.5 (b).

2.6. Consider a quantum register of four qubits.

^{2.3}This problem was published in Gottesman (1999).

(a) Analyze the following quantum circuit



and evaluate the resulting state $|\Psi\rangle$ explicitly. The state is a so-called cluster state or graph state, a crucial resource in the measurement-based quantum computation (see Section 3.4).

- (b) Show that in the state $|\Psi\rangle$ from (b), every qubit is maximally entangled with the rest of the qubits. That is, the reduced density matrix $\hat{\rho}_j$ of the jth qubit, $\hat{\rho}_j := \operatorname{Tr}_{k\neq j} |\Psi\rangle \langle \Psi|$, is given by $\hat{\rho}_j = \hat{I}/2$, exhibiting coherence in no basis.
- 2.7. Suppose that a qubit is known to be in one of the two eigenstates of the unitary operator

$$\hat{U} = \hat{\sigma}^0 \cos(\phi/2) - i\hat{\sigma}^x \sin(\phi/2) \tag{2.96}$$

with the angle ϕ known. Construct a quantum circuit to figure out the unknown state using an additional qubit.

Hint: Use a controlled-unitary gate to acquire the one-bit information. This is a simplified version of the quantum phase estimation procedure (see Section 4.4), but it can be worked out without resorting to it.

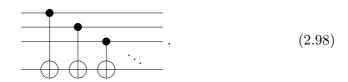
2.8. Suppose that a two-qubit system is known to be in one of the four eigenstates of the unitary operator

$$\hat{U} = e^{i\phi} (|0\rangle \langle 0| + i |1\rangle \langle 1| - |2\rangle \langle 2| - i |3\rangle \langle 3|). \tag{2.97}$$

Construct a quantum circuit to figure out the unknown state using two additional qubits.

Hint: Use the property (2.18) of the Hadamard gate and those of the controlled-unitary gates, where a unitary operator acts on a two-qubit system controlled by a single qubit.

2.9. Consider the following quantum circuit consisting of the CNOT gates on an n-qubit quantum register



- (a) Find the output state for the input of a logical basis state $|x_1\rangle \otimes \cdots \otimes |x_n\rangle \in \mathcal{S}^{\otimes n}$.
- (b) Find the output state for the input state $|+\rangle \otimes \cdots \otimes |+\rangle \otimes |-\rangle$, where $|\pm\rangle := (|0\rangle \pm |1\rangle)/\sqrt{2}$.
- 2.10. Let \hat{U} be a unitary operator on a single qubit. Show that the following three statements are equivalent:
 - (a) There exist unitary gates \hat{A} and \hat{B} such that

$$\begin{array}{c} \bullet \\ \hline \\ U \end{array} = \begin{array}{c} \bullet \\ \hline \\ A \end{array} \begin{array}{c} \bullet \\ B \end{array} \end{array}$$
 (2.99)

- (b) There exists a unitary operator \hat{W} such that $\hat{U} = \hat{W}\hat{Z}\hat{W}^{\dagger}$.
- (c) Tr $\hat{U} = 0$ and det $\hat{U} = -1$.
- 2.11. Let $P(i \leftrightarrow j)$ be the matrix exchanging the *i*th and *j*th rows (columns) of vectors/matrices. For example, on a four-dimensional space,

$$P(1 \leftrightarrow 2) = \begin{bmatrix} 0 & 1 & & \\ 1 & 0 & & \\ & & 1 & \\ & & & 1 \end{bmatrix} . \tag{2.100}$$

 $\hat{P}(i \leftrightarrow j)$ is the corresponding operator.

- (a) Find a quantum circuit for $\hat{P}(2 \leftrightarrow 3)$ on a two-qubit system (four-dimensional vector space $\mathcal{S} \otimes \mathcal{S}$) using CNOT gates only.
- (b) Find a quantum circuit for $\hat{P}(2 \leftrightarrow 3)$ on a three-qubit system $(\mathcal{S}^{\otimes 3})$ using multi-control NOT gates only.
- (c) Find a quantum circuit for $\hat{P}(4 \leftrightarrow 7)$ on a three-qubit system $(\mathcal{S}^{\otimes 3})$ using multi-control NOT gates only.
- 2.12. Let $\hat{U}=e^{i\phi}\hat{I}$ be a single-qubit unitary operator that globally shifts the phase by ϕ . Also define

$$\hat{T} = |0\rangle\langle 0| + e^{i\phi} |1\rangle\langle 1| \doteq \begin{bmatrix} 1 & 0\\ 0 & e^{i\phi} \end{bmatrix}, \qquad (2.101)$$

which shifts the *relative* phase by ϕ .

(a) Show that

where the labels "U" and "T" denote the unitary operators \hat{U} and \hat{T} , respectively.

(b) Show also that

$$= \begin{array}{c} T \\ \hline \end{array} = \begin{array}{c} T \\ \hline \end{array} = \begin{array}{c} T \\ \hline \end{array} = \begin{array}{c} T \\ \hline \end{array} . \tag{2.103}$$