Microcontroller pin usage on EVK1100

PA00	_		E		E 4: C	EX/1/1100	
PA01	Pin			Function B	Function C	EVK1100	Ī
PA02 2 USART0 - CLK						_	
PA03 3 USART0 - RTS							
PA04							MMC]
PA05 5 USART1 - RXD	PA03	3	USART0 - RTS	EIM - EXTINT[4]	DAC - DATA[0]	USART_0 - RTS	
PA06 6 USART1 - TXD PWM - PWM[5] USART_1 - TXD PA07 7 USART_1 - CLK PM - GCLK[0] SPI0 - NPCS[3] SPI1 - SPI_WRITEPROTECT[SD/MMC] PA08 8 USART_1 - RTS SPI0 - NPCS[1] EIM - EXTINT[7] USART_1 - RTS PA09 9 USART_1 - CTS SPI0 - NPCS[2] MACB - WOL USART_1 - CTS PA10 10 SPI0 - NPCS[0] EIM - EXTINT[6] SPI - SPI0_CS0 PA11 11 SPI0 - MISO USB - USB_ID USB - USB_ID SPI - SPI0_MISO USB - USB_ID USB - USB_ID SPI - SPI0_MISO PA13 13 SPI0 - SCK SPI - SPI0_MOSI SPI - SPI0_MOSI SPI - SPI0_MOSI SPI - SPI0_MOSI PA13 13 SPI0 - SCK SPI - NPCS[0] EBI - NCS[0] SPI1 - CS0[DATAFLASH] PA15 15 SSC - TX_CLOCK SPI1 - SCK EBI - ADDR[20] SPI1 - SCK PA16 16 SSC - TX_DATA SPI1 - MISO EBI - ADDR[21] SPI1 - MISO PA17 17 SSC - RX_DATA SPI1 - MISO EBI - ADDR[21] SPI1 - MISO PA18 18 SSC - RX_CLOCK SPI1 - NPCS[1] MACB - WOL SPI1 - CS1[SD/MMC] PA20 20 EIM - EXTINT[8] SPI1 - NPCS[3] SPI1 - SEIL -	PA04			EIM - EXTINT[5]	DAC - DATAN[0]		
PA07 7 USART1 - CLK PM - GCLK[0] SPI0 - NPCS[3] SPI1 - SPI_WRITEPROTECT[SD/MMC]		5	USART1 - RXD	PWM - PWM[4]		USART_1 - RXD	
PA08 8 USART1 - RTS SPI0 - NPCS[1] EIM - EXTINT[7] USART_1 - RTS PA09 9 USART1 - CTS SPI0 - NPCS[2] MACB - WOL USART_1 - CTS PA10 10 SPI0 - NPCS[0] EIM - EXTINT[6] SPI - SPI0_SCO PA11 11 SPI0 - MISO USB - USB_ID USB - USB_ID USB - USB_ID SPI - SPI0_MOSI PA12 12 SPI0 - MOSI USB - USB_VBOF SPI - SPI0_MOSI PA13 13 SPI0 - SCK SPI - SPI0_SCK SPI - SPI0_SCK PA14 14 SSC - TX_FRAME_SYNC SPI1 - NPCS[0] EBI - NCS[0] SPI1 - CS0[DATAFLASH] PA15 15 SSC - TX_CLOCK SPI1 - SCK EBI - ADDR[20] SPI1 - MOSI PA16 16 SSC - TX_DATA SPI1 - MISO EBI - ADDR[21] SPI1 - MOSI PA17 17 SSC - RX_DATA SPI1 - MISO EBI - ADDR[22] SPI1 - MISO PA18 18 SSC - RX_CLOCK SPI1 - NPCS[1] MACB - WOL SPI1 - CS1[RD/MMC] PA19 19 SSC - RX_FRAME_SYNC SPI1 - NPCS[2] SPI1 - SPI0_SIMC PA20 20 EIM - EXTINT[8] SPI1 - NPCS[3] JOYSTICK - EXTINT PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_ID SENSORS - POT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATA[1] SENSORS - LIGHT PA25 25 ADC - AD[4] EIM - EXTINT[3] DAC - DATA[1] SENSORS - LIGHT PA26 26 ADC - AD[5] EIM - SCAN[0] EBI - NDS[0] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[1] EBI - ADDR[21] JOYSTICK - D PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SCL PB00 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB01 34 MACB - TXD[0] DAC - DATA[0] ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX_EN PB03 34 MACB - TXD[0] DAC - DATA[0] ETH - TX_EN PB04 34 MACB - TXD[0] DAC - DATA[0] ETH - TX_EN PB05 SAME - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB06 SAME - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB07 SAME - TX_EN USAR	PA06	6	USART1 - TXD	PWM - PWM[5]			
PA09	PA07	7	USART1 - CLK	PM - GCLK[0]	SPI0 - NPCS[3]	SPI1 - SPI_WRITEPROTECT[S	D/MMC]
PA10	PA08	8	USART1 - RTS	SPI0 - NPCS[1]	EIM - EXTINT[7]	USART_1 - RTS	
PA11	PA09	9	USART1 - CTS	SPI0 - NPCS[2]	MACB - WOL	USART_1 - CTS	
PA12 12 SPI0 - MOSI USB - USB_VBOF SPI - SPI0_MOSI PA13 13 SPI0 - SCK SPI - NCS[0] EBI - NCS[0] SPII - CS0[DATAFLASH] PA14 14 SSC - TX_FRAME_SYNC SPII - NPCS[0] EBI - NCS[0] SPII - SCK PA15 15 SSC - TX_CLOCK SPII - SCK EBI - ADDR[20] SPII - SCK PA16 16 SSC - TX_DATA SPII - MOSI EBI - ADDR[21] SPII - MOSI PA17 17 SSC - RX_DATA SPII - MISO EBI - ADDR[21] SPII - MISO PA18 18 SSC - RX_CLOCK SPII - NPCS[1] MACB - WOL SPII - CS1[SD/MMC] PA19 19 SSC - RX_FRAME_SYNC SPII - NPCS[2] SPII - CS2[LCD] PA20 20 EIM - EXTINT[8] SPII - NPCS[2] SPII - SC2[LCD] PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATA[1] SENSORS - LIGHT PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[21] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - C PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SDA PA30 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB00 34 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB00 34 MACB - TX_D[0] DAC - DATA[0] ETH - TX_O	PA10	10	SPI0 - NPCS[0]	EIM - EXTINT[6]		SPI - SPIO_CS0	
PA13 13 SPI0 - SCK SPI - SPI0_SCK SPI1 - NPCS[0] EBI - NCS[0] SPI1 - CS0[DATAFLASH] PA14 14 SSC - TX_FRAME_SYNC SPI1 - NPCS[0] EBI - NCS[0] SPI1 - CS0[DATAFLASH] PA15 15 SSC - TX_CLOCK SPI1 - SCK EBI - ADDR[20] SPI1 - SCK PA16 16 SSC - TX_DATA SPI1 - MOSI EBI - ADDR[21] SPI1 - MOSI PA17 17 SSC - RX_DATA SPI1 - MISO EBI - ADDR[21] SPI1 - MISO PA18 18 SSC - RX_CLOCK SPI1 - NPCS[1] MACB - WOL SPI1 - CS2[SD/MMC] PA19 19 SSC - RX_FRAME_SYNC SPI1 - NPCS[2] SPI1 - CS2[LCD] PA20 20 EIM - EXTINT[8] SPI1 - NPCS[3] JOYSTICK - EXTINT PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATA[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[1] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[21] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - RTS TWI - SDA PB00 32 MACB - TX_CLK USART2 - CTS USART3 - CTS ETH - REF_CLK PB01 33 MACB - TX_D[0] DAC - DATA[0] ETH - TX_0	PA11	11	SPIO - MISO	USB - USB_ID		USB - USB_ID	SPI - SPI0_MISO
PA14	PA12	12	SPI0 - MOSI	USB - USB_VBOF		SPI - SPI0_MOSI	
PA15	PA13	13	SPI0 - SCK			SPI - SPI0_SCK	
PA16	PA14	14	SSC - TX_FRAME_SYNC	SPI1 - NPCS[0]	EBI - NCS[0]	SPI1 - CS0[DATAFLASH]	
PA17	PA15	15	SSC - TX_CLOCK	SPI1 - SCK	EBI - ADDR[20]	SPI1 - SCK	
PA18 18 SSC - RX_CLOCK SPII - NPCS[1] MACB - WOL SPII - CS1[SD/MMC] PA19 19 SSC - RX_FRAME_SYNC SPII - NPCS[2] SPII - CS2[LCD] PA20 20 EIM - EXTINT[8] SPII - NPCS[3] JOYSTICK - EXTINT PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] SENSORS - LIGHT PA25 25 ADC - AD[4] EIM - EXTINT[3] DAC - DATAN[1] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA30 30	PA16	16	SSC - TX_DATA	SPI1 - MOSI	EBI - ADDR[21]	SPI1 - MOSI	
PA19 19 SSC - RX_FRAME_SYNC SPI1 - NPCS[2] SPI1 - CS2[LCD] PA20 20 EIM - EXTINT[8] SPI1 - NPCS[3] JOYSTICK - EXTINT PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA17	17	SSC - RX_DATA	SPI1 - MISO	EBI - ADDR[22]	SPI1 - MISO	
PA20 20 EIM - EXTINT[8] SPII - NPCS[3] JOYSTICK - EXTINT PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA18	18	SSC - RX_CLOCK	SPI1 - NPCS[1]	MACB - WOL	SPI1 - CS1[SD/MMC]	
PA21 21 ADC - AD[0] EIM - EXTINT[0] USB - USB_ID SENSORS - TEMP PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - CTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN	PA19	19	SSC - RX_FRAME_SYNC	SPI1 - NPCS[2]		SPI1 - CS2[LCD]	
PA22 22 ADC - AD[1] EIM - EXTINT[1] USB - USB_VBOF SENSORS - POT PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA20	20	EIM - EXTINT[8]	SPI1 - NPCS[3]		JOYSTICK - EXTINT	
PA23 23 ADC - AD[2] EIM - EXTINT[2] DAC - DATA[1] SENSORS - LIGHT PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA21	21	ADC - AD[0]	EIM - EXTINT[0]	USB - USB_ID	SENSORS - TEMP	
PA24 24 ADC - AD[3] EIM - EXTINT[3] DAC - DATAN[1] PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA22	22	ADC - AD[1]	EIM - EXTINT[1]	USB - USB_VBOF	SENSORS - POT	
PA25 25 ADC - AD[4] EIM - SCAN[0] EBI - NCS[0] JOYSTICK - A PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA23	23	ADC - AD[2]	EIM - EXTINT[2]	DAC - DATA[1]	SENSORS - LIGHT	
PA26 26 ADC - AD[5] EIM - SCAN[1] EBI - ADDR[20] JOYSTICK - B PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA24	24	ADC - AD[3]	EIM - EXTINT[3]	DAC - DATAN[1]		
PA27 27 ADC - AD[6] EIM - SCAN[2] EBI - ADDR[21] JOYSTICK - C PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TXO	PA25	25	ADC - AD[4]	EIM - SCAN[0]	EBI - NCS[0]	JOYSTICK - A	
PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA26	26	ADC - AD[5]	EIM - SCAN[1]	EBI - ADDR[20]	JOYSTICK - B	
PA28 28 ADC - AD[7] EIM - SCAN[3] EBI - ADDR[22] JOYSTICK - D PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA27	27	ADC - AD[6]	EIM - SCAN[2]	EBI - ADDR[21]	JOYSTICK - C	
PA29 29 TWI - SDA USART2 - RTS TWI - SDA PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TXO	PA28	28	ADC - AD[7]	EIM - SCAN[3]	EBI - ADDR[22]	JOYSTICK - D	
PA30 30 TWI - SCL USART2 - CTS TWI - SCL PB00 32 MACB - TX_CLK USART2 - RTS USART3 - RTS ETH - REF_CLK PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA29	29	TWI - SDA	USART2 - RTS		TWI - SDA	
PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PA30	1				TWI - SCL	
PB01 33 MACB - TX_EN USART2 - CTS USART3 - CTS ETH - TX_EN PB02 34 MACB - TXD[0] DAC - DATA[0] ETH - TX0	PB00	32	MACB - TX_CLK	USART2 - RTS	USART3 - RTS	ETH - REF_CLK	
	PB01			USART2 - CTS	USART3 - CTS	ETH - TX_EN	
PB03 35 MACB - TXD[1] DAC - DATAN[0] ETH - TX1	PB02	34	MACB - TXD[0]	DAC - DATA[0]		ETH - TX0	
	PB03	35	MACB - TXD[1]	DAC - DATAN[0]		ETH - TX1	

PB04	36	MACB - CRS	USART3 - CLK	EBI - NCS[3]	NEXUS - MSEO1	
PB05	37	MACB - RXD[0]	DAC - DATA[1]		ETH - RX0	
PB06	38	MACB - RXD[1]	DAC - DATAN[1]		ETH - RX1	
PB07	39	MACB - RX_ER			ETH - RX_ER	
PB08	40	MACB - MDC			ETH - MDC	
PB09	41	MACB - MDIO			ETH - MDIO	
PB10	42	MACB - TXD[2]	USART3 - RXD	EBI - SDCK	SDRAM - SDCK	NEXUS - MD00
PB11	43	MACB - TXD[3]	USART3 - TXD	EBI - SDCKE	SDRAM - SDCKE	NEXUS - MD01
PB12	44	MACB - TX_ER	TC - CLK0	EBI - RAS	SDRAM - RASn	NEXUS - MD02
PB13	45	MACB - RXD[2]	TC - CLK1	EBI - CAS	SDRAM - CASn	NEXUS - MD03
PB14	46	MACB - RXD[3]	TC - CLK2	EBI - SDWE	SDRAM - SDWEn	NEXUS - MD04
PB15	47	MACB - RX_DV			ETH - RX_DV	
PB16	48	MACB - COL	USB - USB_ID	EBI - SDA10	SDRAM - SDA10	NEXUS - MD05
PB17	49	MACB - RX_CLK	USB - USB_VBOF	EBI - ADDR[23]	Host VBUS /EN	NEXUS - MSEO0
PB18	50	MACB - SPEED	ADC - TRIGGER	PWM - PWM[6]	LCD - C	
PB19	51	PWM - PWM[0]	PM - GCLK[0]	EIM - SCAN[4]	LED - LED4	NEXUS - EVTIn
PB20		PWM - PWM[1]	PM - GCLK[1]	EIM - SCAN[5]	LED - LED5	NEXUS - EVTO
PB21	53	PWM - PWM[2]	PM - GCLK[2]	EIM - SCAN[6]	LED - LED6	NEXUS - MCKO
PB22	54	PWM - PWM[3]	PM - GCLK[3]	EIM - SCAN[7]	LED - LED7	
PB23	55	TC - A0	USART1 - DCD		USART_1 - DCD	
PB24	56	TC - B0	USART1 - DSR		USART_1 - DSR	
PB25	57	TC - A1	USART1 - DTR		USART_1 - DTR	
PB26	58	TC - B1	USART1 - RI		USART_1 - RI	
PB27		TC - A2	PWM - PWM[4]		LED - LED0	
PB28	60	TC - B2	PWM - PWM[5]		LED - LED1	
PB29		USART2 - RXD	PM - GCLK[1]	EBI - NCS[2]	LED - LED2	
PB30		USART2 - TXD	PM - GCLK[2]	EBI - SDCS	LED - LED3	
PB31		USART2 - CLK	PM - GCLK[3]	EBI - NWAIT		
PC00	64					
PC01	65					
PC02	66					
PC03	67					
PC04	68					
PC05	69					
PX00		EBI - DATA[10]	USART0 - RXD		SDRAM - D10	
PX01		EBI - DATA[9]	USART0 - TXD		SDRAM - D9	
PX02		EBI - DATA[8]	USARTO - CTS		SDRAM - D8	
PX03	97	EBI - DATA[7]	USART0 - RTS		SDRAM - D7	

PX04 96 EBI - DATA[6] USART1 - RXD SDRAM - D6 PX05 95 EBI - DATA[5] USART1 - TXD SDRAM - D5 PX06 94 EBI - DATA[4] USART1 - CTS SDRAM - D4 PX07 93 EBI - DATA[3] USART1 - RTS SDRAM - D3 PX08 92 EBI - DATA[2] USART3 - RXD SDRAM - D2 PX09 91 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[1] USART3 - TXD SDRAM - D0 PX11 109 EBI - NWE1 USART2 - RXD SDRAM - D0 PX11 109 EBI - NWE0 USART2 - RXD SDRAM - D0 PX12 108 EBI - NWE0 USART2 - RTS PX13 107 EBI - NRD USART2 - RTS PX14 106 EBI - NCS[1] TC - A0 SDRAM - CSIn PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A16 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[13] EIM - SCAN[0] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[12] EIM - SCAN[1] TC - CLK0 SDRAM - A11 PX22 82 EBI - ADDR[12] EIM - SCAN[1] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[10] EIM - SCAN[1] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A10 PX25 79 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A11 PX26 78 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A11 PX27 77 EBI - ADDR[10] EIM - SCAN[1] SDRAM - A11 PX26 78 EBI - ADDR[10] EIM - SCAN[1] SDRAM - A11 PX27 79 EBI - ADDR[10] EIM - SCAN[1] SDRAM - A11 PX28 76 EBI - ADDR[10] EIM - SCAN[1] SDRAM - A11 PX29 75 EBI - ADDR[1] EIM - SCAN[1] SDRAM - A2 PX20 75 EBI - ADDR[1] SPI0 - MISO SDRAM - A2 PX21 70 EBI - ADDR[1] SPI0 - MISO SDRAM - A2 PX22 72 EBI - ADDR[1] SPI0 - SCK SDRAM - A2 PX30 74 EBI - ADDR[1] SPI0 - SCK SDRAM - DU4 PX31 70					1	
PX06 94 EBI - DATA[4] USARTI - CTS SDRAM - D4 PX07 93 EBI - DATA[3] USARTI - RTS SDRAM - D3 PX08 92 EBI - DATA[2] USART3 - RXD SDRAM - D2 PX09 91 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[0] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[0] USART3 - TXD SDRAM - D0 PX11 109 EBI - NWE0 USART2 - TXD SDRAM - D0 PX12 108 EBI - NWE0 USART2 - CTS PX13 107 EBI - NCS[1] TC - A0 SDRAM - CSIn PX14 106 EBI - NCS[1] TC - A0 SDRAM - CSIn PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A16 PX18 86 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[13] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[1] TC - CLK0 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A11 PX24 80 EBI - ADDR[13] EIM - SCAN[2] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[1] EIM - SCAN[2] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[1] EIM - SCAN[3] SDRAM - A10 PX24 80 EBI - ADDR[1] EIM - SCAN[3] SDRAM - A10 PX25 78 EBI - ADDR[1] EIM - SCAN[5] SDRAM - A8 PX27 77 EBI - ADDR[1] EIM - SCAN[1] SDRAM - A8 PX28 75 EBI - ADDR[1] EIM - SCAN[1] SDRAM - A8 PX29 75 EBI - ADDR[1] SPIO - NPCS[0] SDRAM - A5 PX30 74 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - A2 PX31 73 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - A2 PX33 71 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - A2 PX34 70 EBI - ADDR[1] SPIO - NPCS[2] SDRAM - A2 PX35 105 EBI - ADDR[1] SPIO - NPCS[2] SDRAM - D0M0 PX35 105 EBI - DATA[14] SPII - SCK SDRAM - D14 PX36 104 EBI - DATA[14] SPII - SCK SDRAM - D14	PX04	96	EBI - DATA[6]	USART1 - RXD		SDRAM - D6
PX07 93 EBI - DATA[3] USART1 - RTS SDRAM - D3 PX08 92 EBI - DATA[2] USART3 - RXD SDRAM - D2 PX09 91 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[0] USART3 - TXD SDRAM - D0 PX11 109 EBI - DATA[0] USART2 - RXD SDRAM - D0 PX11 109 EBI - NWE1 USART2 - TXD SDRAM - D0M1 PX12 108 EBI - NWE0 USART2 - CTS SDRAM - D0M1 PX13 107 EBI - NRD USART3 - RTS SDRAM - CS1n PX14 106 EBI - NCS[1] TC - A0 SDRAM - CS1n PX15 89 EBI - ADDR[19] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[16] TC - A2 SDRAM - A16 PX20 84 EBI - ADDR[14] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[13] EIM - SCAN[1] TC - CLK0 SDRAM - A13 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[1] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[1] SDRAM - A11 PX24 80 EBI - ADDR[1] EIM - SCAN[1] SDRAM - A10 PX25 79 EBI - ADDR[19] EIM - SCAN[1] SDRAM - A9 PX26 78 EBI - ADDR[18] EIM - SCAN[1] SDRAM - A8 PX27 77 EBI - ADDR[19] EIM - SCAN[1] SDRAM - A6 PX28 76 EBI - ADDR[1] SPIO - MISO SDRAM - A6 PX30 74 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - A3 PX31 73 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - A3 PX33 71 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - DIS PX34 70 EBI - ADDR[1] SPIO - NPCS[1] SDRAM - DIS PX35 TOS EBI - ADDR[1] SPIO - NPCS[1] SDRAM - DIS PX36 TOS EBI - ADDR[1] SPIO - NPCS[1] SDRAM - DIS PX36 TOS EBI - ADDR[1] SPIO - NPCS[2] SDRAM - DIS PX36 TOS EBI - ADDR[1] SPIO - NPCS[2] SDRAM - DIS PX36 TOS EBI - ADDR[1] SPIO - NPCS[2] SDRAM - DIS PX36 TOS EBI - DATA[14] SPIO - NPCS[3] SDRA	PX05			USART1 - TXD		SDRAM - D5
PX08 92 EBI - DATA[2] USART3 - RXD SDRAM - D2 PX09 91 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[0] USART2 - RXD SDRAM - D0 PX11 109 EBI - NWE1 USART2 - TXD SDRAM - DQM1 PX12 108 EBI - NWE0 USART2 - CTS PX13 107 EBI - NRD USART2 - RTS PX14 106 EBI - NCS[1] TC - A0 SDRAM - CSIn PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[19] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[13] EIM - SCAN[1] TC - CLK0 SDRAM - A13 PX22 82 EBI - ADDR[12] EIM - SCAN[1] TC - CLK1 SDRAM - A11 PX22 82 EBI - ADDR[11] EIM - SCAN[1] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[1] TC - CLK2 PUSHBUTTON - USW3 PX24 80 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX25 79 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[1] EIM - SCAN[6] SDRAM - A8 PX27 77 EBI - ADDR[18] EIM - SCAN[1] SDRAM - A8 PX28 76 EBI - ADDR[15] SPI0 - MOS SDRAM - A5 PX30 74 EBI - ADDR[14] SPI0 - NPCS[0] SDRAM - A6 PX31 73 EBI - ADDR[1] SPI0 - NPCS[1] SDRAM - A2 PX33 TO EBI - ADDR[1] SPI0 - NPCS[1] SDRAM - A2 PX34 70 EBI - ADDR[1] SPI0 - NPCS[1] SDRAM - A2 PX35 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX31 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX33 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX34 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX35 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX36 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX36 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - DQM0 PX36 TO EBI - DATA[14] SPI1 - MISO SDRAM - DI5 PX36 TO	PX06	94	EBI - DATA[4]	USART1 - CTS		SDRAM - D4
PX09 9 EBI - DATA[1] USART3 - TXD SDRAM - D1 PX10 90 EBI - DATA[0] USART2 - RXD SDRAM - D0 PX11 109 EBI - NWE0 USART2 - TXD SDRAM - D0 PX12 108 EBI - NWE0 USART2 - CTS PX13 107 EBI - NRD USART2 - RTS PX14 106 EBI - NCS[1] TC - A0 SDRAM - CS1n PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A17 PX18 80 EBI - ADDR[16] TC - A2 SDRAM - A16 PX20 84 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A11 PX24 80 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[4] SDRAM - A10 PX25 79 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A10 PX26 78 EBI - ADDR[1] EIM - SCAN[6] SDRAM - A9 PX27 77 EBI - ADDR[1] EIM - SCAN[6] SDRAM - A8 PX29 75 EBI - ADDR[1] SP10 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SP10 - MOS1 SDRAM - A6 PX30 74 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A3 PX31 73 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SP10 - NPCS[1] SDRAM - A3 PX33 71 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX33 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX33 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX34 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - DUS PX36 104 EBI - DATA[14] SP11 - SCK SDRAM - DU5 PX36 104 EBI - DATA[14] SP11 - SCK SDRAM - DU5 PX36 TX	PX07	93	EBI - DATA[3]	USART1 - RTS		SDRAM - D3
PX10 90 EBI - DATA[0] USART2 - RXD SDRAM - D0 PX11 109 EBI - NWE1 USART2 - TXD SDRAM - DQM1 PX12 108 EBI - NWE0 USART2 - CTS PX13 107 EBI - NRD USART2 - RTS PX14 106 EBI - NCS[1] TC - A0 SDRAM - CSIn PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A17 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[2] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[10] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[10] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A10 PX25 79 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[1] EIM - SCAN[6] SDRAM - A9 PX27 77 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX29 75 EBI - ADDR[1] SP10 - MISO SDRAM - A5 PX30 74 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A3 PX31 73 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A2 PX33 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX34 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX33 TO EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX34 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX35 TO EBI - DATA[14] SP11 - SCK SDRAM - D15 PX36 TO EBI - DATA[14] SP11 - SCK SDRAM - D15 PX36 TO EBI - DATA[14] SP11 - SCK SDRAM - D14	PX08	92	EBI - DATA[2]	USART3 - RXD		SDRAM - D2
PX11 109 EBI - NWE1 USART2 - TXD SDRAM - DQM1 PX12 108 EBI - NWE0 USART2 - CTS PX13 107 EBI - NRD USART2 - RTS PX14 106 EBI - NCS[1] TC - A0 SDRAM - CSIn PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A17 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A10 PX25 79 EBI - ADDR[19] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[1] SPI0 - MISO SDRAM - A7 PX27 77 EBI - ADDR[1] SPI0 - MISO SDRAM - A6 PX29 75 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX20 T3 EBI - ADDR[1] SPI0 - NPCS[1] SDRAM - A6 PX30 74 EBI - ADDR[2] SPI0 - NPCS[1] SDRAM - A7 PX31 73 EBI - ADDR[2] SPI0 - NPCS[1] SDRAM - A2 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 73 EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX34 70 EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - A2 PX33 TO EBI - ADDR[1] SPI0 - NPCS[2] SDRAM - DIS PX34 T0 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX35 T0 EBI - DATA[15] SPI1 - MISO SDRAM - D15 PX36 T04 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX36 T04 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX36 T04 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX36 T04 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX31 T3 EBI - DATA[14] SPI1 - SCK	PX09	91	EBI - DATA[1]	USART3 - TXD		SDRAM - D1
PX12 108 EBI - NWE0	PX10	90	EBI - DATA[0]	USART2 - RXD		SDRAM - D0
PX13	PX11	109	EBI - NWE1	USART2 - TXD		SDRAM - DQM1
PX14 106 EBI - NCS[1] TC - A0 SDRAM - CS1n PX15 89 EBI - ADDR[19] USART3 - RTS TC - B0 PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A16 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[12] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A6 PX29 75 EBI - ADDR[6] SP10 - MOS1 SDRAM - A6 PX29 75 EBI - ADDR[4] SP10 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[2] SP10 - NPCS[1] SDRAM - A2 PX33 71 EBI - ADDR[1] SP10 - NPCS[1] SDRAM - A2 PX34 70 EBI - ADDR[1] SP10 - NPCS[2] SDRAM - A2 PX35 105 EBI - DATA[15] SP11 - MOS1 SDRAM - DU5 PX36 104 EBI - DATA[14] SP11 - SCK SDRAM - DU5 PX36 104 EBI - DATA[14] SP11 - SCK SDRAM - DU4	PX12			USART2 - CTS		
PX15	PX13			USART2 - RTS		
PX16 88 EBI - ADDR[18] USART3 - CTS TC - A1 PUSHBUTTON - USW1 PX17 87 EBI - ADDR[17] TC - B1 SDRAM - A17 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[13] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[4] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MISO SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - MOSI SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[2] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DUM0 PX35 105 EBI - DATA[14] SPI1 - MOSI SDRAM - DUM0 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - DU4	PX14			TC - A0		SDRAM - CS1n
PX17 87 EBI - ADDR[17] TC - BI SDRAM - A17 PX18 86 EBI - ADDR[16] TC - A2 SDRAM - A16 PX19 85 EBI - ADDR[15] EIM - SCAN[0] TC - B2 PUSHBUTTON - USW2 PX20 84 EBI - ADDR[14] EIM - SCAN[1] TC - CLK0 SDRAM - A14 PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[12] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[4] SDRAM - A10 PX25 79 EBI - ADDR[10] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A9 PX27 77 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MISO SDRAM - A6 PX29 75 EBI - ADDR[6] SPI0 - MOSI SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A2 PX33 71 EBI - ADDR[2] SPI0 - NPCS[1] SDRAM - A2 PX34 70 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX35 105 EBI - DATA[14] SPI1 - MOSI SDRAM - DQM0 PX35 105 EBI - DATA[14] SPI1 - MOSI SDRAM - D14 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX36 TATAL TATA	PX15	89	EBI - ADDR[19]	USART3 - RTS	TC - B0	
PX18	PX16	88	EBI - ADDR[18]	USART3 - CTS	TC - A1	PUSHBUTTON - USW1
PX19	PX17	87	EBI - ADDR[17]	TC - B1		SDRAM - A17
PX20	PX18	86	EBI - ADDR[16]	TC - A2		SDRAM - A16
PX21 83 EBI - ADDR[13] EIM - SCAN[2] TC - CLK1 SDRAM - A13 PX22 82 EBI - ADDR[12] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[5] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[2] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX48 SDRAM - D15 SDRAM - D14 PX49 SDRAM - D14 SDRAM - D14 PX40 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX42 SDRAM - D14 SDRAM - D14 PX43 SDRAM - D14 SDRAM - D14 PX44 SDRAM - D15 SDRAM - D14 PX45 SDRAM - D14 SDRAM - D14 PX46 SDRAM - D14 SDRAM - D14 PX47 SDRAM - D14 SDRAM - D14 PX48 SDRAM - D14 SDRAM - D14 PX49 SDRAM - D14 SDRAM - D14 PX40 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D15 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX42 SDRAM - D14 SDRAM - D14 PX41 SDRAM - D14 SDRAM - D14 PX42 SDRAM - D14 SDRAM - D14 PX45 SDRAM - D14 SDRAM - D14 PX45 SDRAM - D14 SDRAM - D14 PX46 SDRAM - D14 SDRAM - D14 PX46 SDRAM - D15 SDRAM - D14 PX47 SDRAM - D15 SDRAM - D14 PX48 SDRAM - D15 SDRAM - D15 SDRAM - D15 PX48 SDRAM - D14 SDRAM - D15	PX19	85	EBI - ADDR[15]	EIM - SCAN[0]	TC - B2	PUSHBUTTON - USW2
PX22 82 EBI - ADDR[12] EIM - SCAN[3] TC - CLK2 PUSHBUTTON - USW3 PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[5] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - SCK SDRAM - D14 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14 PX37 SDRAM - D14 SDRAM - D14 PX38 SDRAM - D14 SDRAM - D14 PX39 SDRAM - D14 SDRAM - D14 PX30 SDRAM - D14 SDRAM - D14 PX30 SDRAM - D14 SDRAM - D14 PX30 SDRAM - D14 SDRAM - D14 PX31 SDRAM - D14 SDRAM - D14 PX32 SDRAM - D14 SDRAM - D14 PX33 SDRAM - D14 SDRAM - D14 PX34 SDRAM - D15 SDRAM - D14 PX35 SDRAM - D14 SDRAM - D14 PX36 SDRAM - D15 SDRAM - D14 PX37 SDRAM - D14 SDRAM - D14 PX38 SDRAM - D15 SDRAM - D14 PX39 SDRAM - D14 SDRAM - D14 PX30 SDRAM - D14 SDRAM - D14 PX31 SDRAM - D14 SDRAM - D14 PX31 SDRAM - D15 SDRAM - D14 PX31 SDRAM - D14 SDRAM - D14 PX31 SDRAM - D15 SDRAM - D14 PX31 SDRAM - D15 SDRAM - D15 SDRAM - D15 PX36 SDRAM - D14 SDRAM - D15 SDRAM - D15 PX36 SDRAM - D14 SDRAM - D15 SDRAM - D15 PX36 SDRAM - D15 SDRAM - D15 SDRAM - D15 PX31 SDRAM - D15 SDRAM	PX20	84	EBI - ADDR[14]	EIM - SCAN[1]	TC - CLK0	SDRAM - A14
PX23 81 EBI - ADDR[11] EIM - SCAN[4] SDRAM - A11 PX24 80 EBI - ADDR[10] EIM - SCAN[5] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A9 PX27 77 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[6] SPIO - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPIO - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPIO - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPIO - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPIO - NPCS[1] SDRAM - A2 PX32 72 EBI - ADDR[2] SPIO - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPIO - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPII - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPII - MOSI SDRAM - D14 PX36 104 EBI - DATA[14] SPII - SCK SDRAM - D14	PX21	83	EBI - ADDR[13]	EIM - SCAN[2]	TC - CLK1	SDRAM - A13
PX24 80 EBI - ADDR[10] EIM - SCAN[5] SDRAM - A10 PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D14 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX22	82	EBI - ADDR[12]	EIM - SCAN[3]	TC - CLK2	PUSHBUTTON - USW3
PX25 79 EBI - ADDR[9] EIM - SCAN[6] SDRAM - A9 PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX23	81	EBI - ADDR[11]	EIM - SCAN[4]		SDRAM - A11
PX26 78 EBI - ADDR[8] EIM - SCAN[7] SDRAM - A8 PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX24	80	EBI - ADDR[10]	EIM - SCAN[5]		SDRAM - A10
PX27 77 EBI - ADDR[7] SPI0 - MISO SDRAM - A7 PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX25		= =	EIM - SCAN[6]		SDRAM - A9
PX28 76 EBI - ADDR[6] SPI0 - MOSI SDRAM - A6 PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX26	78	EBI - ADDR[8]	EIM - SCAN[7]		SDRAM - A8
PX29 75 EBI - ADDR[5] SPI0 - SCK SDRAM - A5 PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX27	77	EBI - ADDR[7]	SPI0 - MISO		SDRAM - A7
PX30 74 EBI - ADDR[4] SPI0 - NPCS[0] SDRAM - A4 PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX28	76	EBI - ADDR[6]	SPI0 - MOSI		SDRAM - A6
PX31 73 EBI - ADDR[3] SPI0 - NPCS[1] SDRAM - A3 PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX29	75	EBI - ADDR[5]	SPI0 - SCK		SDRAM - A5
PX32 72 EBI - ADDR[2] SPI0 - NPCS[2] SDRAM - A2 PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX30	74	EBI - ADDR[4]			SDRAM - A4
PX33 71 EBI - ADDR[1] SPI0 - NPCS[3] Host VBUS Over Current Detect PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX31	73	EBI - ADDR[3]	SPI0 - NPCS[1]		SDRAM - A3
PX34 70 EBI - ADDR[0] SPI1 - MISO SDRAM - DQM0 PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX32	72	EBI - ADDR[2]			
PX35 105 EBI - DATA[15] SPI1 - MOSI SDRAM - D15 PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14				SPI0 - NPCS[3]		
PX36 104 EBI - DATA[14] SPI1 - SCK SDRAM - D14	PX34	70	EBI - ADDR[0]	SPI1 - MISO		SDRAM - DQM0
	PX35	105	EBI - DATA[15]	SPI1 - MOSI		SDRAM - D15
PX37	PX36	104	EBI - DATA[14]	SPI1 - SCK		SDRAM - D14
	PX37	103	EBI - DATA[13]	SPI1 - NPCS[0]		SDRAM - D13
PX38 102 EBI - DATA[12] SPI1 - NPCS[1] SDRAM - D12	PX38	102	EBI - DATA[12]	SPI1 - NPCS[1]		SDRAM - D12
PX39 101 EBI - DATA[11] SPI1 - NPCS[2] SDRAM - D11	PX39	101	EBI - DATA[11]	SPI1 - NPCS[2]		SDRAM - D11