

Kanchan Badlani

Flat#101, ND Hazel, 13th Main 12th Cross, BTM 2nd Stage, Bangalore -560076
Tel:9739840247 / Email: kanchan_badlani@yahoo.com

CARRER SUMMARY:

5+ years of experience in teaching high school mathematics,
1+ years of experience in testing and automating GUI and CLI of network devices.
Active Member of Hacker rank.
Excellent communication and inter personal skills.
Excellent mathematical and problem solving skills, and ability to tackle any urgent situations
Good analytical capability and comprehensive problem solving abilities

TECHNICAL SKILLS

Mathematics: ICSE, CBSE and IGCSE High School syllabus
Programming Languages: C, C++, Data Structure and Algorithms, Python, HTML
Operating Systems: Windows 7, XP, Vista,
Networking: TCP/IP
Tools: Silk 6.0 (automated testing tool)

EDUCATION

Bachelors of Engineering – Biomedical engineering – 62.74%, June 2001.
Thadomal Shahani Engineering College, Mumbai, India.

CCNA certified in 2002

ONLINE COURSES DONE:

1. Algorithms from ‘Algorithm Design by Tim Roughgarden.’

- This course helped me to understand the theoretical aspects of solutions to different problems which can be solved using Computer science.
- Understood various data structures like **arrays, linked Lists, Array Lists, Trees, Binary search trees, Graph** etc.
- Understood **analysis of algorithms**, and also different techniques like **Iterator, Divide and Conquer, Greedy Algorithms**.

2. Active Member of Hackerrank.

Been an active member of Hackerrank and solved different problems related to different **data**

structures and Algorithms also an active participant of coding competitions

3. Python by Google Python Class

This class help me to understand basic python programming : string, list, tuple, regular expression, function, sorting by working on given exercises.

4. From NAND to Tetris' by Noam Nisan and Shimon Schocken.

- This course basically aims to understand the complete internal working of a computer by starting from basic **NAND gates** and implementing a complete Computer.
- Universal gates NAND were used to implement different chips like **OR gate, AND gate, 16 bit Multiplexer and Demultiplexor**.

PROFESSIONAL EXPERIENCE

1. Presently coaching mathematics to X grade IGCSE and CBSE students

2. St. Mira High School, Bangalore, 2006-2009

Teaching ICSE Mathematics and Physics to VIII, IX, and X students.

3. Network Robots (Bivio Networks), Pleasanton, CA, US: 2002-2003

Trainee Engineer

- Writing Test plans, testing GUI manually
- Developing test cases using silk test tool for unit testing, functional and Integration testing.
- Reporting bugs through EDTs bug tracking system

4. Atoga Systems, Fremont, CA, US – 2001-2002.

Trainee Engineer

- Writing Test plans for testing AppDirector, testing AppDirector manually, developing test cases using Silk test tool, reporting bugs through Bugzilla bug tracking system.
- Assist other SQA and NMS engineers with issues on Atoga Application AppDirector.

