KANDAGATLA U15974415 USF EE

This is the simple loopback system. The output is the value transmitted into the system i.e., HELLO. If you observe the receiver data we can see the same data sent from transmitter. All these signals depends on rising edge of the clock.The reset is in active high state. The transmission and receiving signals are in the rising state at the same intervals. The data is transmitted when the txsig signal is 1 and then it goes to 0 and then the txrdy signal becomes 1 and the process continues for every data. It has clk baud rate for sending the data. It is the total view of sending the data in loopback system.