Lw R9, 0(R6)	IFC-1	IFC-2	IFC-3	DEC	EXE	MEM-1	MEM-2	MEM-	-3 WBK																	
Mul R9, R9, R8		IFC-1	IFC-2	IFC-3	DEC	Ø	Ø	Ø	• EXE	MEM-1	MEM-2	МЕМ-3	WBK													
Lw R10, 0(R5)			IFC-1	IFC-2	IFC-3	Ø	Ø	Ø	DEC	EXE	MEM-1	MEM-2	MEM-3	O WBK												
Sub R10, R10, R9				IFC-1	IFC-2	Ø	Ø	Ø	IFC-3	DEC	Ø	Ø	Ø	EXE (	MEM-1	MEM-2	МЕМ-3	WBK								
Sw R10, 0(R5)					IFC-1	Ø	Ø	Ø	IFC-2	IFC-3	Ø	Ø	Ø	DEC (	EXE	MEM-1	MEM-2	МЕМ-3	WBK							
Addiu R6, R6, 4									IFC-1	IFC-2	Ø	Ø	Ø	IFC-3	DEC	EXE	MEM-1	MEM-2	МЕМ-3	WBK						
Addiu R5, R5, 4										IFC-1	Ø	Ø	Ø	IFC-2	IFC-3	DEC	EXE (	MEM-1	MEM-2	МЕМ-3	WBK					
Bne R5, R7, Loop														IFC-1	IFC-2	IFC-3	Ø	DEC	EXE	MEM-1	MEM-2	МЕМ-3	WBK			
Nop															IFC-1	IFC-2	Ø	IFC-3	DEC	EXE	MEM-1	MEM-2	МЕМ-3	WBK		
Nop																IFC-1	Ø	IFC-2	IFC-3	DEC	EXE	MEM-1	MEM-2	МЕМ-3	WBK	
Nop																		IFC-1	IFC-2	IFC-3	DEC	EXE	MEM-1	MEM-2	МЕМ-3	WBK

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