Quiz 2

Due by 8am on Friday, October 6. You only get one submission. Corresponds to the lecture on October 4, and the reading from section 6.4.1 in the Harris textbook.

* Required

1. **Email address** *
   ANSWER KEY

2. **What is the opcode in the 32-bit ARM instruction 1110000 0100 0 0010 0000 00000000 0001?** *
   Mark only one oval.
   - ◯ 0100
   - ◯ 0000
   - ◯ 0010
   - ◯ 0001

3. **For the instruction set from lecture on October 4, which of the below is the best English description of the instruction 00 01 10 10?** *
   Mark only one oval.
   - ◯ Add r1 to r1 and store the result in r2
   - ◯ Subtract r2 from r1 and store the result in r2
   - ◯ Move the value 1010 to r1
   - ◯ Add r2 to r2 and store the result in r1

4. **There is an instruction in ARM that subtracts a number from a register directly(e.g. using an immediate value), without requiring that the value be stored in a register first.** *
   Mark only one oval.
   - ◯ True
   - ◯ False

5. **The opcode in this instruction corresponds to ADD: 1110000 0010 0 0010 0000 00000000 0001** *
   Mark only one oval.
   - ◯ True
   - ◯ False

☐ Send me a copy of my responses.