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\* Required

1. Email address *
ANSWER KEY
2. What register does the ARM calling convention use for the return value? *  Mark only one oval.  r0  r1  lr  sp  r4
3. If, in C, we write the function call "f(34, 56, 78)", what register will the called function expect to contain 56 according to the ARM calling convention? *
Mark only one oval.
r0
r2
r3
sp
4. A function always needs to save the arguments to the stack as local variables in order to execute correctly. *
Mark only one oval.
True
False
5. How is the "bl" instruction different from "b"? *  Mark only one oval.
It only runs if the "I" flag is set
It saves the return address to the link register before branching
It pushes any necessary registers to the stack before branching
It only works in leaf functions

A copy of your responses will be emailed to the address you provided

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