## COMP 3400 Computational Logic and Automated Reasoning

## Winter 2017 Assignment 3

## **Instructions:**

1. For your solution use the template file that was posted on the course news, and follow the instructions in it.

In particular: (a) Include at the top of the first page: full name, student number, and email address. (b) Assignments have to be created with Latex, and submitted in pdf format, as a single file. (c) Every problem solution MUST include the problem statement, even if the assignment does not (sometimes the assignments will refer to slides presented in class). The source file for this assignment is provided.

Latex has to be used as such, not as you would use a text editor, such as Notepad. In particular, formulas have to be written using Latex's mathematical features, and then compiled.

- 2. Assignments are individual, no groups.
- 3. Submit by email to the instructor, with "Assignment "Number", CompLog" in the subject. **Include your last name in the file name!** For example, in the subject: "Assig. 3 CompLog". The file name: "YourLastName-3.pdf".

Only a single pdf file will be accepted as submission (nothing other than a pdf file will be accepted). No tar or zip files (or anything like that), please. Keep your Latex source files in case you are requested to show them.

- 4. Explain your solution very carefully, but still be succinct with your answers. No unnecessary verbose arguments, please. Go to the point.
  - Make explicit all your assumptions.
- 5. Not following the instructions above or the solution template file will make you lose points.
- 1. A binary adder is shown in Figure 1. The adder, on test inputs may be showing unintended results. Actually, the input values are: A = 0, B = 1, C = 0; and the output values are: D = 0 E = 1.
- (a) Outside Prover9 (using formulas written with Latex), construct a weak-failure model of the circuit using PROPOSITIONAL LOGIC. Explain each formula and its role in the model. Start by listing the propositional variables and their intended meanings.

(b) You want to obtain minimal *conflicts*, that is, sets of components that cannot be all simultaneously working well at the light of the observation. (Minimality means in this case that no proper subset of a conflict is a conflict.)

Before going into Prover9, explain your methodology based on automated reasoning à la Prover9 for obtaining: (a) conflicts, (b) minimal ones (how can you be sure?), and (c) more than one with the same general specification.

- (c) Use Prover9 to obtain all possible minimal conflicts. Show the main ingredients of the input file, with an explanation. Do the same with the output file, interpreting and explaining the results.
- (d) Attach as appendices the full input and output files (including the runs) for the parts above, clearly indicating each of them.

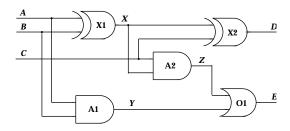


Figure 1: A Complete Adder

(Gates X1, X2 are exclusive or-gates)

2. (a) Produce a knowledge base (program) in PROPOSITIONAL PRO-LOG capturing the following information as closely as possible as stated here: If Tweety is a bird and it is not an abnormal bird, then it flies. A bird is abnormal exactly when it is an ostrich, a penguin, or not an abnormal wooden bird. A wooden bird is abnormal exactly when operated under remote control. Tweety is a bird, it is not an ostrich nor a penguin nor a remote controlled wooden bird. All this has to be provided as a set of clauses (rules) outside Prolog, using formulas written in Latex. Start by listing the propositional variables and their intended meanings. Notice that the two forms of abnormality mentioned here may be different, one is if birds,

the other is for wooden birds.

- (b) Show by means of a refutation tree developed á la Prolog that *Tweety flies* (as we did in class).
- (c) Now use SWI Prolog to prove that *Tweety flies*. Explain your approach and what Prolog shows through its run. Attach a file showing the input file, and the whole run (interaction, trace) with Prolog.

The whole assignment has to be submitted as a single PDF file. Deadline: March 6, at 23:55