Wikhil Microcontroller Monday, 16 September 2019

1. assembler-instructions

2. ATMEGAS - 8KB DX 0000

3. 2-byte instruction

RISC

5. (9) LDI R16,0x25 - produce opcode

(b) ADD R23, R19 -> Broduce Opeode (c) ORG -> directive

(d) JMPHERE - opcode

R20 =5 221 = 2

ADD R20, R21 => R20 = 7

ADD R20, R21 => R20 =9 1/2-

2. PORTB = PORTB ^ OXFF;

DDRB = 0x00; [input]
DDRC = 0xFF; output 3. PORTC - PINB;

4. LD1 R20,4

> DEC R20 720 DEC R20 7:0

> DEC R20 2=0

220 DEC 2=1