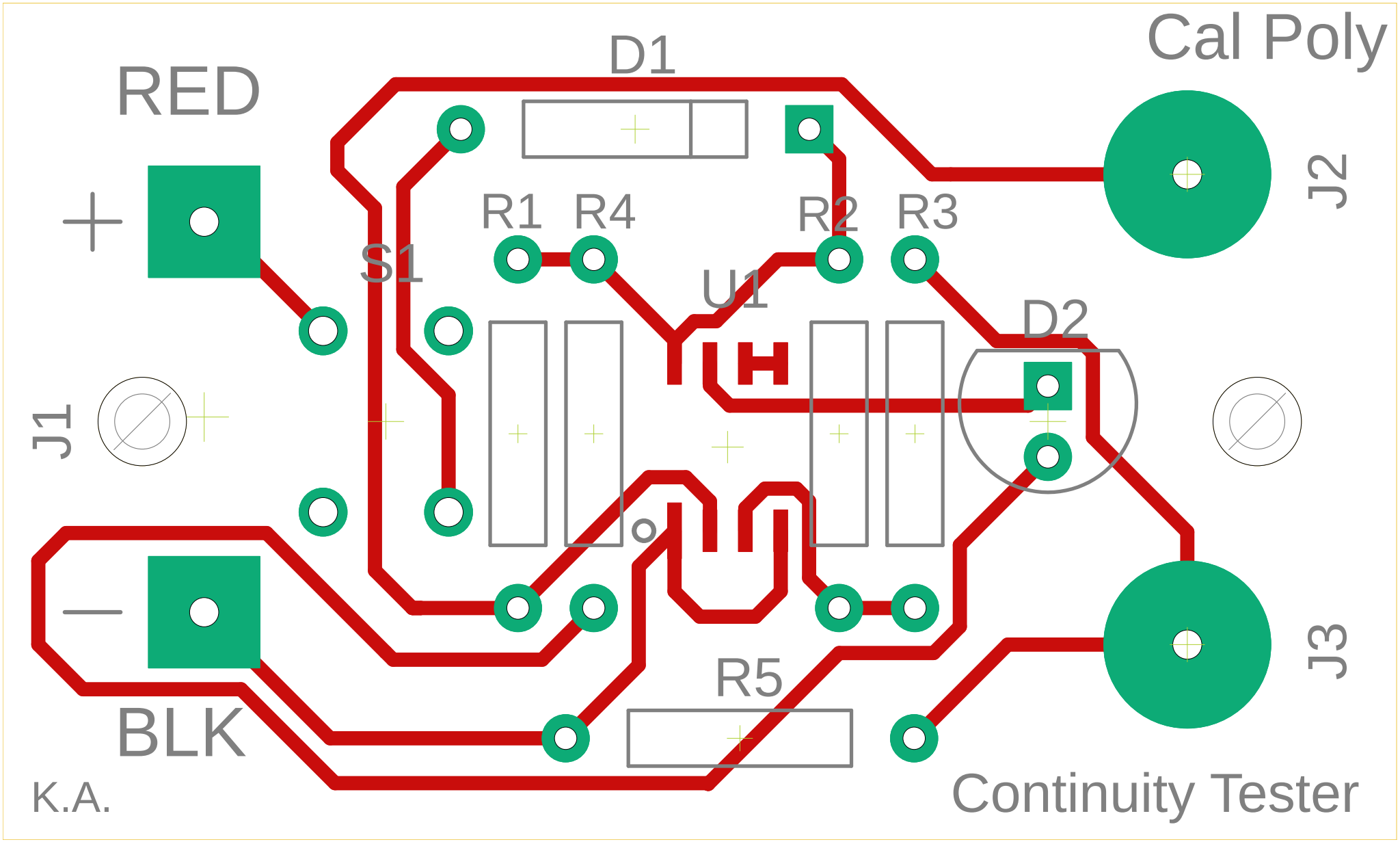
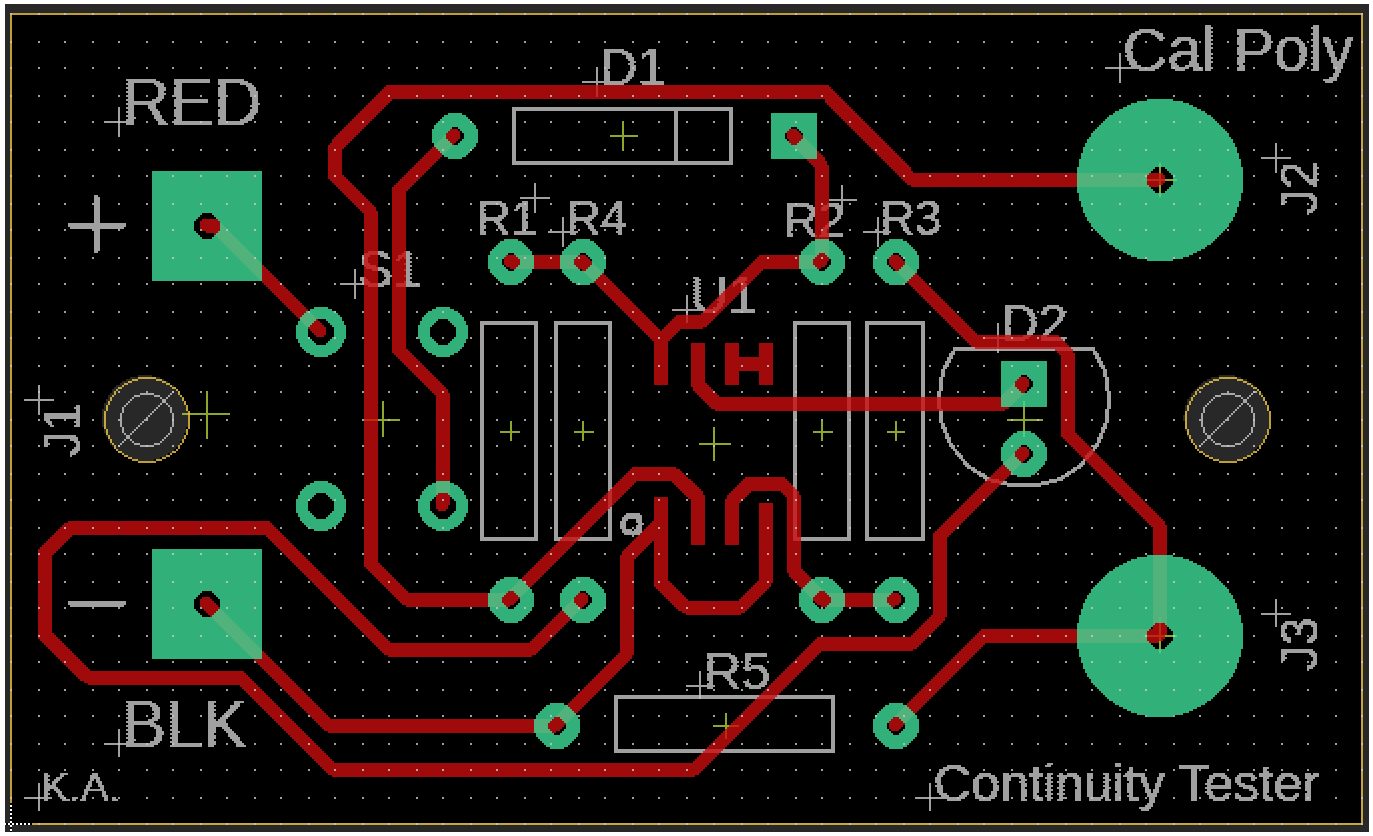

Cal Poly
RED
D1
J2
R1
R4
R2
R3
S1
U1
D2
J1
J3
R5
BLK
K.A.
Continuity Tester


Cal Poly
RED
D1
R1
R4
R2
R3
S1
U1
D2
J1
J2
J3
R5
BLK
K.A.
Continuity Tester

1 Board - C:\Users\katri\Desktop\EAGLE PCB\Continuity Tester.brd - EAGLE 9.1.0 free

File Edit Draw View Tools Library Options Window Help

Layer: 1 Top

Design Manager

Devices Signals

Net Classes 1 of 1 shown (1 selected)

Search

Name	Min Width (mm)	Min Via Drill (mm)	Clearance (mm)
default	0.000	0.000	0.000

Signals 10 of 10 shown (0 selected)

Search

Name	Pins	Trace (mm)	Airwire (mm)	Vias
N$1	2	5.890	0.000	0
N$2	2	15.259	0.000	0
N$3	5	20.685	0.000	0
N$5	3	10.571	0.000	0
N$6	3	30.810	0.000	0

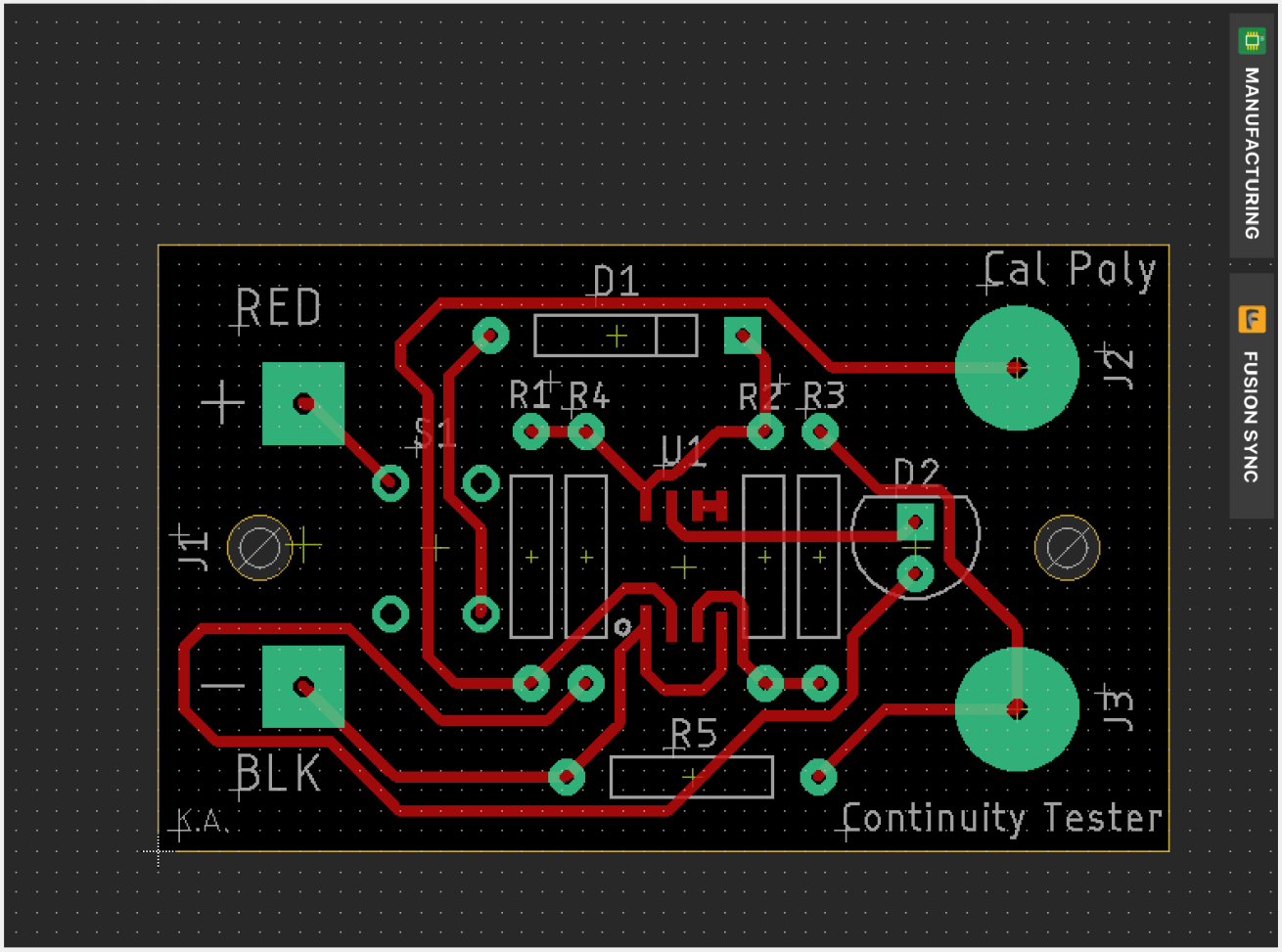
Items 0 of 0 shown (0 selected)

Search

Type	Device	Name	Laye

1 mm (16 15)

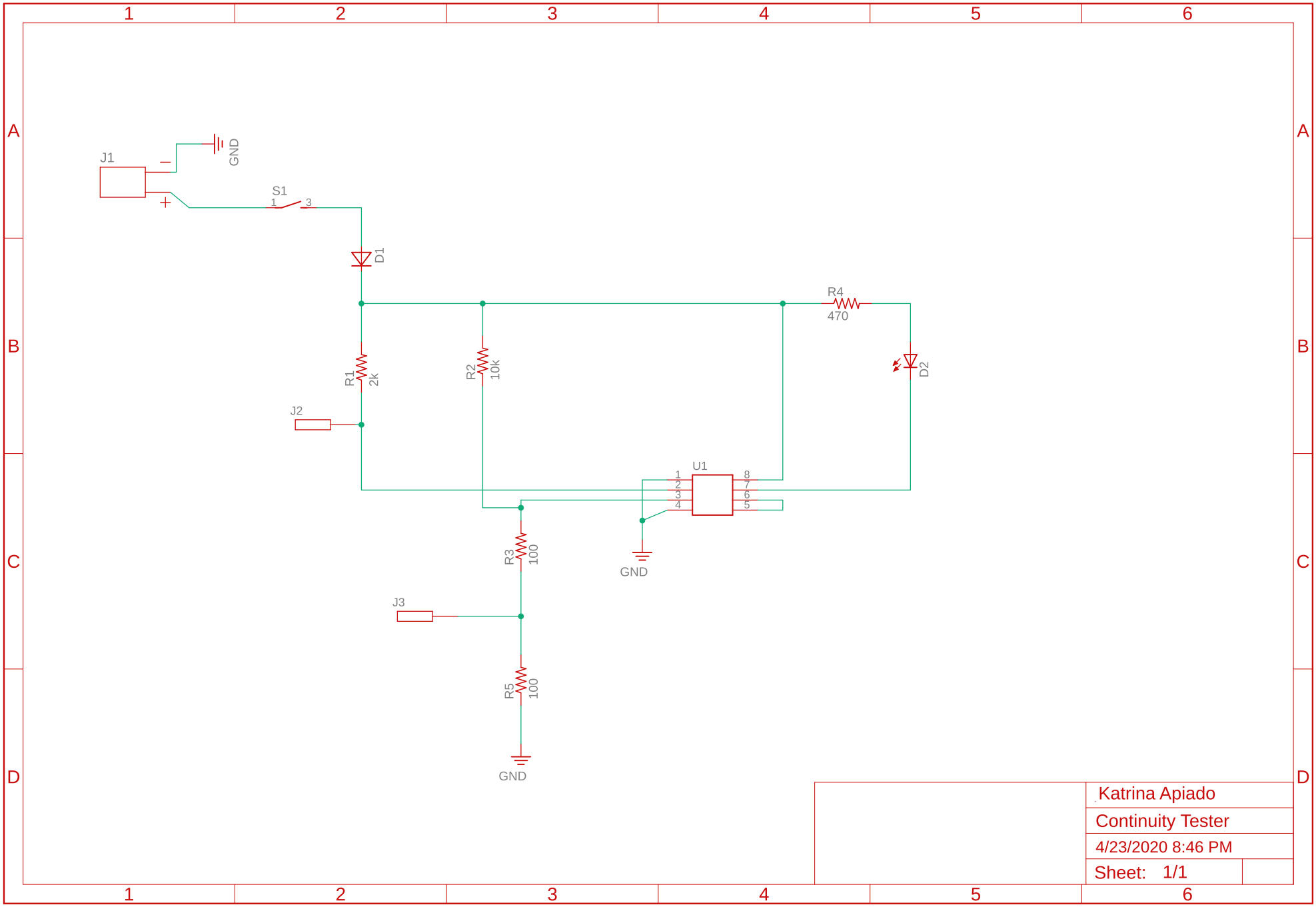
Click or press Ctrl+L key to activate command line mode



MANUFACTURING

FUSION SYNC

DRC: No errors. Left-click & drag to define group (or left-click to start defining a group polygon)



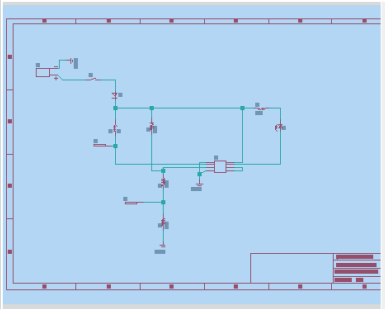
4/26/2020 3:45 PM f=1.00 C:\Users\katri\Desktop\EAGLE PCB\Continuity Tester.sch (Sheet: 1/1)

1 Schematic - C:\Users\katri\Desktop\EAGLE PCB\Continuity Tester.sch - EAGLE 9.1.0 free

File Edit Draw View Tools Library Options Window Help

Layer: 91 Nets

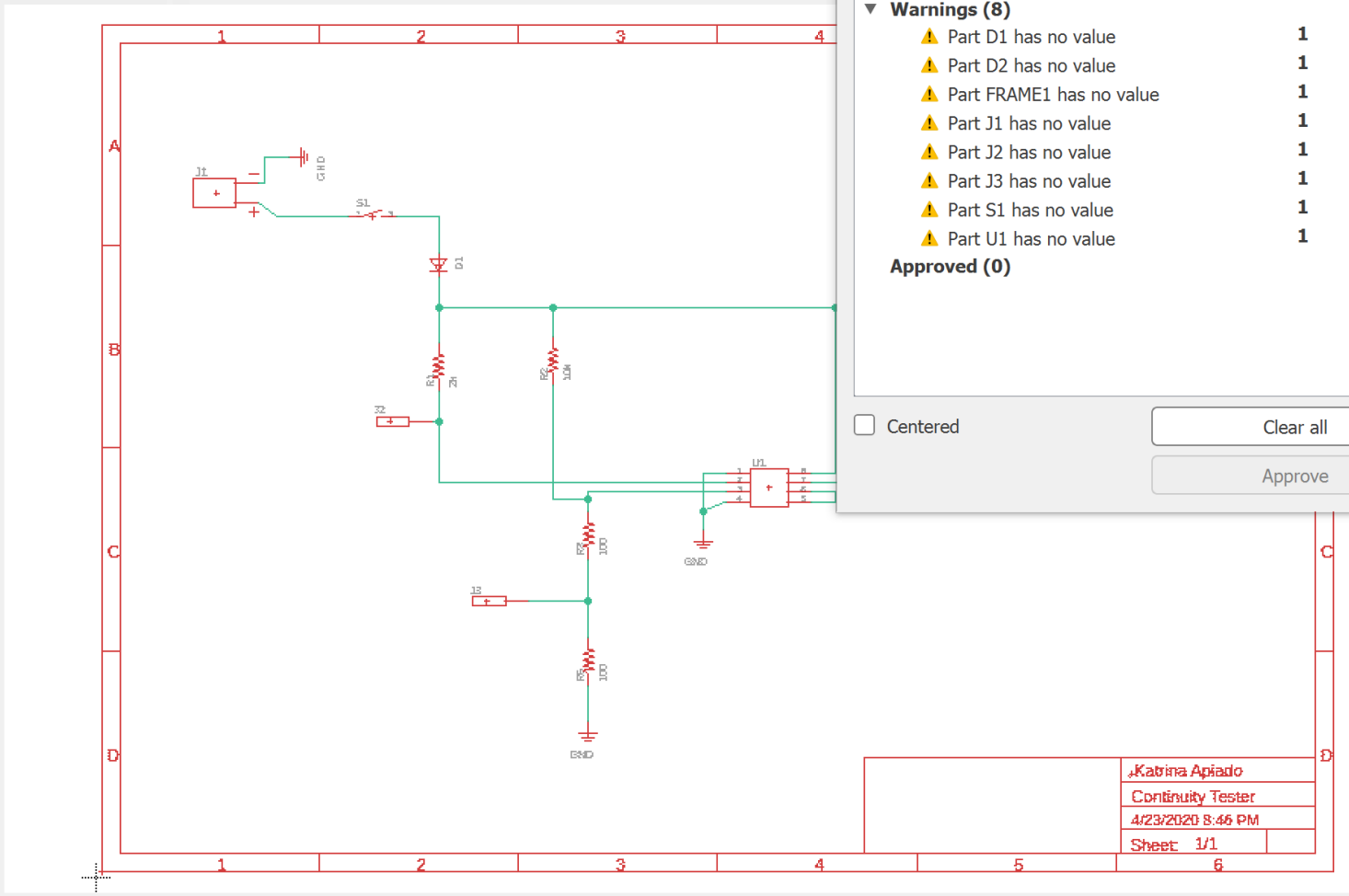
Sheets



1

0.1 inch (5.1 0.2)

Click or press Ctrl+L key to activate command line mode



ERC Errors

Type	Sheet	Module
Board and schematic are consistent		
Errors (0)		
Warnings (8)		
Part D1 has no value	1	
Part D2 has no value	1	
Part FRAME1 has no value	1	
Part J1 has no value	1	
Part J2 has no value	1	
Part J3 has no value	1	
Part S1 has no value	1	
Part U1 has no value	1	
Approved (0)		

Centered

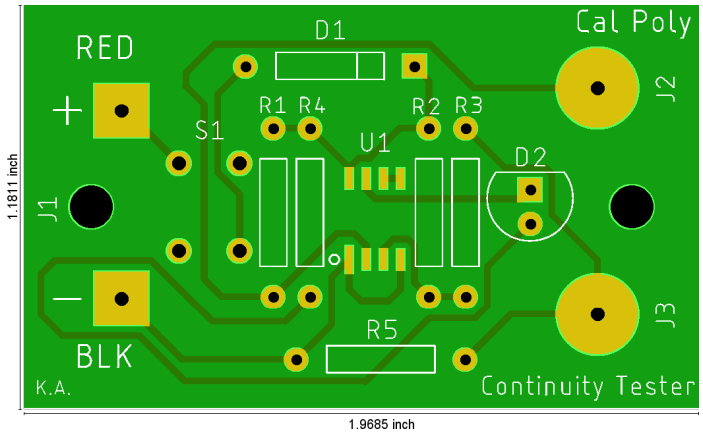
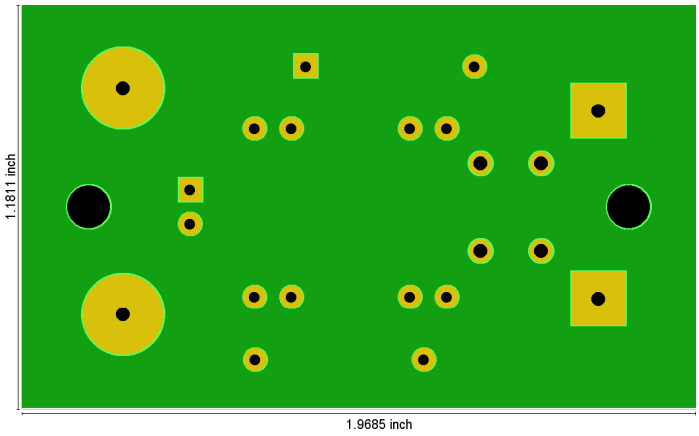
Clear all

Approve

ERC: 8 errors/warnings - board and schematic are consistent. Left-click & drag to define group (or left-click to start defining a group polygon)

Name	dhd16ni2.zip	Id.	41561 - QED OK
Report Generated on	Apr 26, 2020 12:21:52 PM	Customer	InstantDFM
Board Id			

Single PCB View - Original

Top View	Bottom View
	

Summary - General - Original

PCB Size	1.9685 inch x 1.1811 inch	Surface Finish	unknown
PCB Thickness	62.00 mil	Max. Aspect Ratio on PTH	2.0
Customer Panel Size		Number of Nets	12
Copper Layers	2	Electrical Test	Single Sided
Solder Mask	Both	Drilled SMD Pads	No
Solder Mask Color	Green	SMD Pads Top	8
Legend	Top Only	SMD Pads Bottom	0
Legend Color	White	BGA Pads Top	0
Peeloff Mask	None	BGA Pads Bottom	0
Carbon Mask	None	Drill Hole Density	10 Holes/inch^2
Edge Connectors	No		

Summary - Copper Layer Minima - Original

Layer Type	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Trace to Trace	Same Net Clr.	Ring	Plated	Copper to NPTH Clr.	Copper to Outline Clr.
	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
Outer	>16.00	>16.00	>16.00	>16.00 [1]	20.00 [2]	20.00	>20.00 [3]	13.51	>32.00	>64.00	40.00 [4]

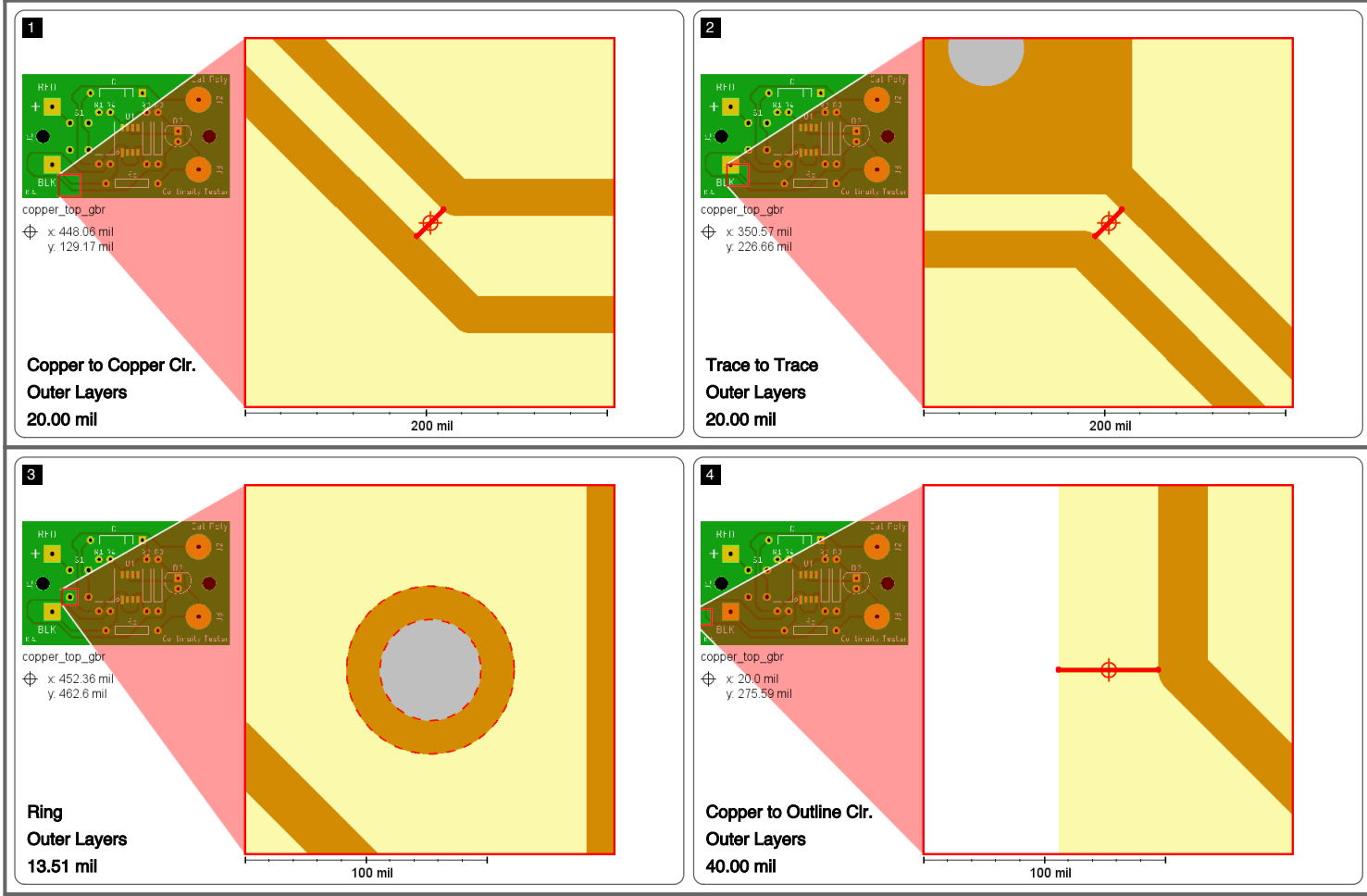
Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Moves	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.
			mil	mil			mil	mil	mil
PTH	1	2	31.50	40.98	22	0	13.51		>32.00
NPTH	1	1	125.00	125.00	2	0	>32.00		>64.00
Total	2	3	31.50	125.00	24	0	13.51		>64.00

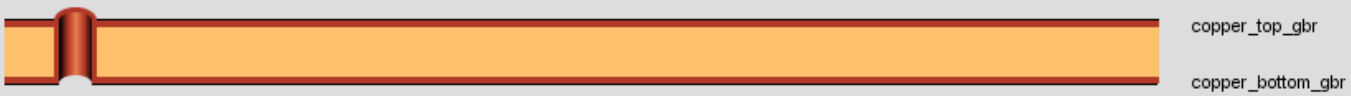
The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.

Name	dhd16ni2.zip	Id.	41561 - QED OK
Report Generated on	Apr 26, 2020 12:21:53 PM	Customer	InstantDFM
Board Id			

Summary Minimum Design Characteristics - Locations - Original



Stackup - Original



Pressing Stages	1

Copper Layer Minima & Area - Original

File	Pos.	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Same Net Clr.	Copper Area	
		mil	mil	mil	mil	mil	mil	inch^2	%
copper_top_gbr	1	>16.00	>16.00	>16.00	>16.00	20.00	>20.00	0.3922	17
copper_bottom_gbr	2	>16.00	>16.00	>16.00	>16.00	>32.00	>20.00	0.2033	9

The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.

Name	dhd16ni2.zip	Id.	41561 - QED OK
Report Generated on	Apr 26, 2020 12:21:53 PM	Customer	InstantDFM
Board Id			

Copper Layer Minima - Copper vs Drill - Original

File	Pos.	Ring					Copper vs Drill Clr.		Copper to Outline Clr.			
		Overall	Via	Laser Via	Comp.	Mech.	Plated	NPTH	Overall	Pad to Outline	Track to Outline	Region to Outline
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
copper_top_gbr	1	13.51			13.51		>32.00	>64.00	40.00	>64.00	40.00	>64.00
copper_bottom_gbr	2	13.51			13.51		>32.00	>64.00	>64.00	>64.00	>64.00	>64.00

Drill Tools - Original

File	Tool Nr.	Span	Type	Method	Filled Via	Counter	Dia.	Tol. Min	Tol. Plus	Holes (in PCB)	Moves (in PCB)	Double Hits (in File)	Predrill Hits (in File)
							mil	mil	mil				
drills_xln	1	1-2	NPTH	unknown	unknown	unknown	125.00	0.00	0.00	2	0	0	0
drills_xln	2	1-2	PTH	unknown	unknown	unknown	40.98	0.00	0.00	8	0	0	0
drills_xln	3	1-2	PTH	unknown	unknown	unknown	31.50	0.00	0.00	14	0	0	0

Drill Tools - Drill vs Copper - Original

File	Tool Nr.	Span	Type	Method	Dia.	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
					mil	mil	mil	mil
drills_xln	1	1-2	NPTH	unknown	125.00	>32.00		
drills_xln	2	1-2	PTH	unknown	40.98	13.51		68.00
drills_xln	3	1-2	PTH	unknown	31.50	17.72		66.94

Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.	Min. Hole to Outline Clr.	Min. Slot to Outline Clr.
			mil	mil		mil	mil	mil	mil	mil
1-2	PTH	2	31.50	40.98	22	13.51		>32.00	127.56	disabled
1-2	NPTH	1	125.00	125.00	2	>32.00		>64.00	134.35	disabled
All	All	3	31.50	125.00	24	13.51		>64.00	127.56	disabled

Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.

Name	dhd16ni2.zip	Id.	41561 - QED OK
Report Generated on	Apr 26, 2020 12:21:53 PM	Customer	InstantDFM
Board Id			

Solder Mask - Original

Side	Min. Mask to Mask Clr.	Min. Web	Min. Ring on Cu Defined Pads	Min. Ring on SM Defined Pads	Min. Mask to Copper Clr.	Fully Covered Via Holes	Partly Covered Via Holes	Half Mask Vias
	mil	mil	mil	mil	mil			
Top	>10.00	>10.00	4.00	>10.00	>10.00	No	No	
Bottom	>10.00	>10.00	4.00	>10.00	>10.00	No	No	
Both	>10.00	>10.00	4.00	>10.00	>10.00	No	No	No

Files - Original

Initial	Renamed	Format	Function	Position	Color
solderpaste_top.gbr	solderpaste_top_gbr	ger274x	paste	top	
silkscreen_top.gbr	silkscreen_top_gbr	ger274x	silk	top	white
soldermask_top.gbr	soldermask_top_gbr	ger274x	mask	top	green
copper_top.gbr	copper_top_gbr	ger274x	outer	1	
copper_bottom.gbr	copper_bottom_gbr	ger274x	outer	2	
soldermask_bottom.gbr	soldermask_bottom_gbr	ger274x	mask	bottom	green
drills.xln	drills_xln	excellon2	mixed	1-2	
profile.gbr	profile_gbr	ger274x	cad_outline	none	
silkscreen_bottom.gbr	silkscreen_bottom_gbr	ger274x	empty	none	
solderpaste_bottom.gbr	solderpaste_bottom_gbr	ger274x	empty	none	
gerber_job.gbrjob		text	document		

Comments - Original

The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.