

EXTENDS *Integers*

VARIABLES

turn, This is one of the three items that mutate in *Peterson's* algorithm. It is what keeps track of which process's turn it is to access the shared resource

processState, This is a function mapping each process (represented by an integer 0 or 1) to its current state (a string).

flag This contains the remaining two of the three items that mutate in *Peterson's* algorithm. It is a function mapping each process (represented by an integer 0 or 1) to a boolean flag indicating whether the process would like to access the shared resource.

vars $\triangleq \langle turn, processState, flag \rangle$ This is simply a convenient grouping of variables to use later on when defining temporal properties to save on tedious typing.

TypeOK \triangleq
 $\wedge \forall p \in \{0, 1\} : flag[p] \in \{TRUE, FALSE\}$
 $\wedge turn \in \{0, 1\}$
 $\wedge \forall p \in \{0, 1\} : processState[p] \in \{\text{"idle"}, \text{"sentRequest"}, \text{"waiting"}, \text{"critical"}\}$

TypeOk2 \triangleq
 $\wedge turn \in \{0, 1\}$
 $\wedge flag \in [\{0, 1\} \rightarrow \{TRUE, FALSE\}]$
 $\wedge processState \in [\{0, 1\} \rightarrow \{\text{"idle"}, \text{"sentRequest"}, \text{"waiting"}, \text{"critical"}\}]$

This is one possible initial state of *Peterson's* algorithm. It is the most "natural" one, that is the one where both processes are idle and have not yet decided to access the resource.

Init \triangleq
 $\wedge flag = [i \in \{0, 1\} \mapsto FALSE]$
 $\wedge turn \in \{0, 1\}$
 $\wedge processState = [i \in \{0, 1\} \mapsto \text{"idle"}]$

This predicate describes how a process can send a request to access a resource, which essentially amounts to setting its request flag to true.

ProcessRequestFlag(*p*) \triangleq
 $\wedge processState[p] = \text{"idle"}$
 $\wedge flag' = [flag \text{ EXCEPT } ![p] = TRUE]$
 $\wedge processState' = [processState \text{ EXCEPT } ![p] = \text{"sentRequest"}]$
 $\wedge \text{UNCHANGED } \langle turn \rangle$

ProcessBeginWaiting(*p*) \triangleq
 $\wedge processState[p] = \text{"sentRequest"}$
 $\wedge turn' = 1 - p$
 $\wedge processState' = [processState \text{ EXCEPT } ![p] = \text{"waiting"}]$
 $\wedge \text{UNCHANGED } \langle flag \rangle$

$$\begin{aligned}
\text{ProcessEnterCritical}(p) &\triangleq \\
&\wedge \text{processState}[p] = \text{"waiting"} \\
&\wedge (\text{flag}[(1-p)] = \text{FALSE} \vee \text{turn} = p) \\
&\wedge \text{processState}' = [\text{processState} \text{ EXCEPT } ![p] = \text{"critical"}] \\
&\wedge \text{UNCHANGED } \langle \text{flag}, \text{turn} \rangle
\end{aligned}$$

$$\begin{aligned}
\text{ProcessExitCritical}(p) &\triangleq \\
&\wedge \text{processState}[p] = \text{"critical"} \\
&\wedge \text{processState}' = [\text{processState} \text{ EXCEPT } ![p] = \text{"idle"}] \\
&\wedge \text{flag}' = [\text{flag} \text{ EXCEPT } ![p] = \text{FALSE}] \\
&\wedge \text{UNCHANGED } \langle \text{turn} \rangle
\end{aligned}$$

$$\begin{aligned}
\text{Next} &\triangleq \\
&\exists p \in \{0, 1\} : \\
&\quad \vee \text{ProcessRequestFlag}(p) \\
&\quad \vee \text{ProcessBeginWaiting}(p) \\
&\quad \vee \text{ProcessEnterCritical}(p) \\
&\quad \vee \text{ProcessExitCritical}(p)
\end{aligned}$$

$$\text{Spec} \triangleq \text{Init} \wedge \Box[\text{Next}]_{\text{vars}}$$

$$\text{SpecWithFairness} \triangleq \text{Spec} \wedge \text{WF}_{\text{vars}}(\text{Next}) \wedge \forall p \in \{0, 1\} : \text{WF}_{\text{vars}}(\text{ProcessRequestFlag}(p))$$

$$\text{MutualExclusion} \triangleq \neg(\text{processState}[0] = \text{"critical"} \wedge \text{processState}[1] = \text{"critical"})$$

$$\text{THEOREM } \text{Spec} \Rightarrow \Box \text{MutualExclusion}$$

This is a basic liveness requirement that corresponds to what is called “Progress” in the *Wikipedia* article. Both processes should eventually be able to enter their critical sections.

$$\text{WillEventuallyEnterCritical} \triangleq \Diamond(\text{processState}[0] = \text{"critical"}) \wedge \Diamond(\text{processState}[1] = \text{"critical"})$$

$$\text{THEOREM } \text{SpecWithFairness} \Rightarrow \text{WillEventuallyEnterCritical}$$

THIS INVARIANT DOES NOT *HOLD* AND SHOULD NOT *HOLD*! It’s merely instructive of something a reader may intuitively believe about this algorithm that turns out to be false.

See the note in *ProcessEnterCritical*.

$$\text{CanOnlyBeCriticalIfTurn} \triangleq \forall p \in \{0, 1\} : \text{processState}[p] = \text{"critical"} \Rightarrow \text{turn} = p$$

Finally we note simply that our variables should always stay within the bounds we enumerated earlier.

$$\text{THEOREM } \text{Spec} \Rightarrow \Box \text{TypeOK}$$

$$\begin{aligned}
\text{Inv} &\triangleq \wedge \text{TypeOK} \\
&\wedge \forall p, q \in \{0, 1\} : \text{processState}[p] = \text{"critical"} \Rightarrow (\text{flag}[q] = \text{FALSE} \vee \text{turn} = p) \\
&\quad \wedge \text{MutualExclusion}
\end{aligned}$$

$$\text{Inv2} \triangleq \forall p, q : \text{processState}[p] \neq \text{"critical"}$$

$$\text{Inv3} \triangleq \neg \exists p : \text{processState}[p] = \text{"critical"}$$

THEOREM $InitProperty \triangleq Init \Rightarrow Inv$
 BY DEF $Init, Inv, ProcessRequestFlag, ProcessBeginWaiting, ProcessEnterCritical, ProcessExitCritical, T$

THEOREM $processState = [i \in \{0, 1\} \mapsto \text{"idle"}] \Rightarrow Inv$
 ???

THEOREM $InductiveProperty \triangleq Inv \wedge Next \Rightarrow Inv'$
 BY DEF $Inv, Next, MutualExclusion, TypeOK$

$SpecIndCheck \triangleq TypeOk2 \wedge Inv \wedge \Box[Next]_{vars}$

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