

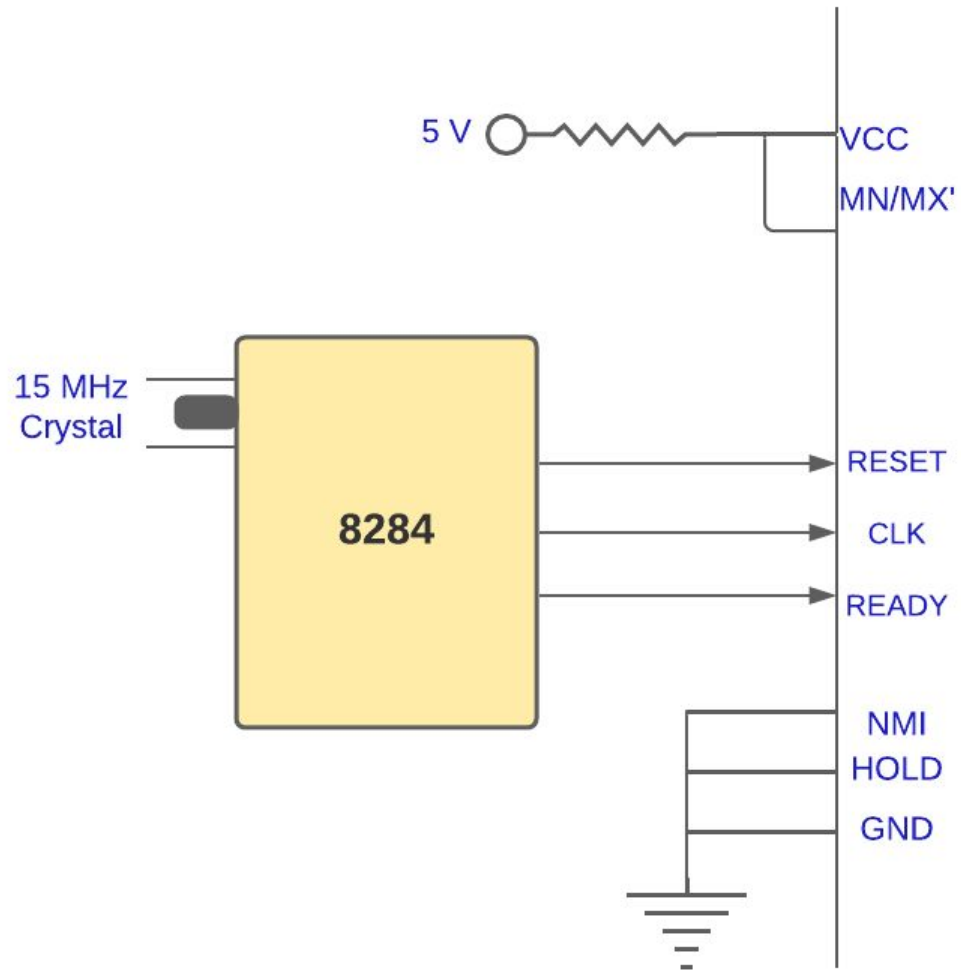


BITS Pilani  
K K Birla Goa Campus

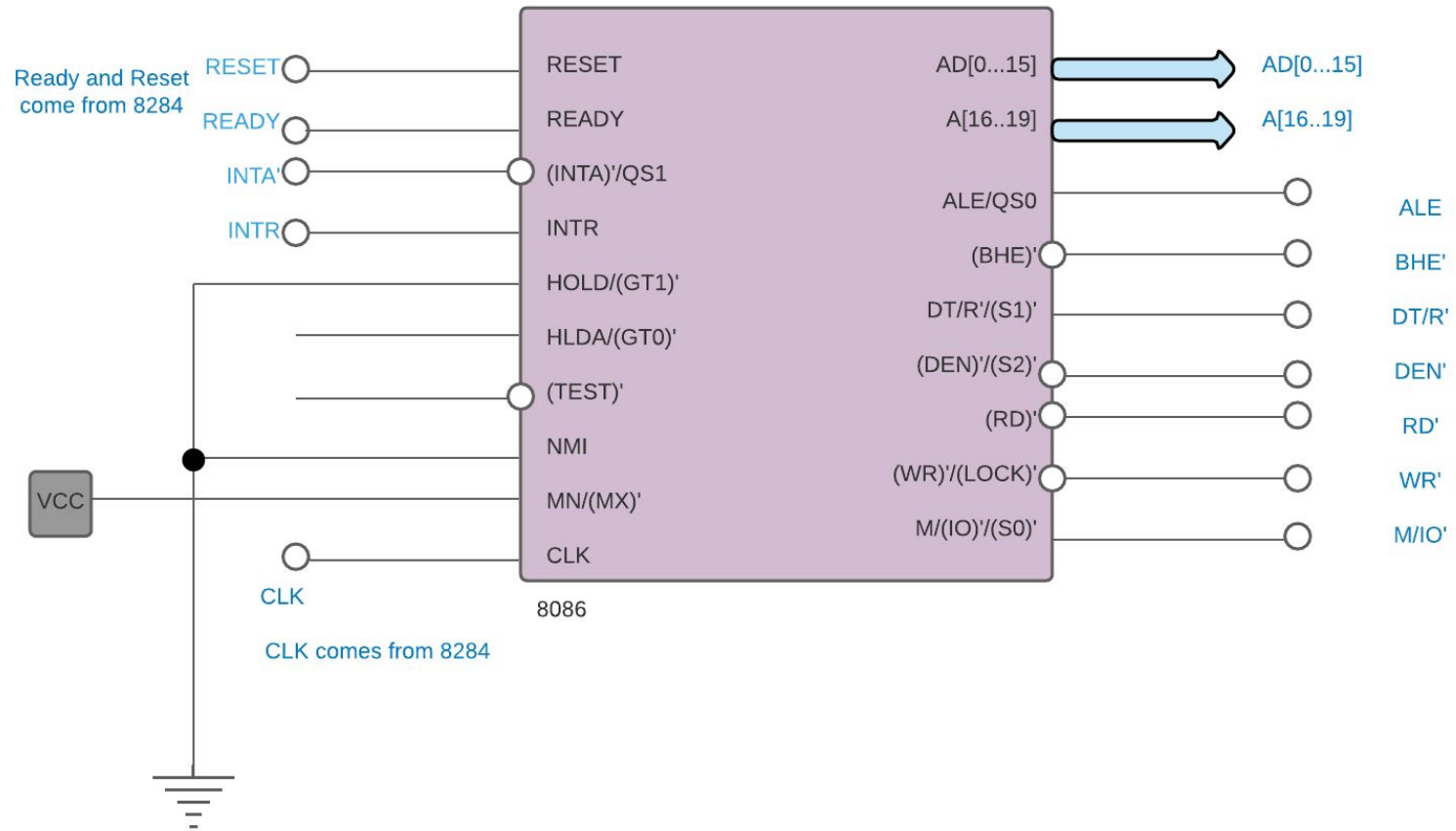
## Hardware Design

### “CHOCOLATE VENDING MACHINE”

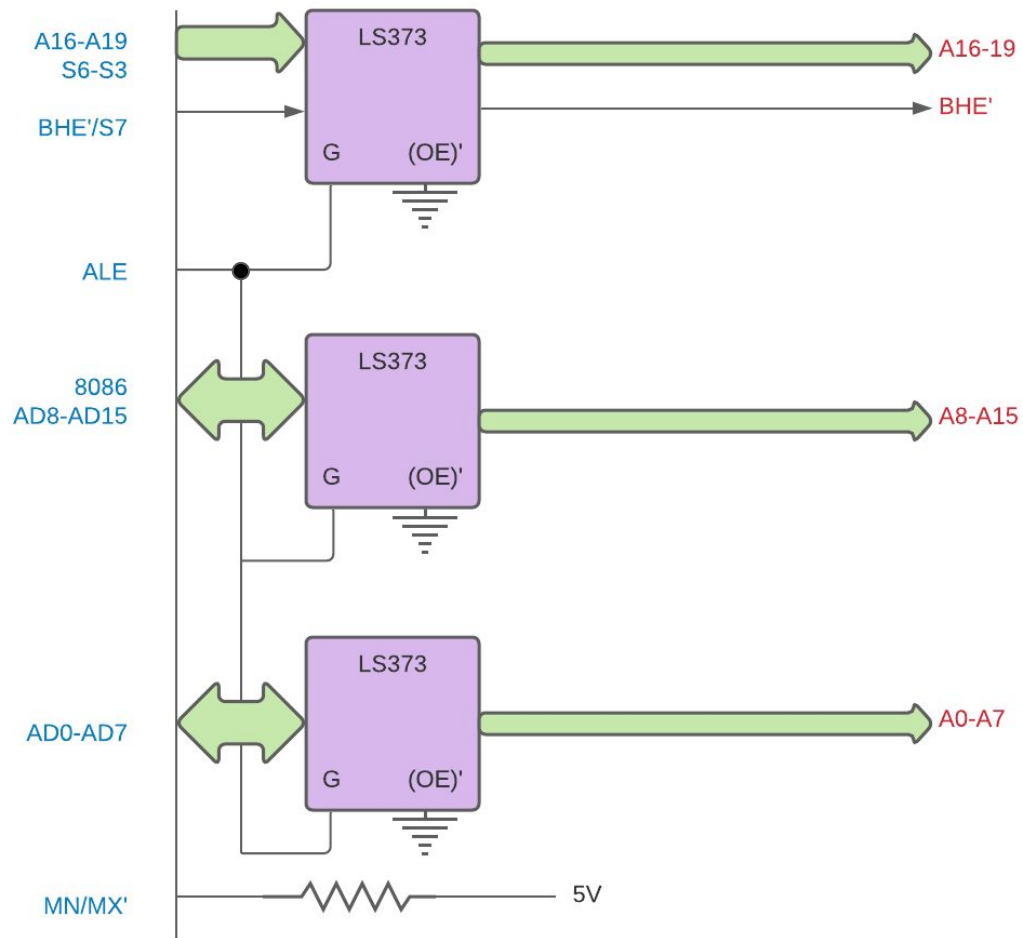
Group 58



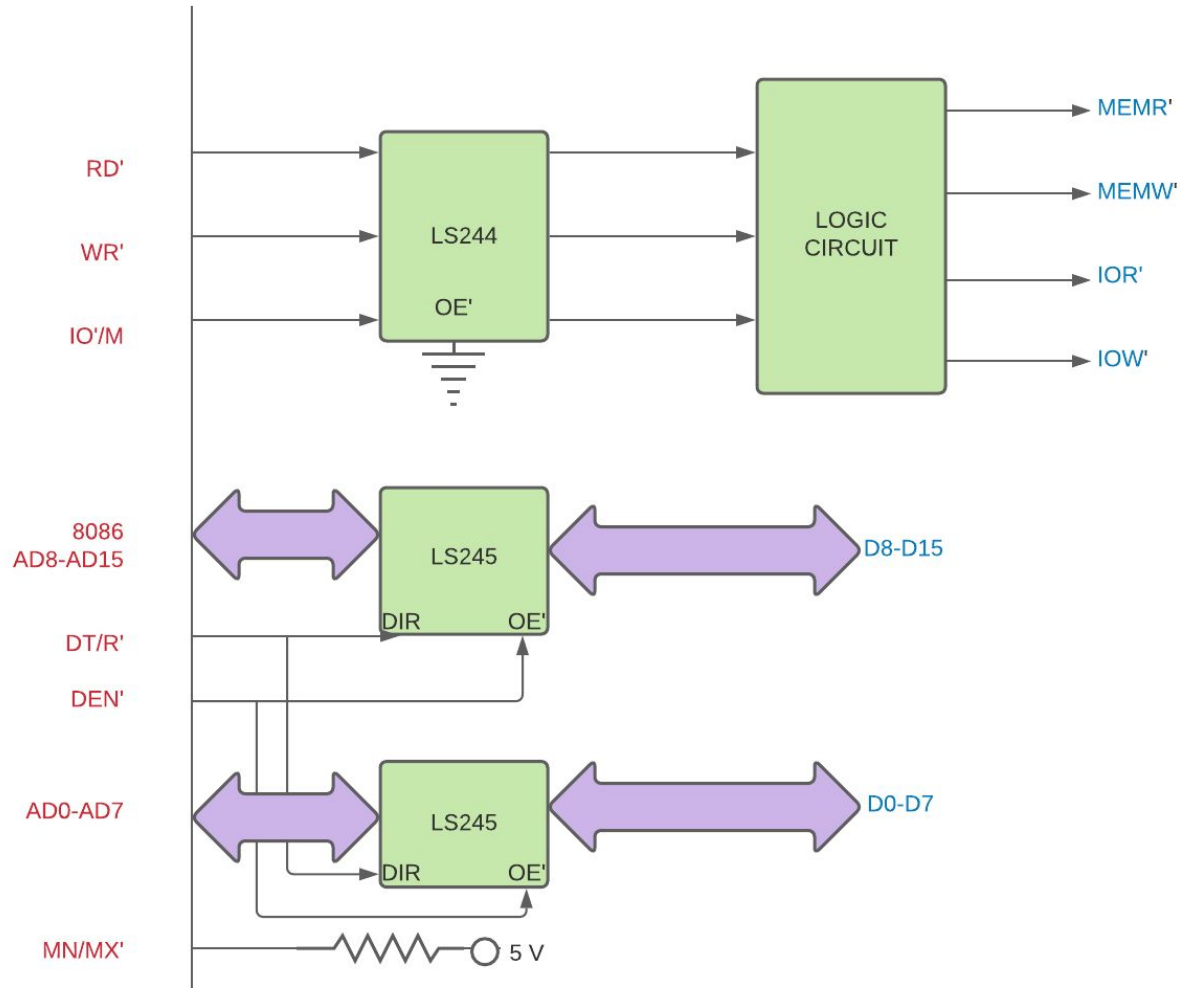
**8284**



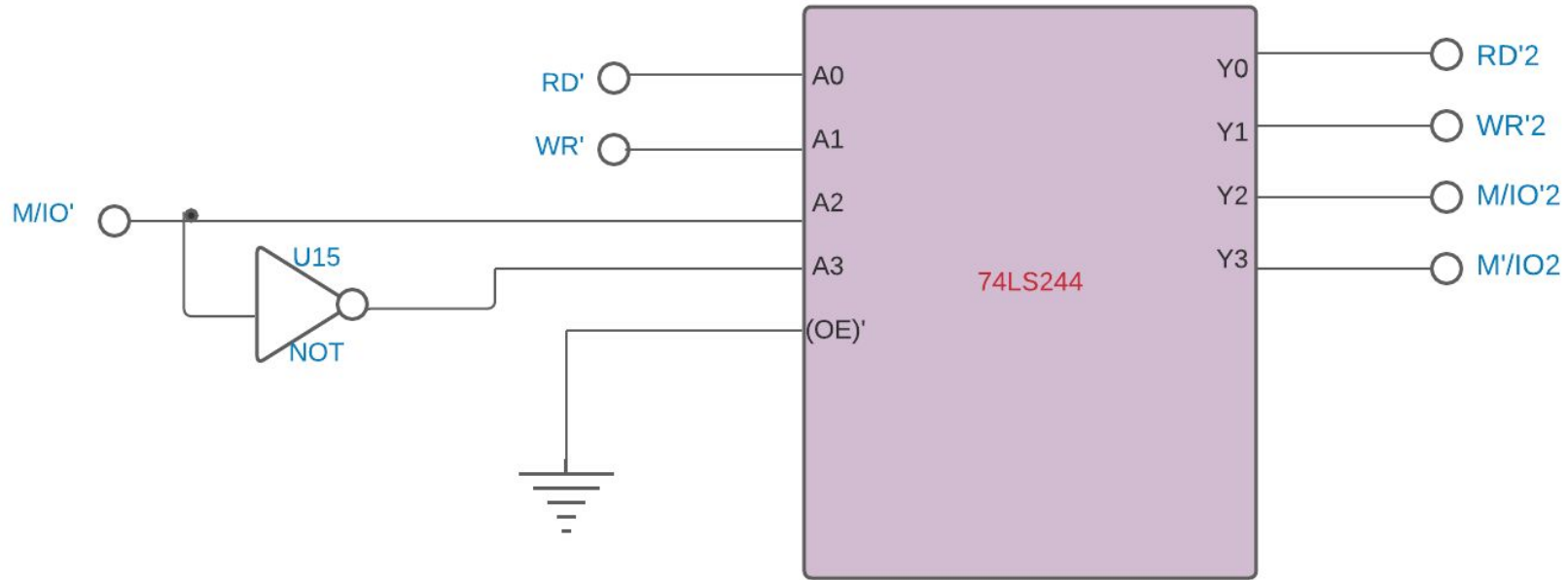
## 8086 INPUTS



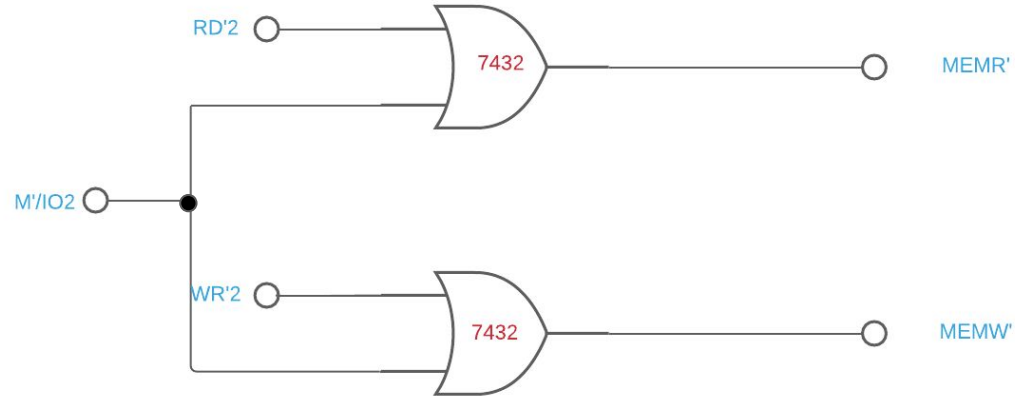
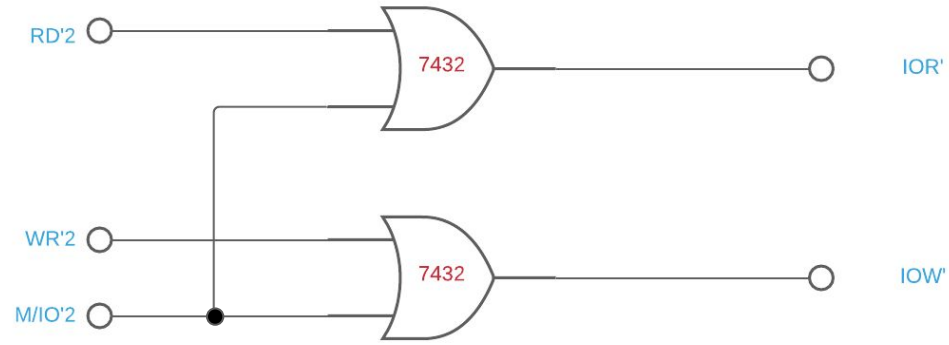
**ADDRESS BUS**



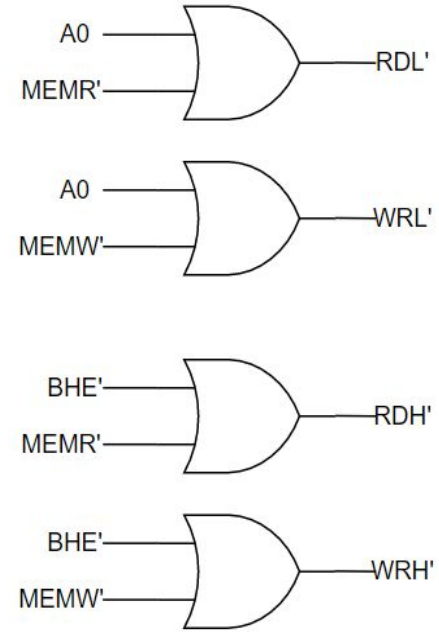
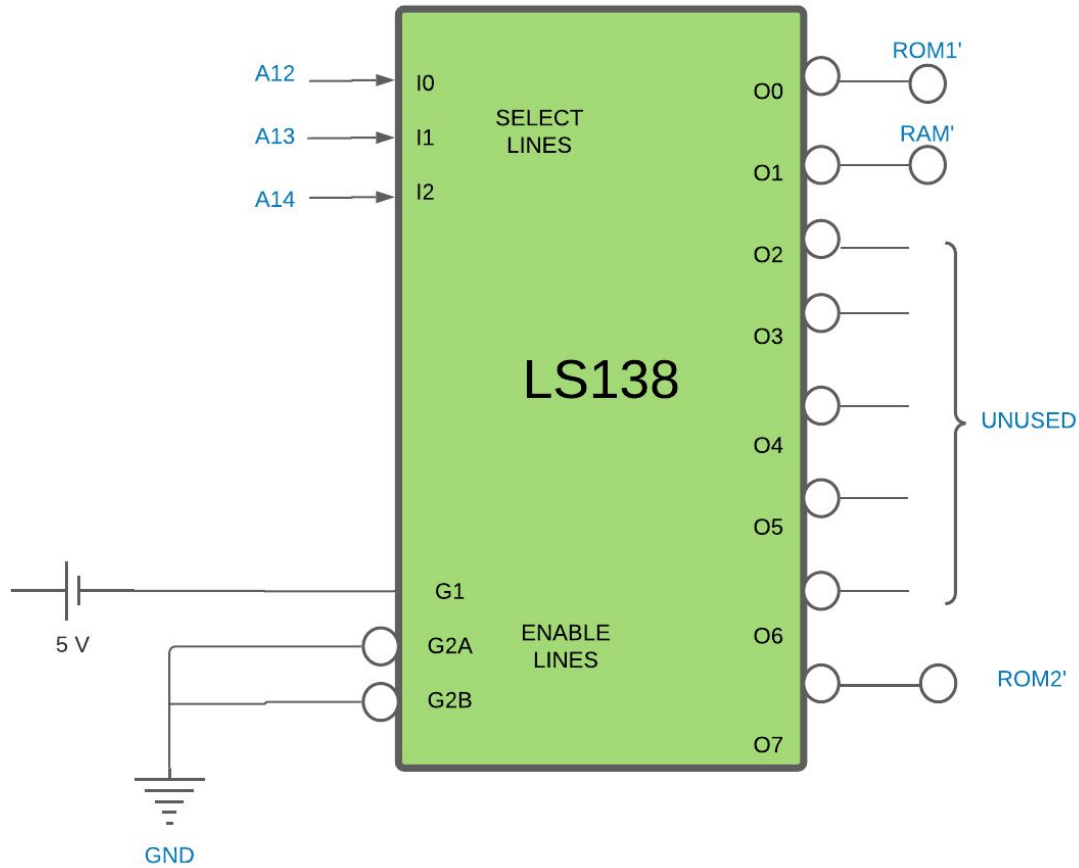
**DATA +  
CONTROL  
BUS**



## CONTROL SIGNAL GENERATION

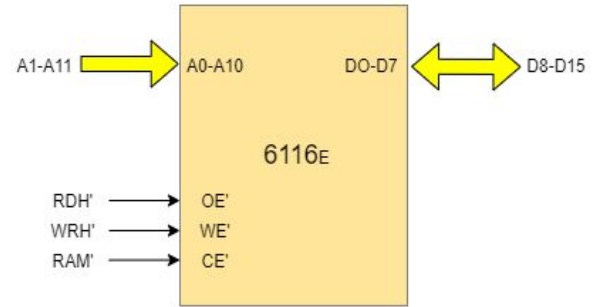
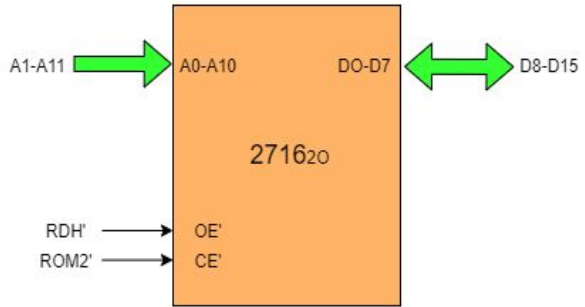
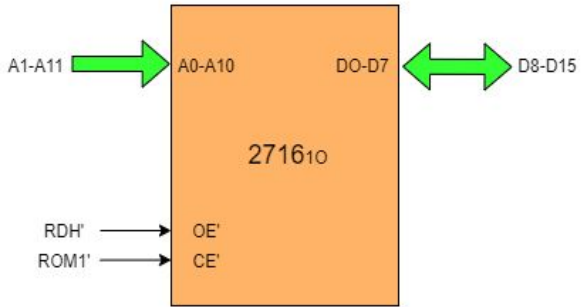
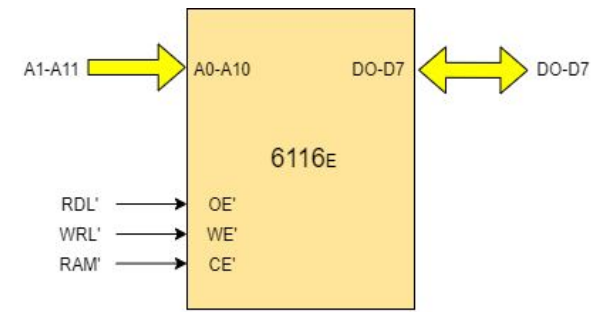
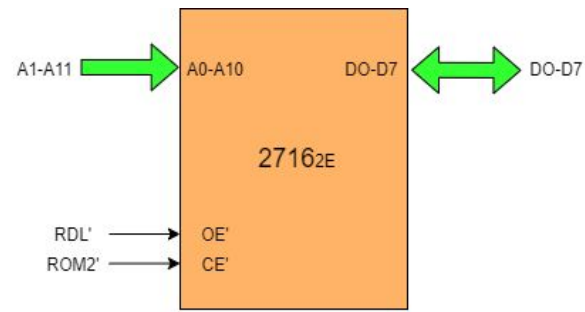
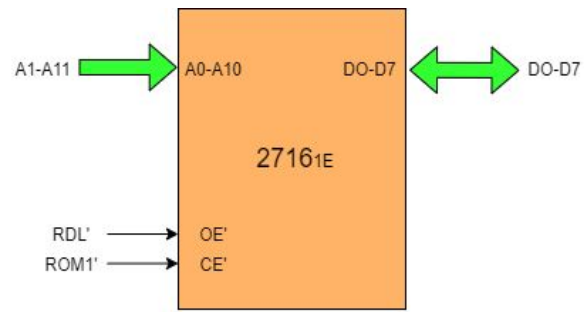


## CONTROL SIGNAL GENERATION

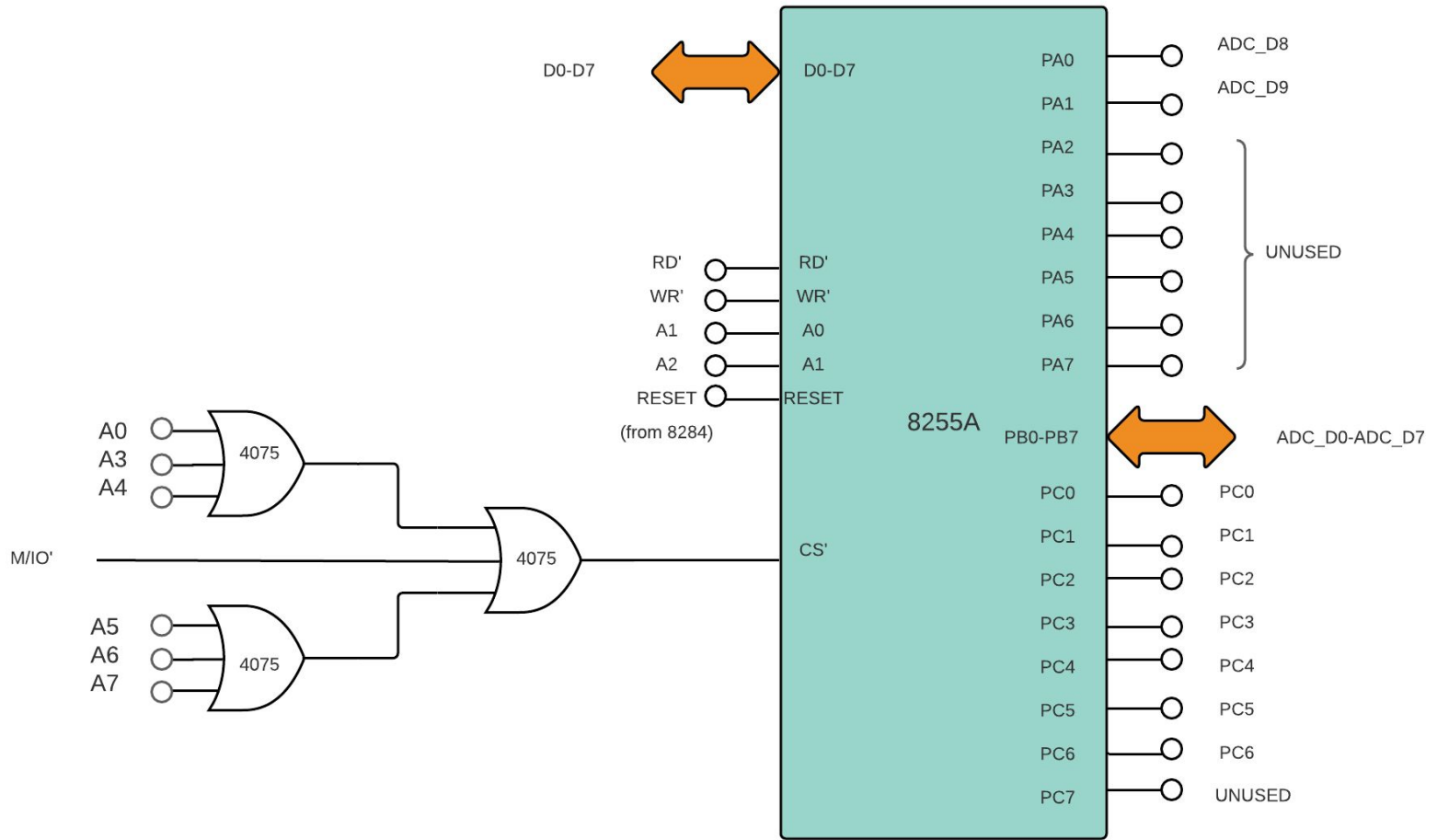


## MEMORY INTERFACING

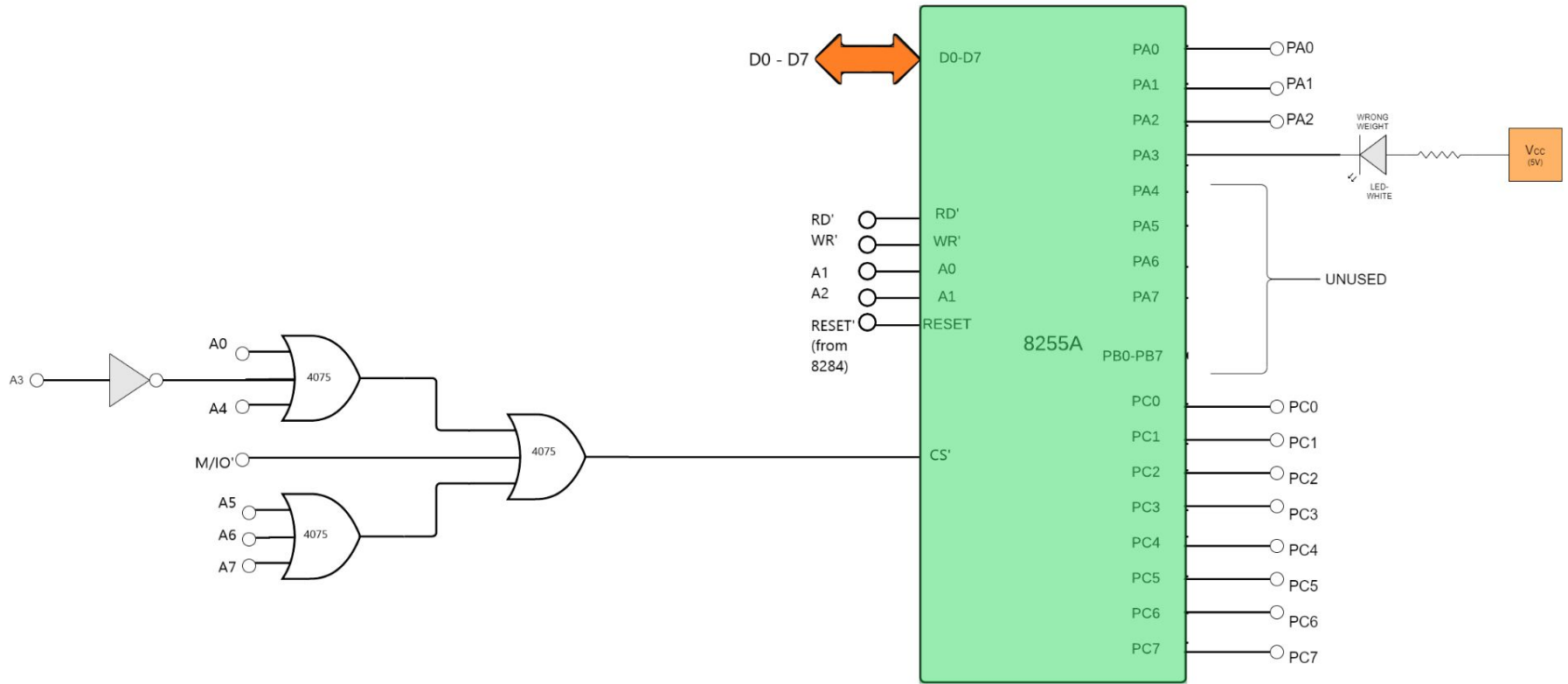




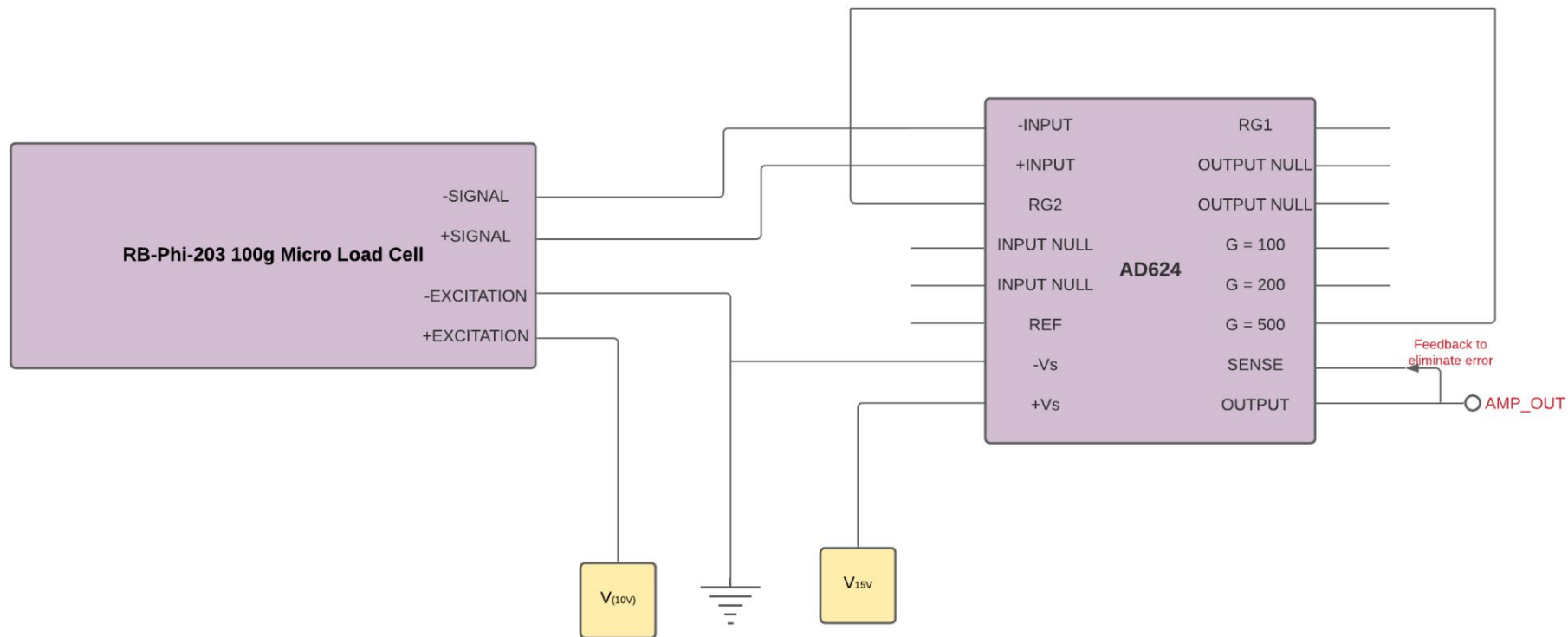
## MEMORY INTERFACING



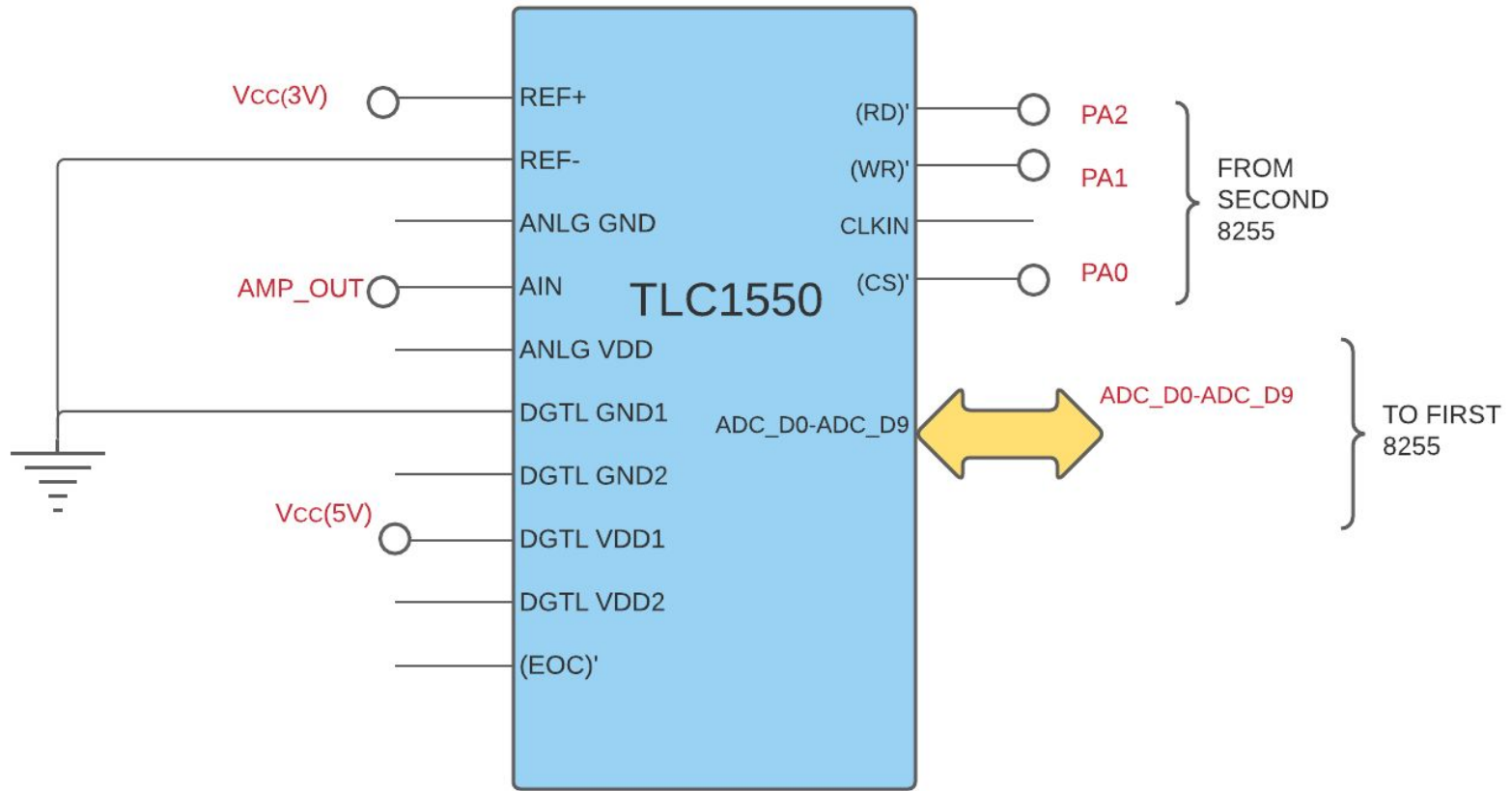
**8255A\_A**



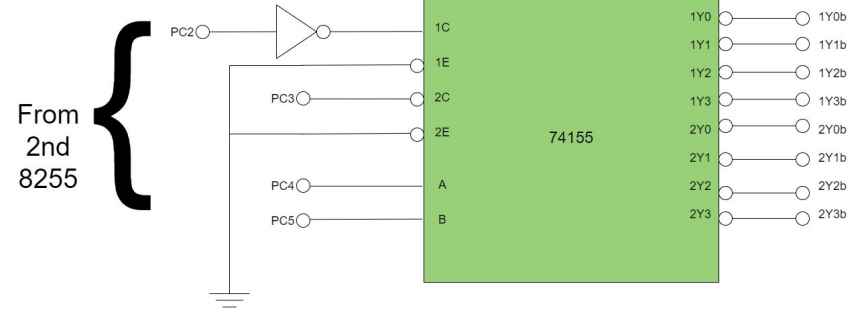
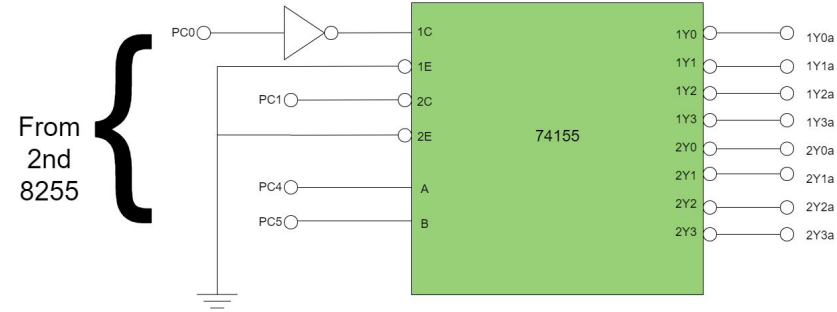
**8255A\_B +LED**



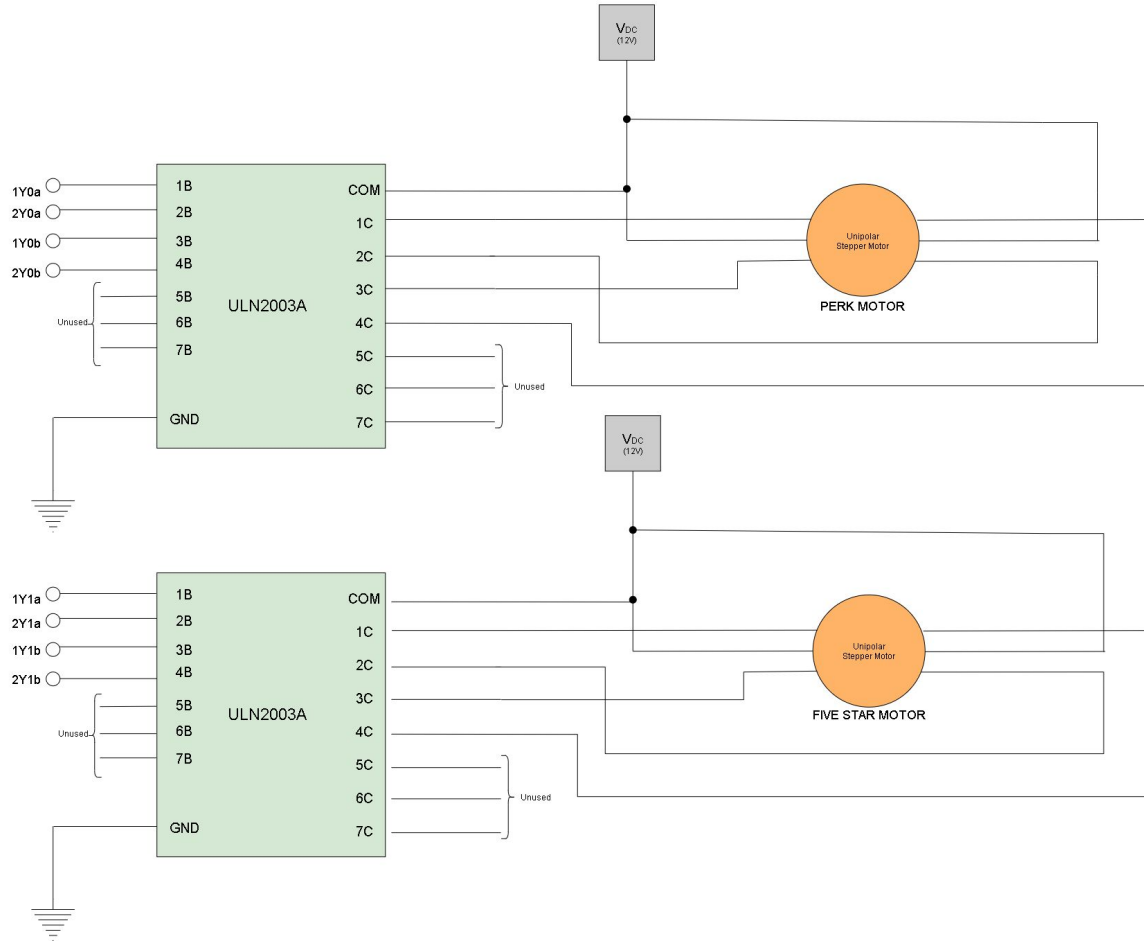
**WEIGHT SENSOR**



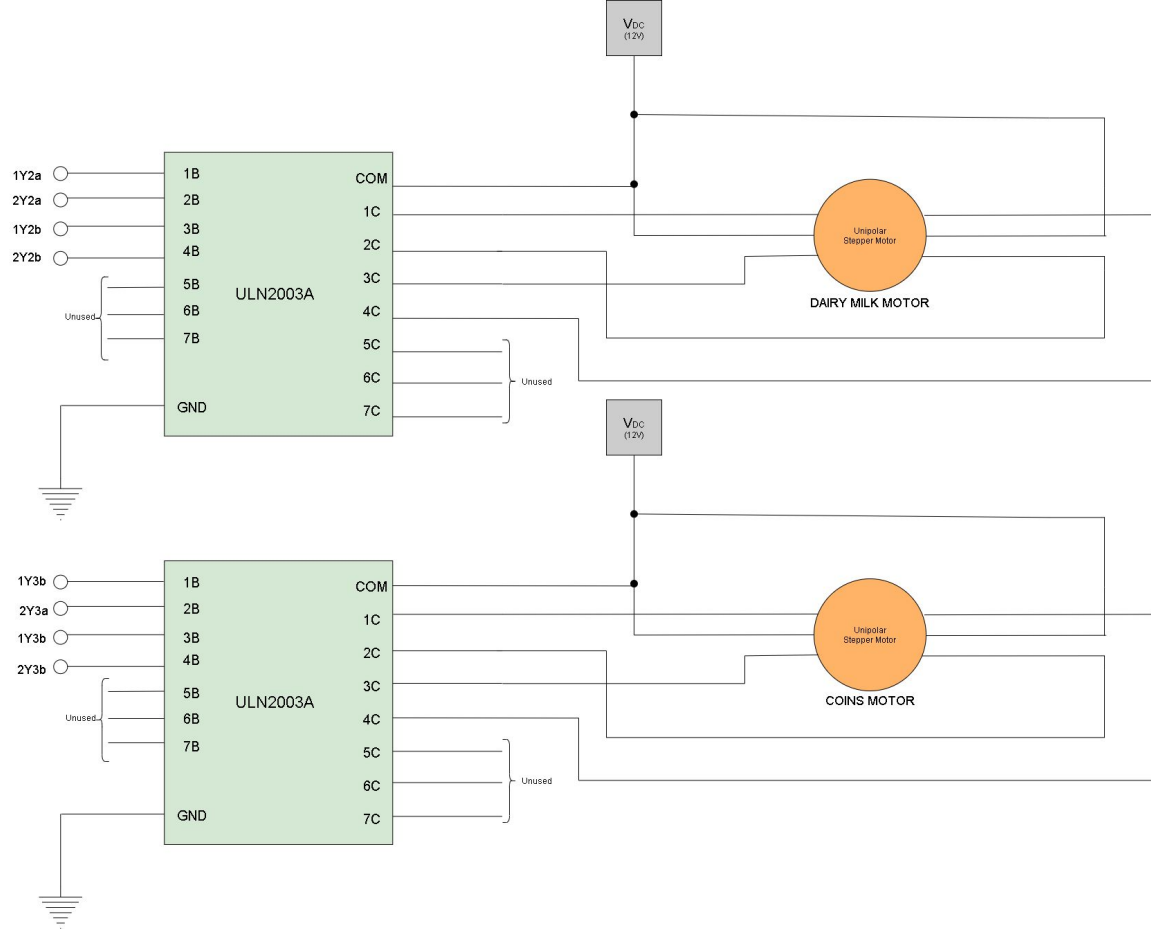
**ADC**



## STEPPER MOTOR INTERFACING LOGIC

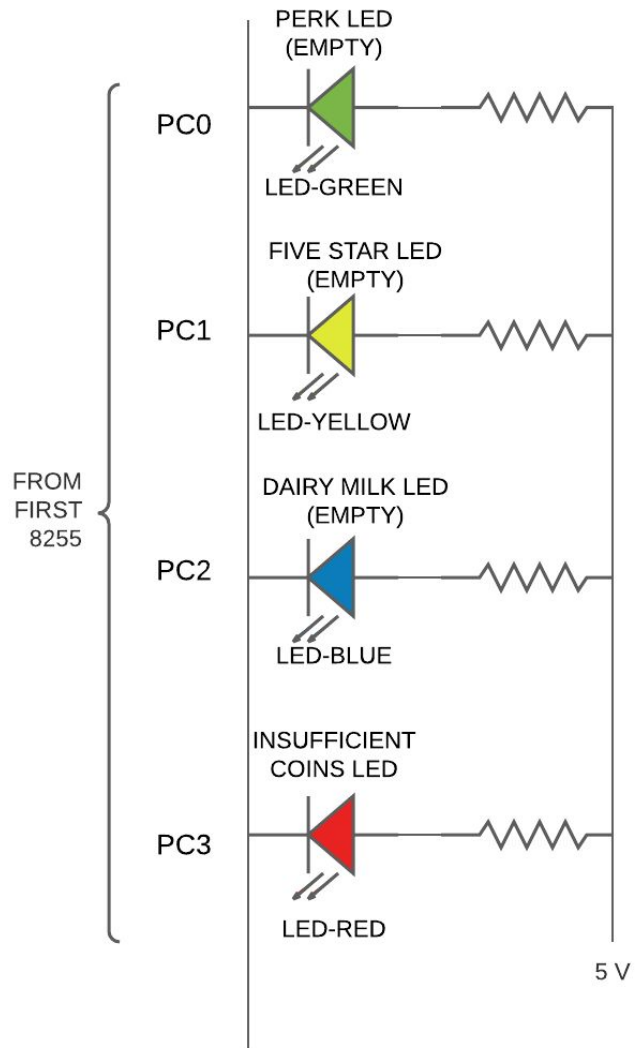


# STEPPER MOTORS AND DRIVERS 1

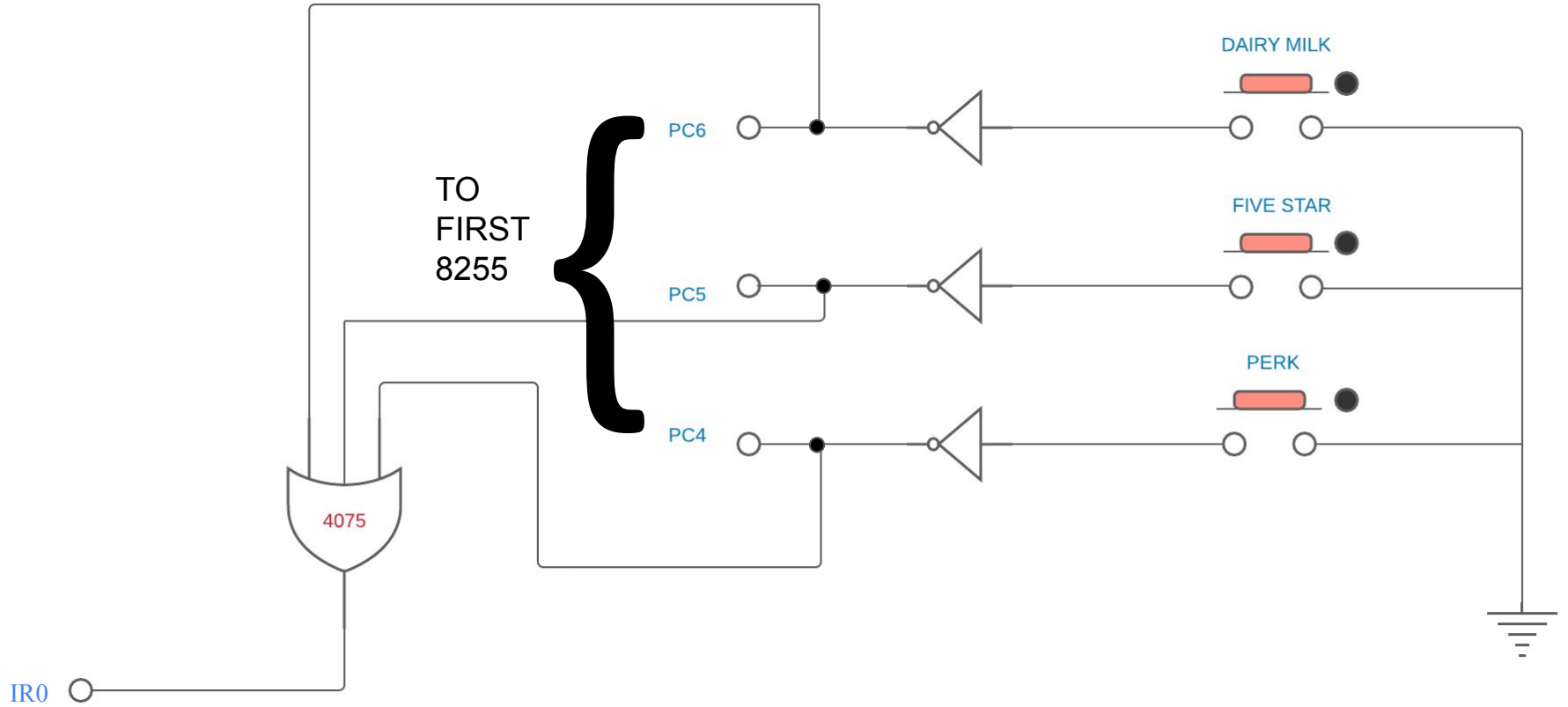


## STEPPER MOTORS AND DRIVERS 2

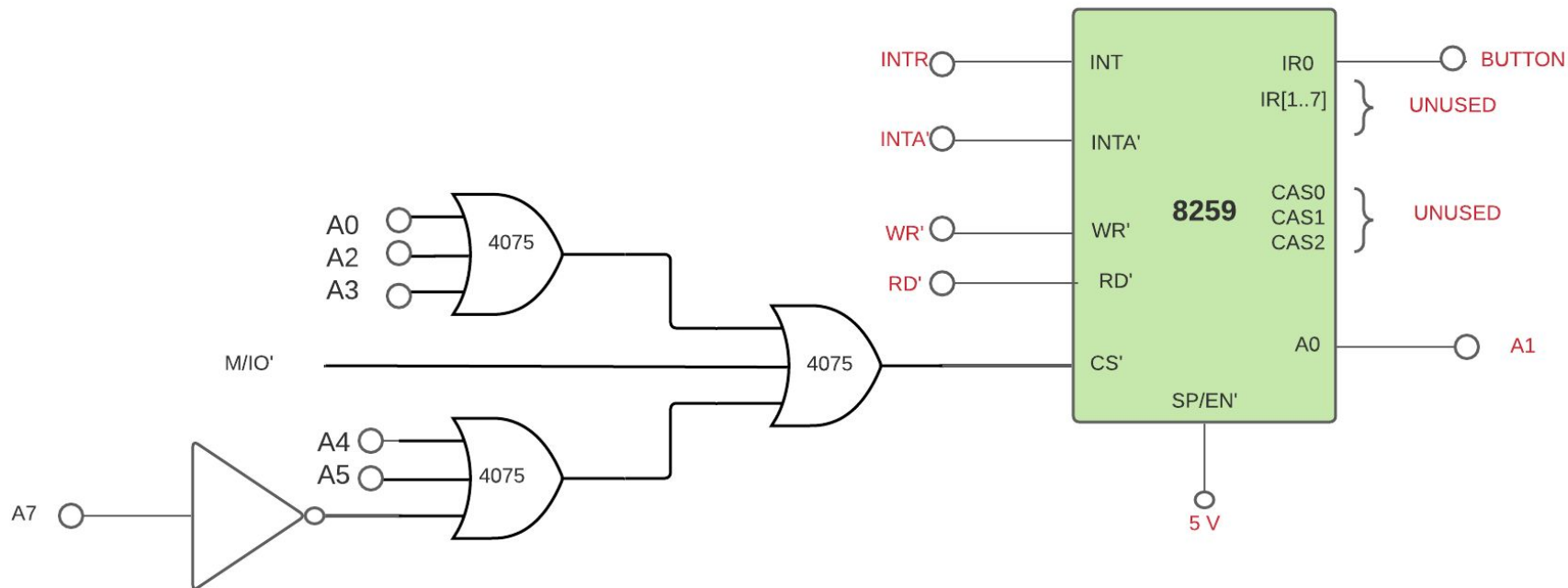




## LEDs (First 4)



**BUTTONS**



**8259**