jatin singh shekhawat

BK Birla Institute of Engineering Technology

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ACADEMIC DETAILS

• BK BIRLA INSTITUTE OF ENGINEERING AND TECHNOLOGY, PILANI

• B Tech ECE (8.21 CGPA)

• Star Academy, Jhunjhunu [2021-22]

Senior Secondary (RBSE)

• STAR ACADEMY, JHUNJHUNU [2019-20]

Matriculation (RBSE)

FIELDS OF INTEREST

• Chip designing, VLSI Design and Verification, Internet of Things (IoT), Hardware Architecture Design

TECHNICAL SKILLS

- **Programming language** (C, Python, TCL).
- VLSI Design Languages (Verilog, VHDL, SystemVerilog).
- Design an Simulation Tools (ModelSim, Xilinx, TCLsh, EDA Playground, Visual Studio Code).
- Software for Projects (Inkscape, Universal Gcode Sender, Drawbot).
- Miscellaneous (IOTs).

CERTIFICATES

- Successfully completed a 45-day training program on "Advanced Digital Design using HDL." Gained practical experience in digital circuit design and received a certification for the course
- Successfully achieved proficiency in the Introduction to the Internet of Things and Embedded Systems through an online course on Coursera.
 - o view certificate

MAJOR PROJECTS AND SEMINAR

CloudPen

(Cloud-IoT Integrated Project, jan'27 - till date)

- Designed and built a handwriting machine using Arduino, ESP8266, stepper motors, servo motors, and 3D-printed components.
- o Developed firmware for Arduino to interpret G-Code instructions and control the writing mechanism.
- o Integrated ESP8266 Wi-Fi module to fetch G-Code files from AWS S3 and relay them to Arduino.
- Collaborated with a software developer to enable cloud-to-hardware communication via AWS IoT Core.
- o Tested and calibrated the system for accuracy, reliability, and real-time handwriting execution.

TRAINING AND INTERNSHIPS

- In-House Summer Internship BK Birla Institute of Engineering Technology (12th-24th June 2023)
 - Successfully completed a summer internship organized by the Department of Applied Sciences Humanities.
 - o Gained hands-on experience in relevant engineering concepts and practical applications

- view certificate
- Advanced Digital Design using HDL FutureWiz (7th June 2024 2nd August 2024)
 - $\circ\,$ Completed 45-day training focused on HDL-based digital design.
 - o Gained hands-on experience in synthesis, simulation, and verification techniques.
 - o view certificate

STRENGTHS

• Social Interaction, Hardworking.

INTEREST AND HOBBIES

- Solving Puzzles.
- Playing Badminton