KV 2134 (Q1) A A [o] X5 £ ×8 gx9 BTO] X6 C [O] X7 @ B[g] = A[:-5A[32] +32] IN XII, 128 (X5) / AC32) : NXII 511:35 ×11 ×11 2 // ×11 \*4 add XII, XII, XII // XII + XII 9ddi x12, x10, 32 Sub X13, X 12, X11 SII; x13, x13, 2 IW x 14, +13(x5) SII; 847 X 15 , X9, 2 (X6) \$100 000 (X6) Sw x14, x15(x6) (1) f = 9 - A [B[c[e4]] 1w ×11 356 (×7) 511; X11, X11, 2 (w ×12, ×11 (x6) 2111 × 15 × 15, 115 1~ × 13, ×12(x5)

kv2154 50 b ×14, ×9, ×13 Sw x14, x8 (i) F = 9 - A[C[16] + B[32]] 1W XII, 64 (X7) 1W X 12, 128 (X 6) dgg x 13 x 11 x 15 511; X13, X13,2 1w ×14, ×13 (x5) SUB X15, X9, X14 s~ x15, x8 (1:1) A[1] = 4B[8; -81] + 4C[321+12] 511; X11, X10, 3 addi x12, x11, -81 IN X 13, X15 (X.P) 5111 × 13, × 13, 2 SII: XII, XII, 2 9dd: x14, X11, 32 1 w x 15, x14 (x7) SIli XIS,XIS,Z dgg x 10, x 12, x13 SIII X17, X10, 2 SW X16, X17(X5)

KV2154 f05/ IRC FOR RB = 2 Clock For PB = 600 MHz By reverse bits in register 1 × 5,0×12345678 1m x6,0 1~ X7,0 1~ X8,31 100p: andi x9, x5,1 or x6, x6, x9 Srli X5, X5,1

KV2154 add: x7, x7,1 b16 x7, x8,100P mv x5, X6

1CU2154

3

IPC for A = 10

Elock rate for A = SOO MHZ

MIPS = IPC x clock rate

= 10 x 500

MIPS for A = SOOO MIPS

BIPC FOR B=2 Clock rate for B=600 MH2 MIPS for B= 1200 MIPS

O The performance between A and B cannot be compared as not enough information has been provided about the types of instructions being executed or the program that is to be run. It could be a case where B hight have CISC like behaviour those doing more tasks per instructions. But until and unless more information is provided, we cant prove any performance metrics

,	-	1
(	0	11
1	0	1
	1	)

(4) -1/4, -1/3 in TEE 754

D-1/4 Sign-1, Exponen = 2-2 = 128

2) - 1/3 Sign = -1, Exponen=2-2, 121

10111101010101010101001001100

1st Application = 80%.

other = 20%.

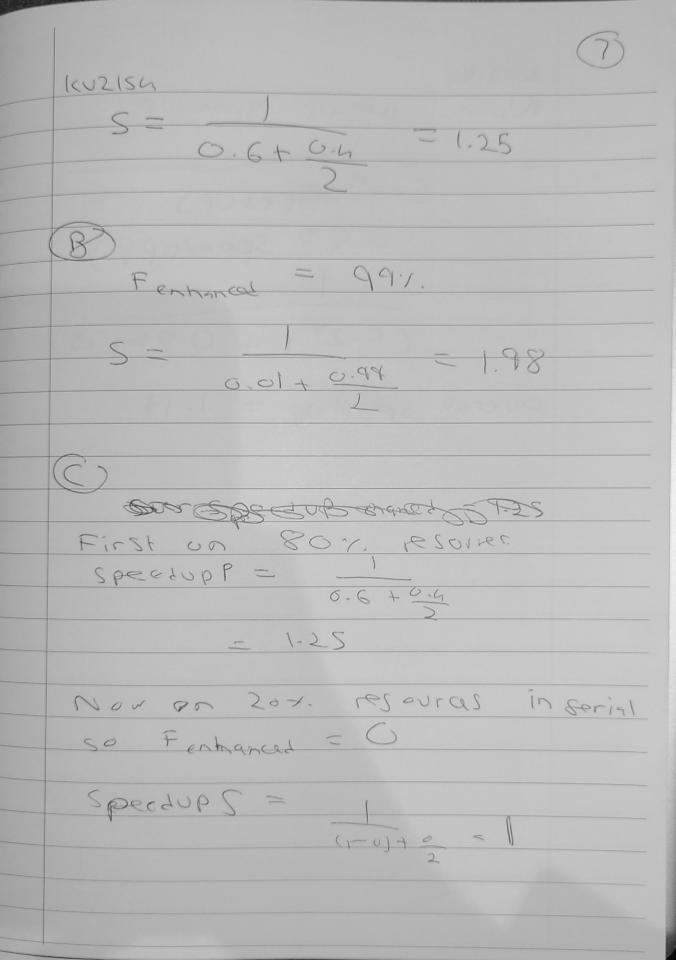
KV2154

Speed up = 5-

S- (1-Featigned + Featigned

Speedup endand)

Fend = 0.4



KU2154 Now over all speedup CO2 SPORELUPS + 0,8 & Speedupp) (0.541 + 0.841.52) OVERAIL Speed UP = 1-19