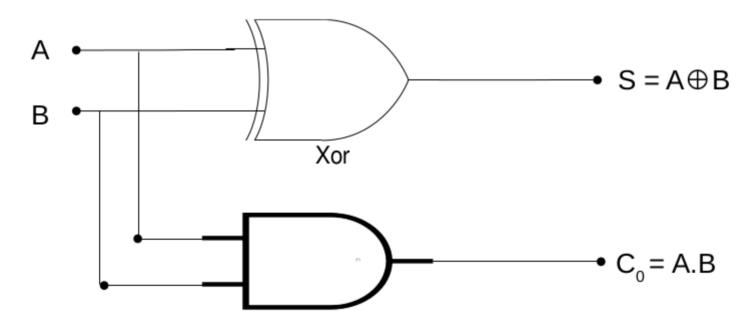
**Experiment 2:** 

Implementation of half adder & full adder

## Half Adder

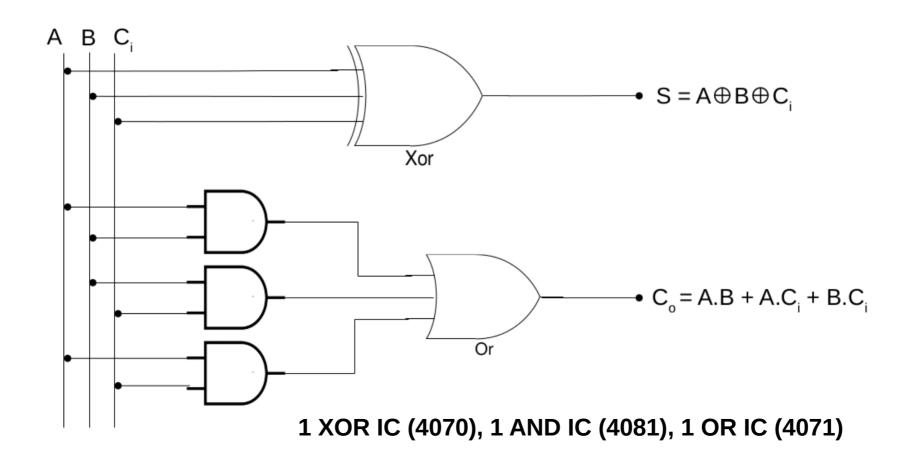


1 XOR IC (4070), 1 AND IC (4081)

# Truth Table

Α	В	S	C <sub>o</sub>
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

#### Full Adder



# Truth Table

A	В	C,	s	C <sub>o</sub>
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

## **Experiment 3:**

**Verification of mux IC: 4539B** 

#### IC 4539B

