



HP Intel Xeon Gold 5120 processor 2.2 GHz 19.25 MB L3

Brand : HP

Product code: L09260-001

Product name : Intel Xeon Gold 5120

Intel Xeon Gold 5120, 19.25M Cache, 2.2 GHz, 105 W TDP, FCLGA3647

[HP Intel Xeon Gold 5120 processor 2.2 GHz 19.25 MB L3:](#)

Options, accessories, services and support for all your HP and Compaq products.

HP Intel Xeon Gold 5120. Processor family: Intel® Xeon® Gold, Processor socket: LGA 3647 (Socket P), Processor lithography: 14 nm. Memory channels: Hexa-channel, Maximum internal memory supported by processor: 768 GB, Memory types supported by processor: DDR4-SDRAM. Market segment: Server, Supported instruction sets: AVX, AVX 2.0, AVX-512, SSE4.2, Scalability: S4S

Processor		Features	
Processor generation	1st Generation Intel® Xeon® Scalable	Scalability	S4S
Processor model *	5120	Embedded options available	✗
Processor base frequency *	2.2 GHz	Processor special features	
Processor family *	Intel® Xeon® Gold	Intel® Hyper Threading Technology (Intel® HT Technology)	✓
Processor cores *	14	Intel® Turbo Boost Technology	2.0
Processor socket *	LGA 3647 (Socket P)	Intel® AES New Instructions (Intel® AES-NI)	✓
Component for	Server/workstation	Enhanced Intel SpeedStep Technology	✓
Processor lithography *	14 nm	Intel Trusted Execution Technology	✓
Processor series	Intel Xeon Gold 5000 Series	Intel® Speed Shift Technology	✓
Processor threads	28	Intel VT-x with Extended Page Tables (EPT)	✓
Processor operating modes *	64-bit	Intel TSX-NI	✓
Processor boost frequency	3.2 GHz	Intel 64	✓
Processor cache	19.25 MB	Intel Virtualization Technology (VT-x)	✓
Processor cache type	L3	Intel Virtualization Technology for Directed I/O (VT-d)	✓
Thermal Design Power (TDP)	105 W	Intel Turbo Boost Max Technology 3.0	✗
Cooler included *	✗	Intel® Optane™ Memory Ready	✗
Stepping	M0	AVX-512 Fused Multiply-Add (FMA) units	1
Bus type	UPI	Intel® vPro™ Platform Eligibility	✓
Processor codename	Skylake	Operational conditions	
Memory		Tcase	81 °C
Maximum internal memory supported by processor	768 GB	Technical details	
Memory types supported by processor	DDR4-SDRAM	Intel® Volume Management Device (VMD) version	✓
Memory clock speeds supported by processor	2400 MHz	Mode-based Execute Control (MBE) version	✓
Memory channels *	Hexa-channel	Number of UPI links	2
ECC	✓	Features	
Graphics		Execute Disable Bit	✓
On-board graphics card *	✗	Market segment	Server
Discrete graphics card *	✗	Maximum number of PCI Express lanes	48
Features		PCI Express slots version	3.0
Execute Disable Bit	✓	Supported instruction sets	AVX, AVX 2.0, AVX-512, SSE4.2
Market segment	Server		
Maximum number of PCI Express lanes	48		
PCI Express slots version	3.0		
Supported instruction sets	AVX, AVX 2.0, AVX-512, SSE4.2		

Disclaimer. The information published here (the "Information") is based on sources that can be considered reliable, typically the manufacturer, but this Information is provided "AS IS" and without guarantee of correctness or completeness. The Information is only indicative and can be changed at any time without notification. No rights can be based on the Information. Suppliers or aggregators of this Information do not accept any liability with regard to the content of (web)pages and other documents, including its Information. The publisher of the Information can not be held liable for the content of 3rd party websites that are linking this Information or are linked to from this Information. You as the User of the Information are solely responsible for the choice and usage of this Information. You are not entitled to transfer, copy or otherwise multiply or distribute the Information. You are obliged to follow the directions of the copyright owner(s) with regard to the use of the Information. Exclusively Dutch law is applicable. With regard to price and stock data on the site, the publisher followed a number of starting points, which are not necessarily relevant for your private or business circumstances. Therefore, the price and stock data are only indicative and are subject to changes. You are personally responsible for the way you use and apply this information. As a user of the Information or sites or documents in which this Information is included, you will adhere to standard fair use including avoidance of spamming, ripping, intellectual-property violations, privacy violations, and any other illegal activity.

Publication date: 26-JUL-2025. Prints or copies of Information are only valid on the printed Publication date