

Report #No. of Gates

* Analyze gates of each Block:

Block	NOT	AND	OR	XOR	XNOR	Mux
* 1-Bit Comparator	3	2	1			
* 4-Bit Comparator	12	23	10			
* 5-Bit Comparator	15	31	13			
* Half Adder		1		1		
* Full Adder		2	1	2		
* 4-Bit Adder		8	5	8		
Final Adder	15	45	19	16		
* Full Subtractor	1	3	2	2		
* 4-Bit Subtractor	4	12	8	8		
Final Subtraction	12	35	15	12	1	
Final Multiplication		40	15	25		
Final Division	20	48	32	32		9
* Binary To BCD (4-Bit)	4	12	7			
* Binary To BCD (8-Bit)	28	84	49			
* BCD To 7-Segment	3	11	19			
Full BCD to 7-Segment	37	117	106			
Main Interface	84	285	187	85	1	12

Total Gates:

NOT → 84

AND → 285

OR → 187

XOR → 85

XNOR → 1

Mux → 12