

Handwritten Assignments 7

Question 1:

Design the circuit of Fig. 1 to establish a drain current of 0.1 mA and a drain voltage of +0.3 V. The MOSFET has $V_t = 0.5$ V, $\mu_n C_{ox} = 400 \mu\text{A/V}^2$, $L = 0.4 \mu\text{m}$, and $W = 5 \mu\text{m}$. Specify the required values for R_S and R_D .

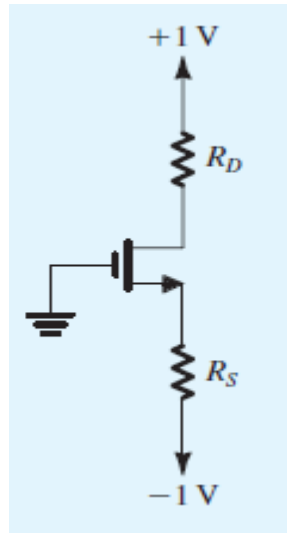


Fig. 1

Question 2:

The PMOS transistor in the circuit of Fig. 2 has $V_t = -0.5$ V, $\mu_p C_{ox} = 100 \mu\text{A/V}^2$, $L = 0.18 \mu\text{m}$, and $\lambda = 0$. Find the values required for W and R in order to establish a drain current of 180 μA and a voltage V_D of 1 V.

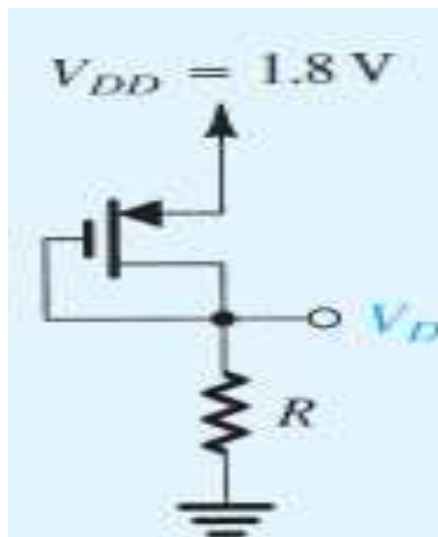


Fig. 2

Question 3:

For the circuits shown in Fig. 3, find the labeled node voltages. The NMOS transistors have $V_t = 0.9 \text{ V}$ and $K_n'(W/L) = 1.5 \text{ mA/V}^2$.

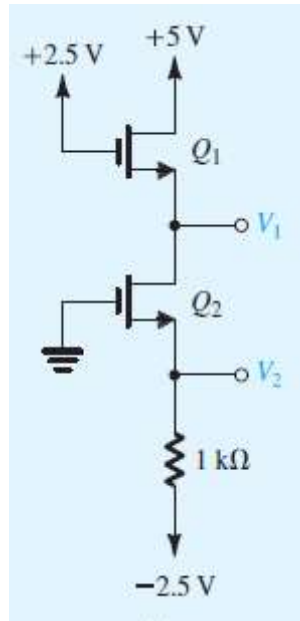


Fig. 3