

# Arithmetic Operations

## 1- Addition

base 2  
Binary

$$\begin{array}{r} 10110 \\ + 10011 \\ \hline 101001 \end{array}$$

$$\begin{array}{r} 9 \\ 4 \\ \hline 3 \end{array}$$

Base 5

$$\begin{array}{r} 342 \\ + 124 \\ \hline 1021 \end{array}$$

Hexa

$$\begin{array}{r} DA1 \\ + 6F \\ \hline E10 \end{array}$$

$$\begin{array}{r} 10 \\ 11 \\ \hline \end{array}$$

## 2- Subtraction

$$\begin{array}{r} 10110 \\ - 10011 \\ \hline \end{array}$$



$$\begin{array}{r} 10110 \\ 01100 \end{array}$$

$$\begin{array}{r} 10110 \\ + 01101 \\ \hline 10001 \end{array}$$

### 3- Multiplication

$$\begin{array}{r}
 10110 \\
 * \quad 101 \\
 \hline
 10110 \\
 00000 \\
 10110 \\
 \hline
 110110
 \end{array}$$

### 4- Division

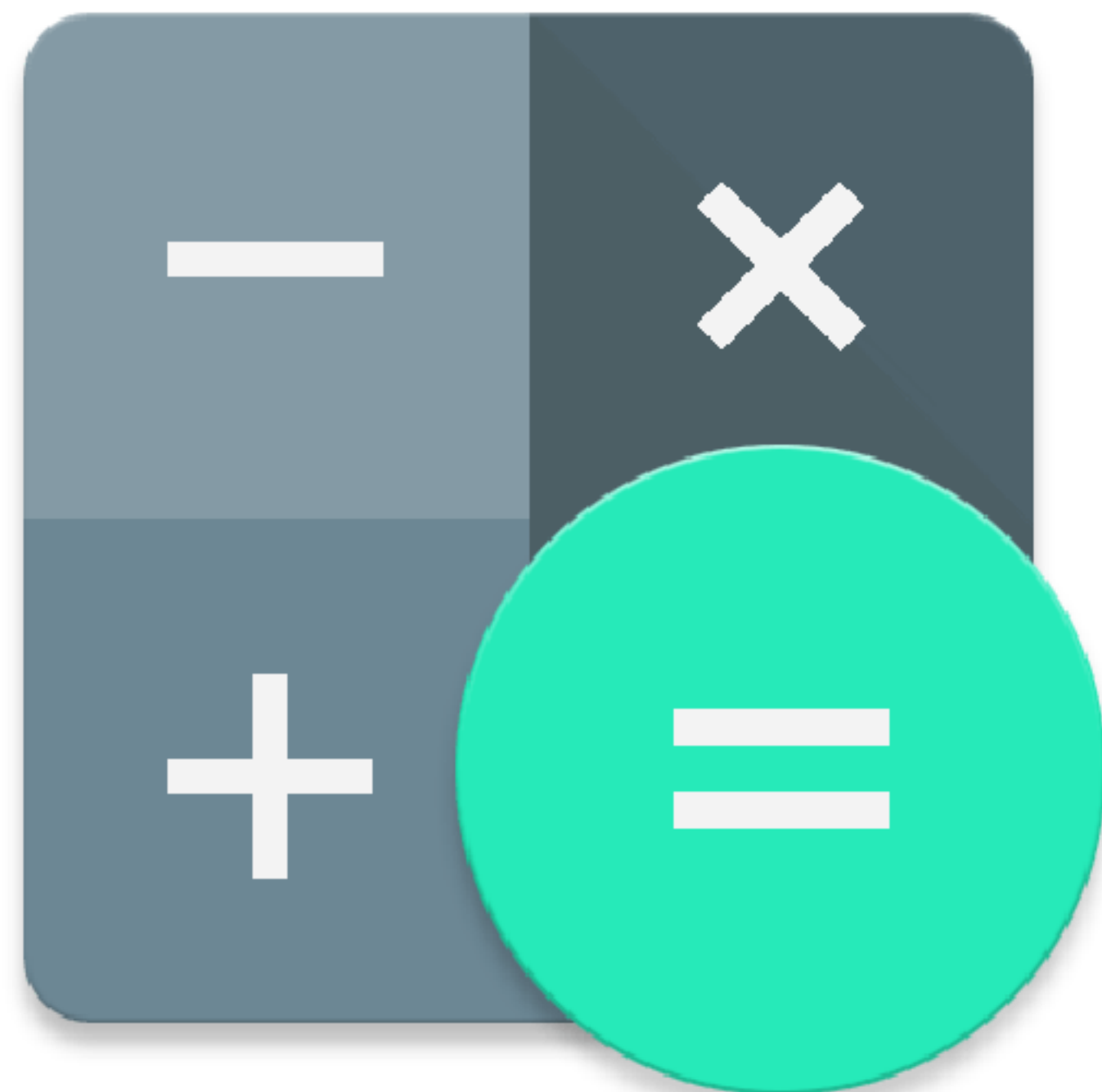
$$1100010 / 101$$

⑤

$$\begin{array}{r}
 101 \overline{) 1100010} \\
 \underline{101} \phantom{00} \\
 0010 \\
 \underline{0010} \\
 0000 \\
 \underline{0000} \\
 0001 \\
 \underline{0001} \\
 0000 \\
 \underline{0000} \\
 0000
 \end{array}$$

exercise

result is 11011 and reminder is 0



# Logic Gates

## Why Logic Gates ?

- We send information through computers using wires that represent 1s and 0s.
- Computers need a way to manipulate those 1s and 0s.
- Computers use logic gates to transform the 1s and 0s from input wires.

ex: Calculator



\*

AND

X	Y	Z
0	0	0
0	1	0
1	0	0
1	1	1

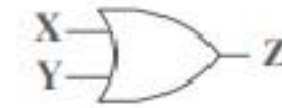
$$Z = X \cdot Y$$



OR

X	Y	Z
0	0	0
0	1	1
1	0	1
1	1	1

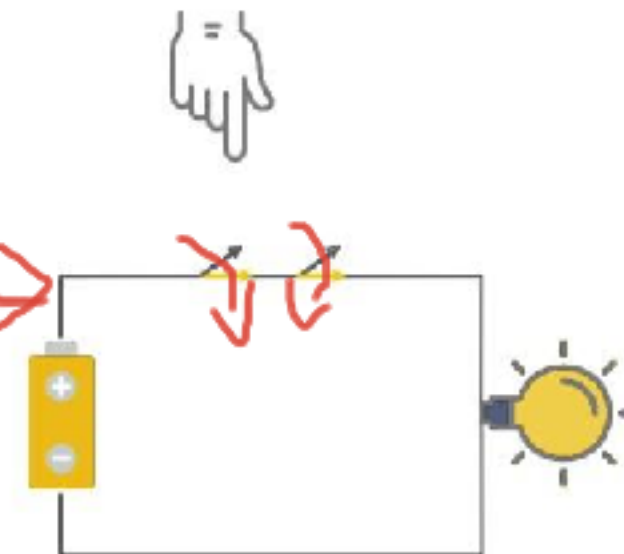
$$Z = X + Y$$



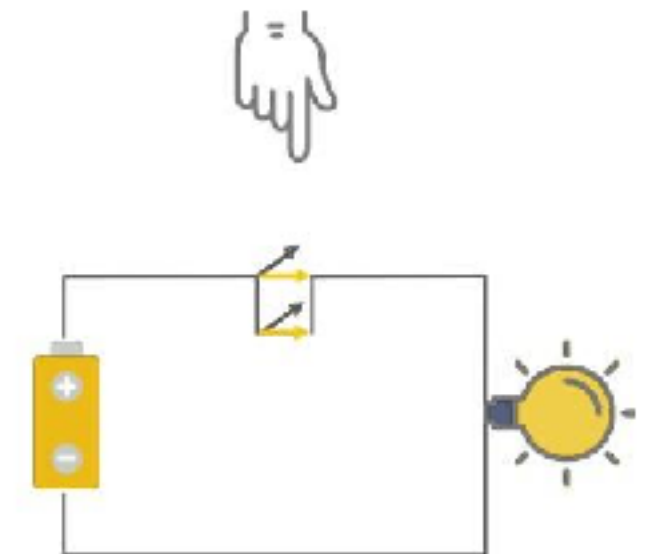
NOT

X	Z
1	0
0	1

$$X = \overline{Z}$$

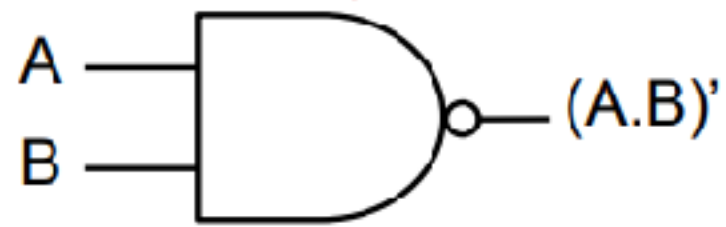


AND



OR

NAND



Truth Table

A	B	$(A.B)'$
0	0	1
0	1	1
1	0	1
1	1	0

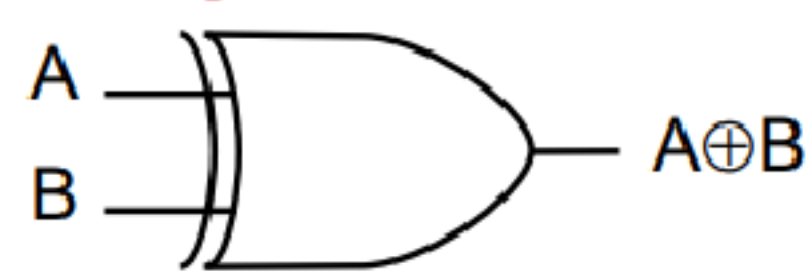
NOR



Truth Table

A	B	$(A+B)'$
0	0	1
0	1	0
1	0	0
1	1	0

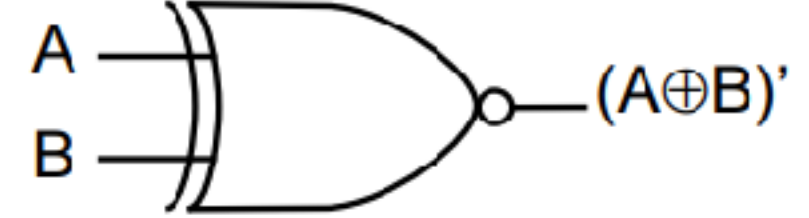
XOR



Truth Table

A	B	$(A \oplus B)$
0	0	0
0	1	1
1	0	1
1	1	0

XNOR



Truth Table

A	B	$(A \oplus B)'$
0	0	1
0	1	0
1	0	0
1	1	1

Subtraction

$$A - B \Rightarrow A + \bar{B} + 1$$

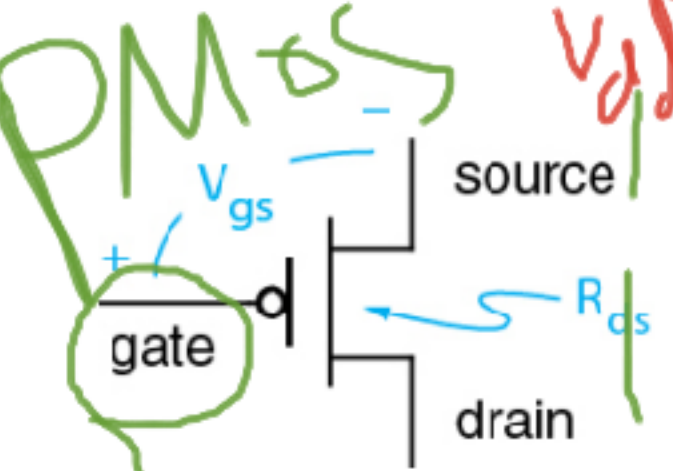
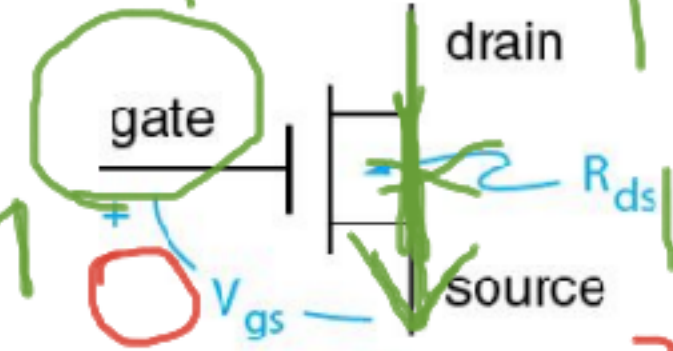


# Transistor Level

⇒ D -

NMOS

pull down



pull up

Vin

Q1

Q2

Vout

off

on

3.3V

NOT

(a)

V<sub>DD</sub> = +3.3 V

V<sub>IN</sub> = LOW

V<sub>OUT</sub> = HIGH

(b)

V<sub>DD</sub> = +3.3 V

V<sub>IN</sub> = HIGH

V<sub>OUT</sub> = LOW

(a)

V<sub>DD</sub> = +3.3 V

V<sub>IN</sub>

Q2 (p-channel)

Q1 (n-channel)

V<sub>OUT</sub>

Draw Transistor Level

PUN



A	B	Q1	Q2	Q3	Q4	Z
LOW	LOW	off	on	off	on	HIGH
LOW	HIGH	off	on	on	off	HIGH
HIGH	LOW	on	off	off	on	HIGH
HIGH	HIGH	on	off	on	off	LOW



## Draw Transistor Level

(b)

A	B	Q1	Q2	Q3	Q4	Z
LOW	LOW	off	on	off	on	HIGH
LOW	HIGH	off	on	on	off	LOW
HIGH	LOW	on	off	off	on	LOW
HIGH	HIGH	on	off	on	off	LOW

(c)



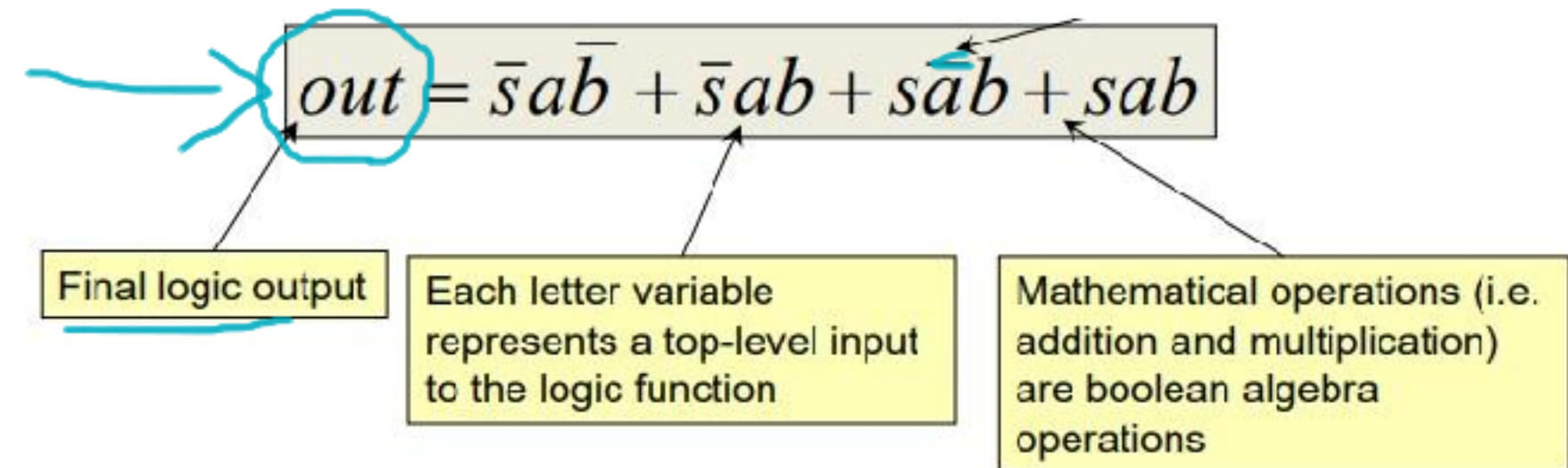


# Boolean Algebra

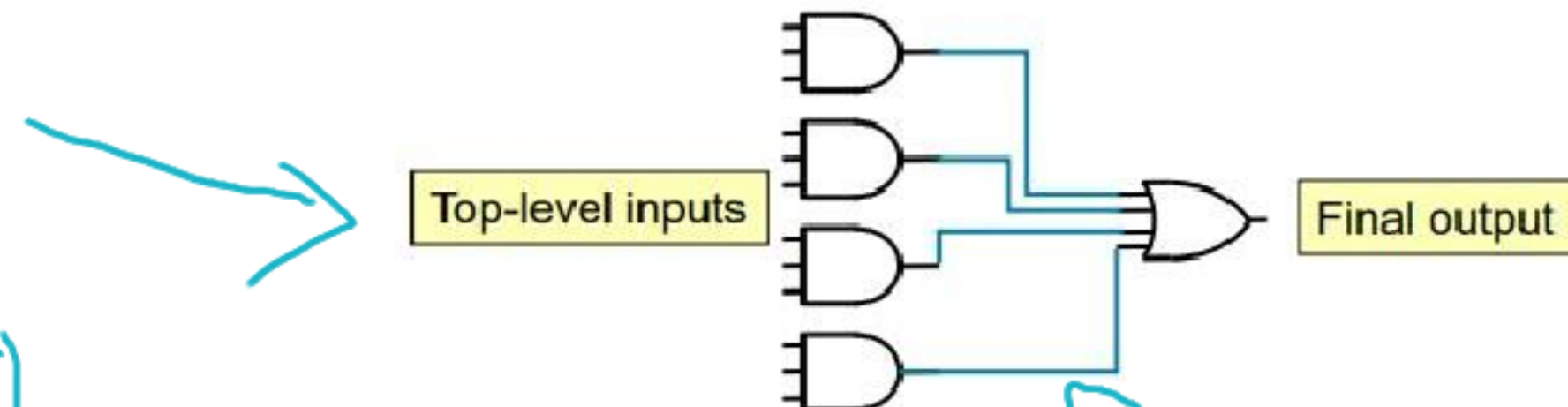
the mathematics associated with binary numbers

- 3 different ways to represent logic functions:

1. Equation: a mathematical representation of a logic function



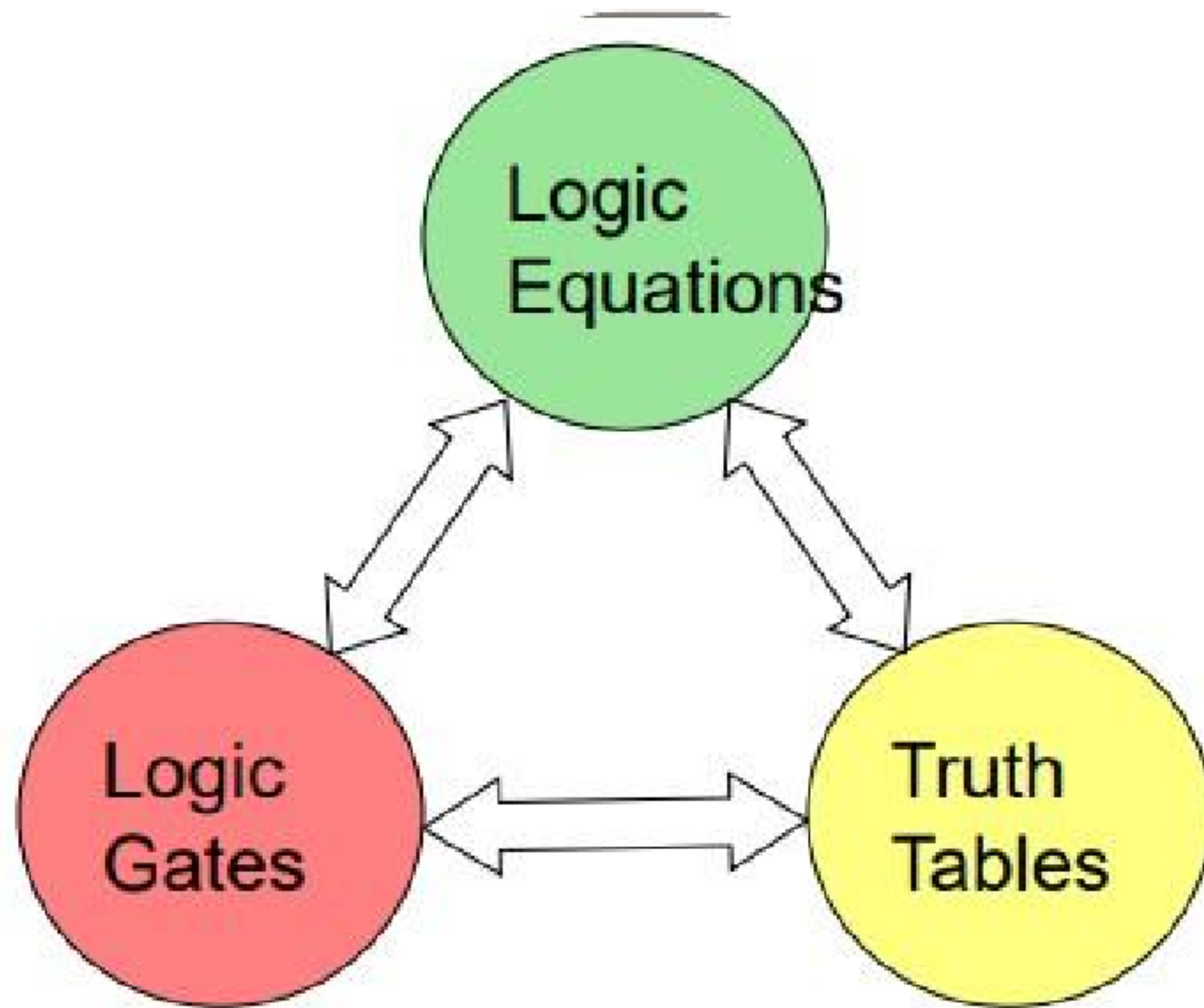
2. Gates: a visual block representation of the function



3. Truth Table: indicates what the output will be for every possible input combination

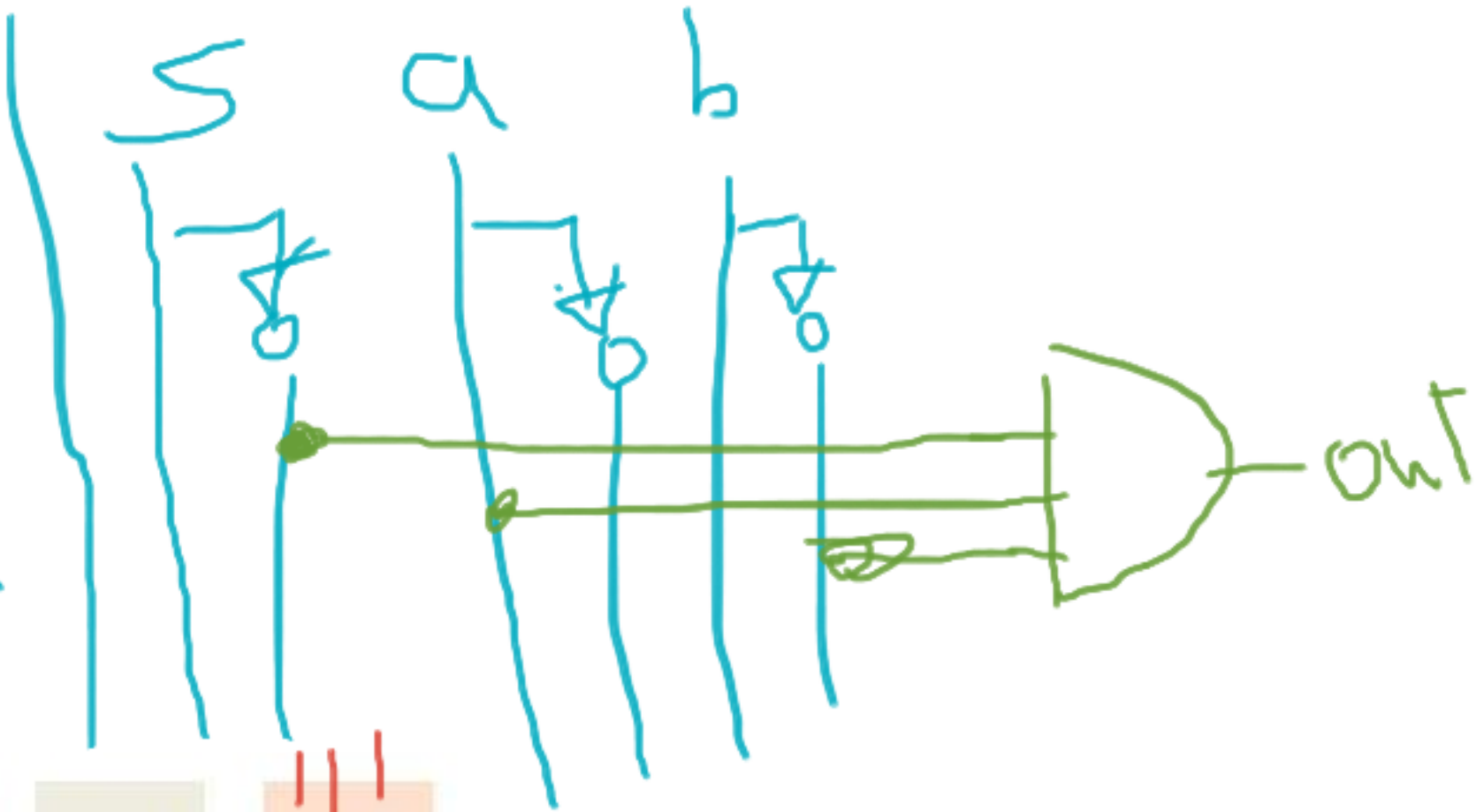
The diagram shows a truth table with 3 input columns (A, B, C) and 1 output column (Z). The table has 8 rows, representing all possible input combinations. A blue arrow points to the input columns with a label "If there are n inputs (left-hand columns) there will be 2^n entries (rows) in the table EX: 3 inputs require 2^3 = 8 rows". A blue arrow points to the output column with a label "There will always be at least one output (right-hand columns)". A blue arrow points to a row with a label "For each input combination (row) outputs will be either 0 or 1".

A	B	C	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



## 1- Equations to Gates

$$y = \bar{s} \cdot a \cdot \bar{b}$$



## 2- Equations to Truth Tables

$$out = \bar{s}a\bar{b} + \bar{s}ab + s\bar{a}b + \underline{sab}$$

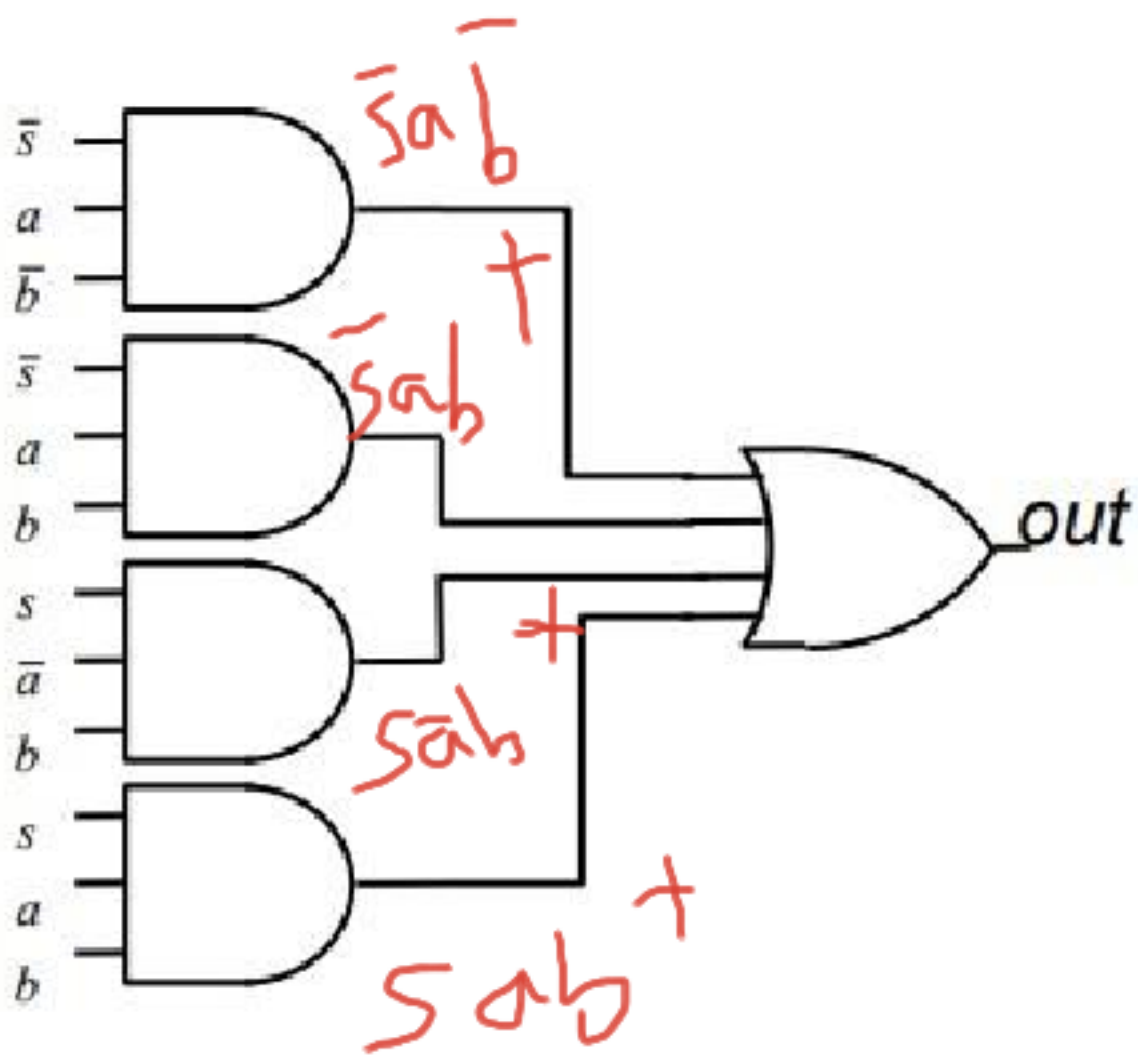
s	a	b	out
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

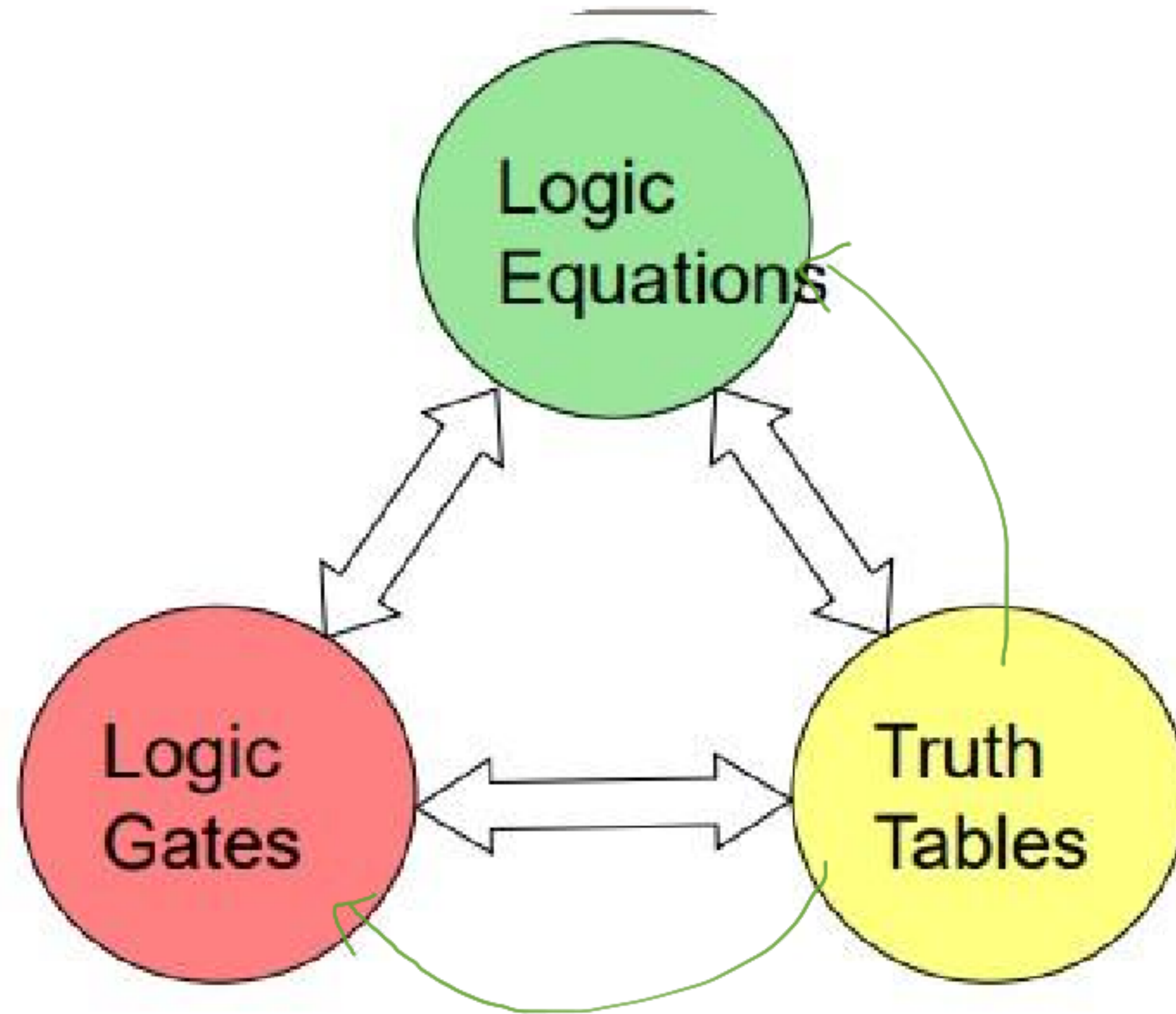


3- Gates to Equations

4- Gates to Truth-Table

	$a$	$b$	out
0	0	0	1
0	1	0	0
0	1	1	0
0	0	1	0
1	0	0	0
1	1	0	0
1	1	1	0
1	0	1	0







# 5- Truth Table to Equations

=> SOP Vs POS

SOP: 0 → False  
1 → True

$$(\bar{S} \cdot \bar{A} \cdot \bar{B}) + (\bar{S} \cdot A \cdot B) + (S \cdot \bar{A} \cdot B) + (S \cdot A \cdot B)$$

POS: 1 → False  
0 → True

$$(S + \bar{A} + \bar{B}) \cdot (S + \bar{B} + \bar{A}) \cdot (\bar{S} + A + \bar{B}) \cdot (\bar{S} + \bar{A} + \bar{B})$$

S	A	B	OUT
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Give a truth table and a standard sum of products expression that describes:

•  $F = A \text{ xor } B \text{ xor } C$

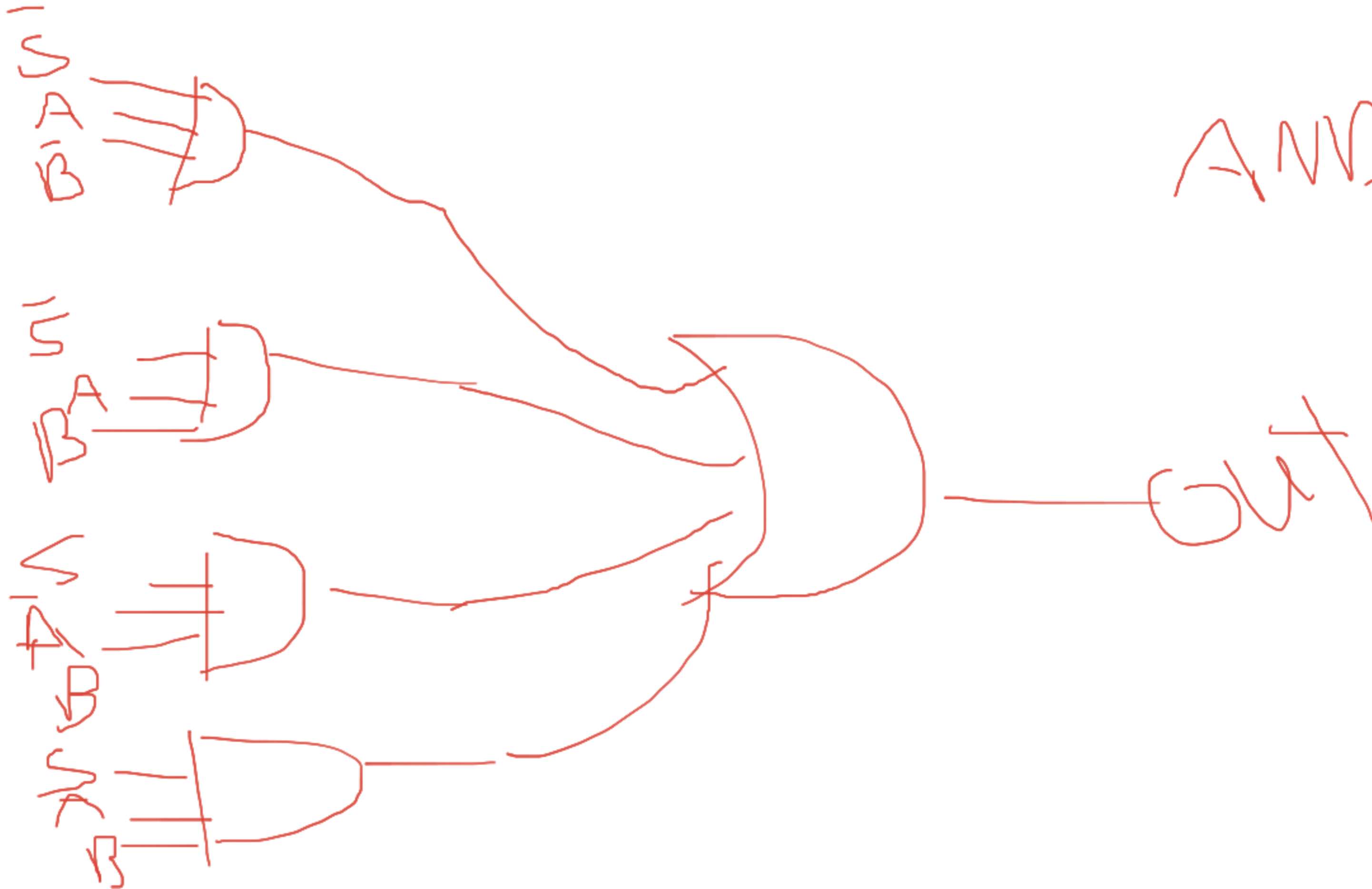
508 0 → F  
1 → T

if odd → out = 1  
if even → out = 0

$$\begin{aligned} & (\bar{A}\bar{B}C) + (\bar{A}B\bar{C}) \\ & + (A\bar{B}\bar{C}) + (AB C) \end{aligned}$$

A	B	C	F
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

## 6- Truth Tables to Gates



AND

S	A	B	OUT
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

OR

Equation to Gates:  $A'B'C' + A B'C' + ABC$